

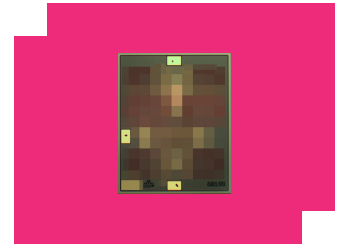
MM1-0312SCH-2

GaAs MMIC Double Balanced Mixer

DEVICE OVERVIEW

General Description

The MM1-0312S is a passive double balanced MMIC mixer. It features excellent conversion loss, superior isolations and spurious performance across a broad bandwidth, in a highly miniaturized form factor. Accurate, nonlinear simulation models are available for Microwave Office® through the Marki Microwave PDK. The MM1-0312S is available as a wire bondable chip or an SMA connectorized package. The MM1-0312S is a superior alternative to Marki Microwave carrier and packaged M1 and M3 mixers.



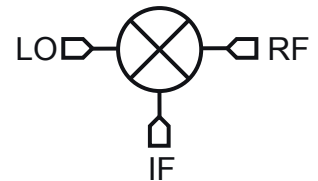
Features

- Compact Chip Style Package (0.054" x 0.046"x0.004")
- CAD Optimized for Superior Isolation and Spurious Response
- Broadband Performance
- Excellent Unit-to-Unit Repeatability
- Fully nonlinear software models available with Marki PDK for Microwave Office®
- RoHS Compliant

Applications

N/A

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Connectors	Green Status	Product Lifecycle	Export Classification
<u>MM1-0312SS</u>	GaAs MMIC Double Balanced Mixer	S	<u>Standard</u>	REACH RoHS	Released	EAR99
MM1-0312SCH-2	GaAs MMIC Double Balanced Mixer	CH	-	REACH RoHS	Released	EAR99

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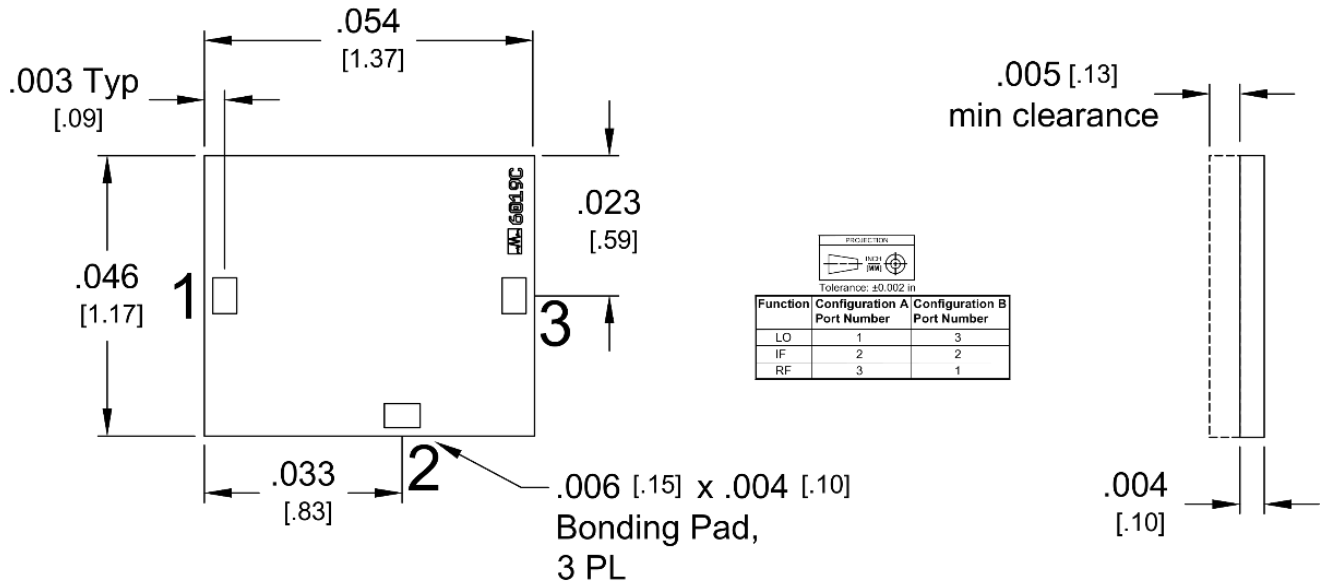
- **Device Overview**
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 - Features
 - Applications
 - Functional Block Diagram
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 - Port Functions
- **Revision History**
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Revision History

Revision Code	Revision Date	Comment
A	2017-08-01	Die shrunk from 1.48 x 1.18 mm ² to 1.37 x 1.17 mm ² . CH package tolerance added.
B	2020-01-01	Updated CH package Outline Dimensions


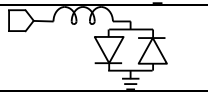
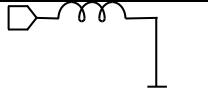
Port Configuration and Functions

Port Diagram


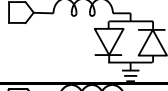



Port Functions

Configuration A

Port	Function	Description	Equivalent Circuit for Package
Port 1	LO	Port 1 is DC short and AC matched to 50 Ω from 3 to 12 GHz. Blocking capacitor is optional.	
Port 2	IF	Port 2 is DC coupled to the diodes. Blocking capacitor is optional.	
Port 3	RF	Port 3 is DC short and AC matched to 50 Ω from 3 to 12 GHz. Blocking capacitor is optional.	

Configuration B

Port	Function	Description	Equivalent Circuit for Package
Port 1	RF	Port 1 is DC short and AC matched to 50 Ω from 3 to 12 GHz. Blocking capacitor is optional.	
Port 2	IF	Port 2 is DC coupled to the diodes. Blocking capacitor is optional.	
Port 3	LO	Port 3 is DC short and AC matched to 50 Ω from 3 to 12 GHz. Blocking capacitor is optional.	

Specifications

Absolute Maximum Ratings

Parameter	Maximum Rating	Unit
Maximum Operating Temperature	100	°C
Maximum Storage Temperature	125	°C
Minimum Operating Temperature	-55	°C
Minimum Storage Temperature	-65	°C
Port 2 DC Current	50	mA
RF Power Handling (RF+LO), 100°C	20	dBm
RF Power Handling (RF+LO), 25°C	25	dBm

Package Information

Parameter	Details	Rating
Dimensions	-	1.37 x 1.17 mm

Recommended Operating Conditions

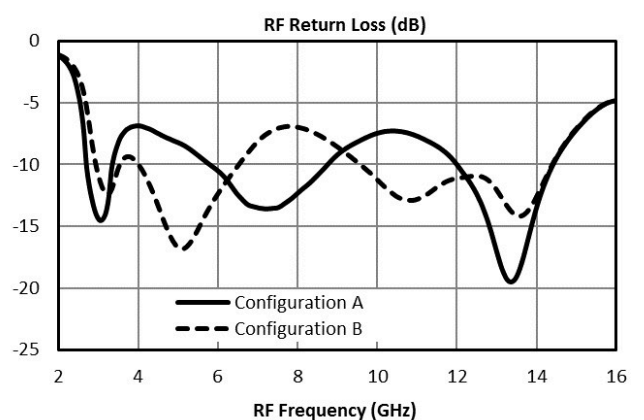
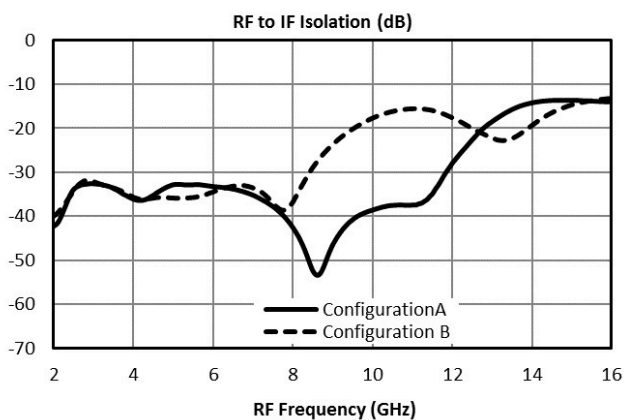
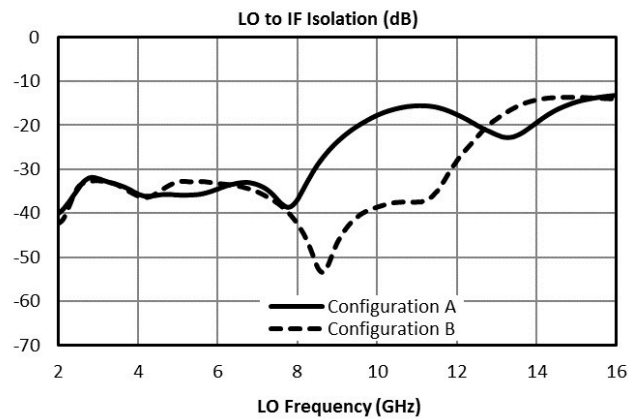
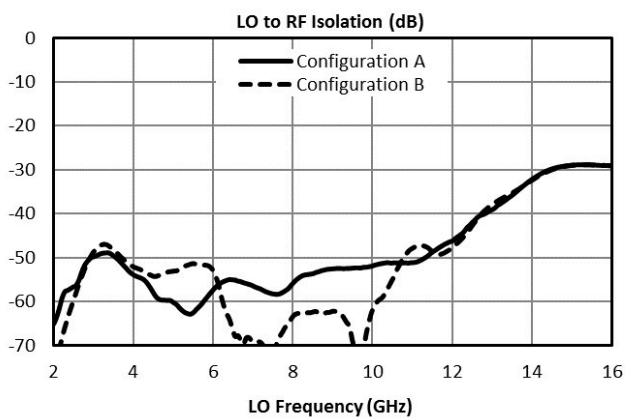
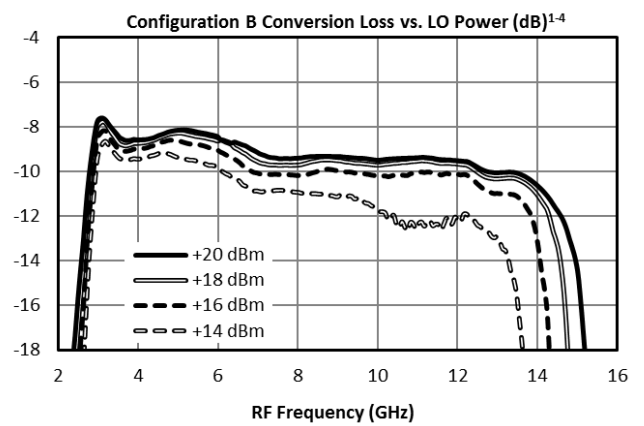
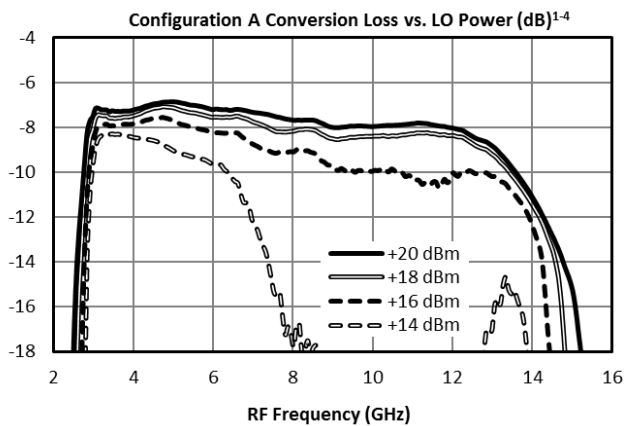
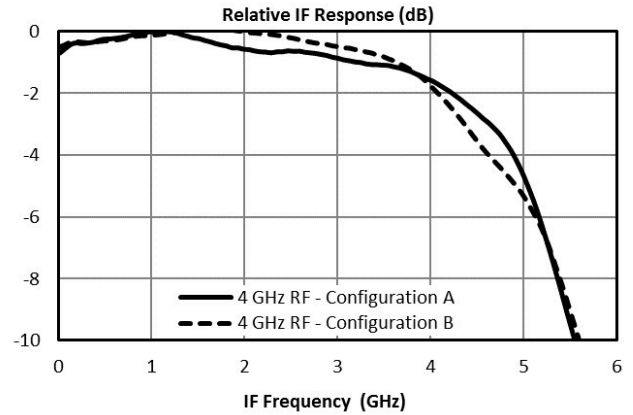
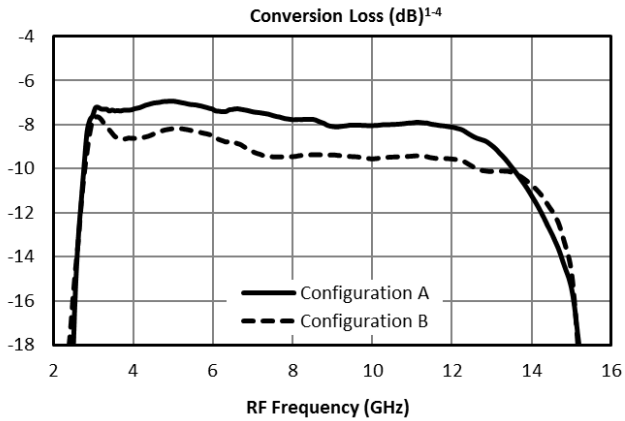
Parameter	Min	Nominal	Max	Unit
LO Input Power	17	-	23	-

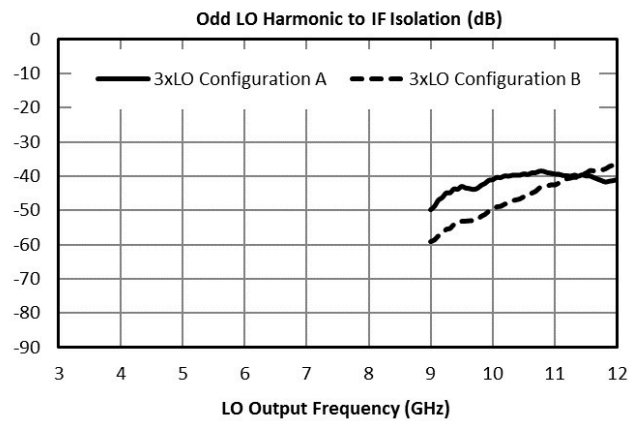
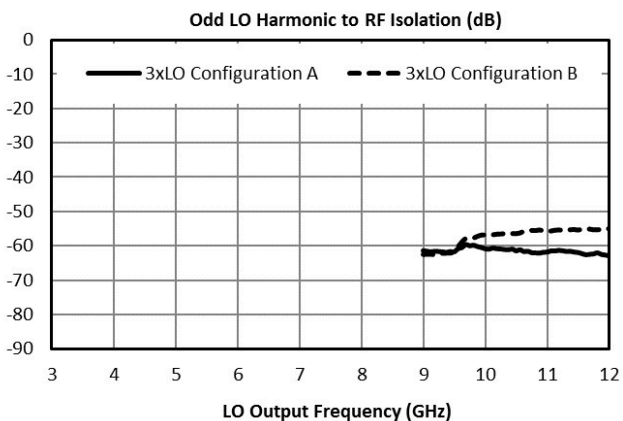
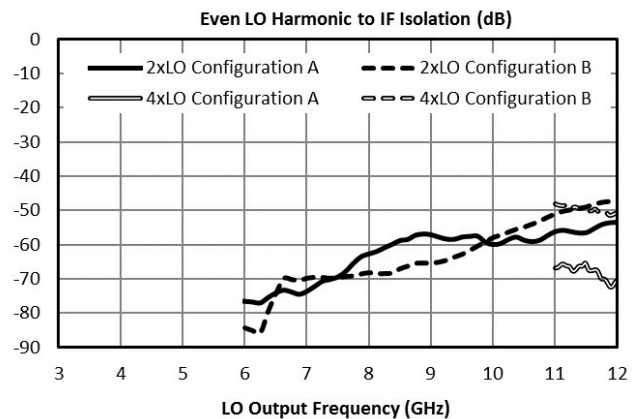
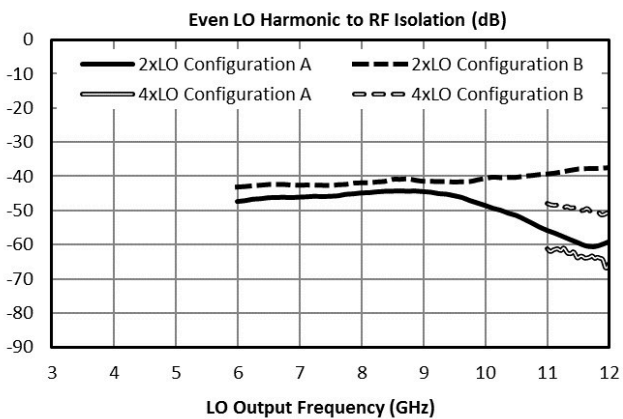
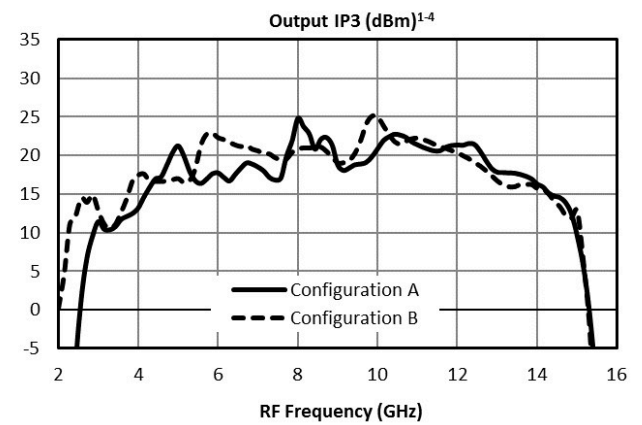
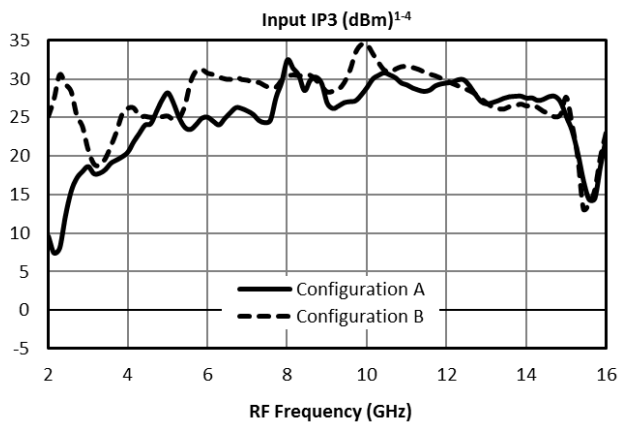
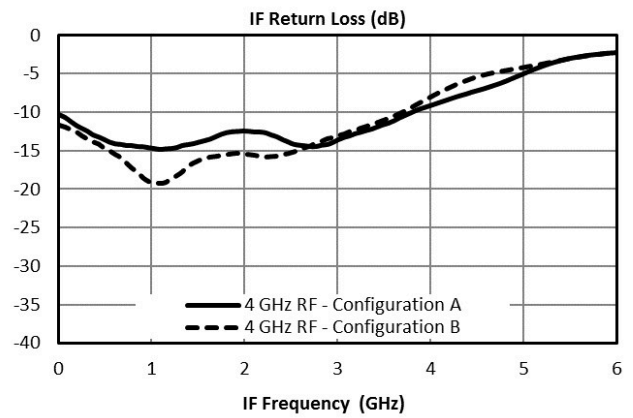
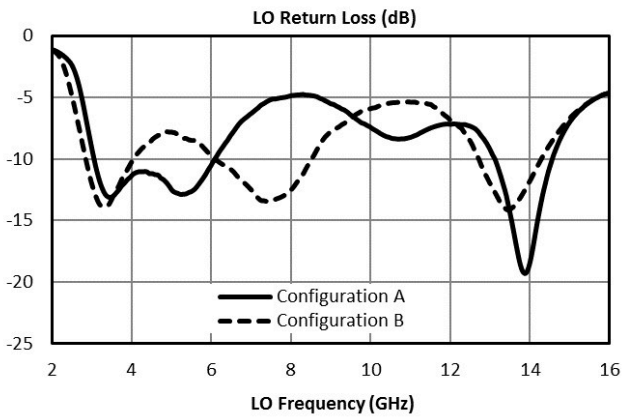
Electrical Specifications

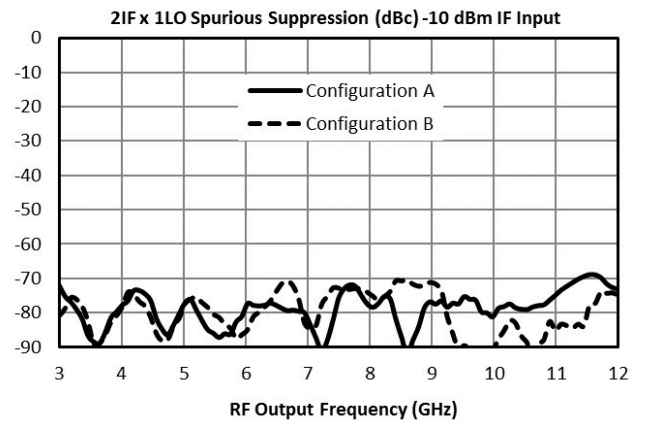
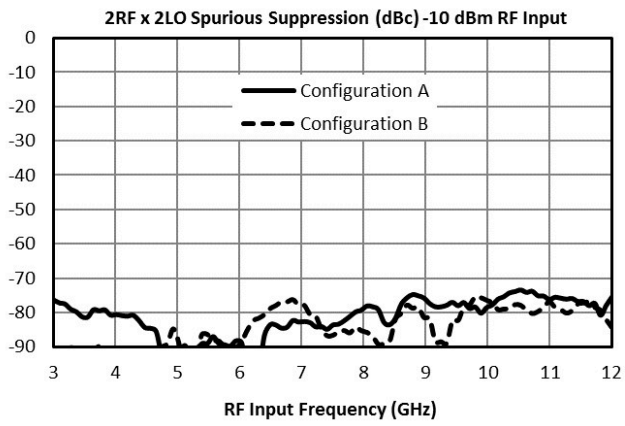
Specifications guaranteed from -55 to +100°C, measured in a 50Ω system. All bare die are 100% DC tested and 100% visually inspected. RF testing is performed on a sample basis to verify conformance to datasheet guaranteed specifications. Consult factory for more information.

Parameter	Port Configuration	Test Conditions	Min	Typ	Max	Unit
Conversion Loss	A	LO/RF=3-12 GHz IF=DC-4.5 GHz LO Drive Level=20dBm	-	7.5	-	dB
Input 1dB Compression	A	LO/RF=3-12 GHz IF=DC-4.5 GHz LO Drive Level=18-23dBm	-	14	-	dBm
Input IP3	A	LO/RF=3-12 GHz IF=DC-4.5 GHz LO Drive Level=18-23dBm	-	24	-	dBm
Conversion Loss	B	LO/RF=3-12 GHz IF=DC-4.5 GHz LO Drive Level=20dBm	-	9	-	dB
Input 1dB Compression	B	LO/RF=3-12 GHz IF=DC-4.5 GHz LO Drive Level=18-23dBm	-	14	-	dBm
Input IP3	B	LO/RF=3-12 GHz IF=DC-4.5 GHz LO Drive Level=18-23dBm	-	26	-	dBm
IF Frequency Range	-	-	0	-	4.5	GHz
Isolation, LO to IF	-	LO/RF=3-12 GHz IF=DC-4.5 GHz LO Drive Level=18-23dBm	-	31	-	dB
Isolation, LO to RF	-	LO/RF=3-12 GHz IF=DC-4.5 GHz LO Drive Level=18-23dBm	-	47	-	dB
Isolation, RF to IF	-	LO/RF=3-12 GHz IF=DC-4.5 GHz LO Drive Level=18-23dBm	-	36	-	dB
LO Frequency Range	-	-	3	-	12	GHz
RF Frequency Range	-	-	3	-	12	GHz

Typical Performance

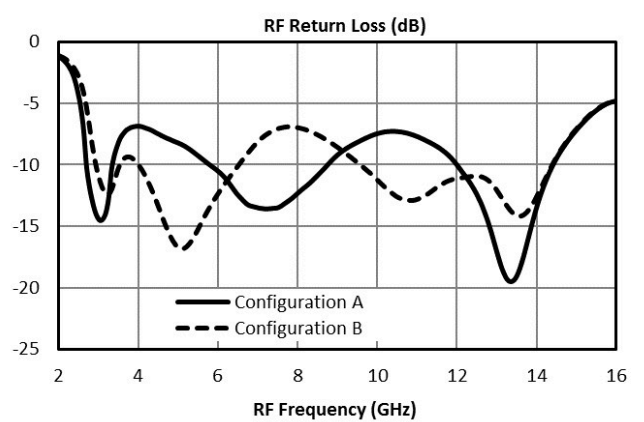
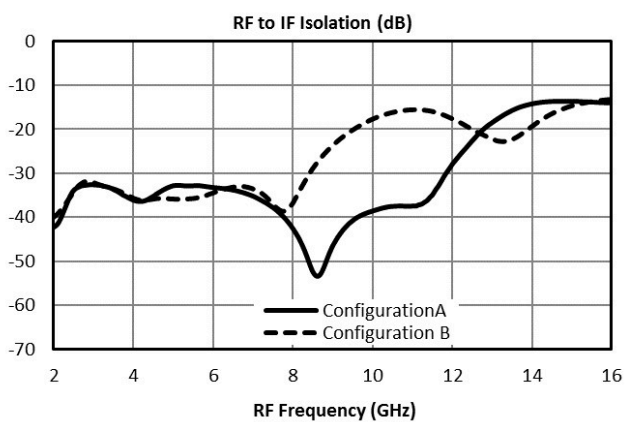
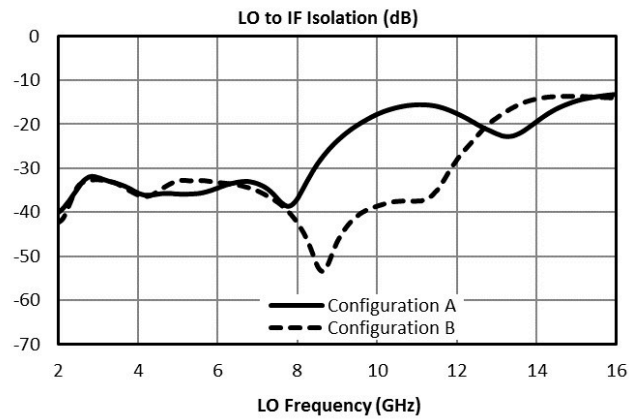
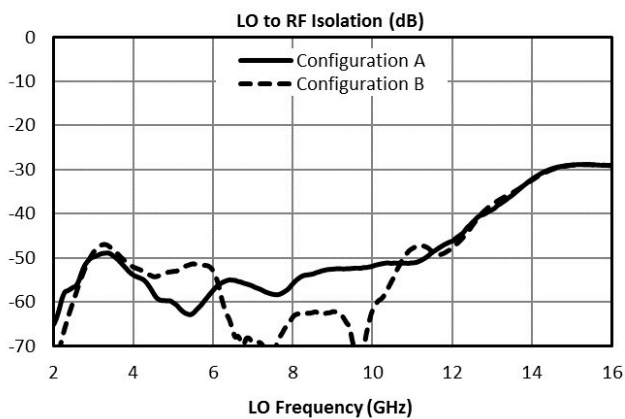
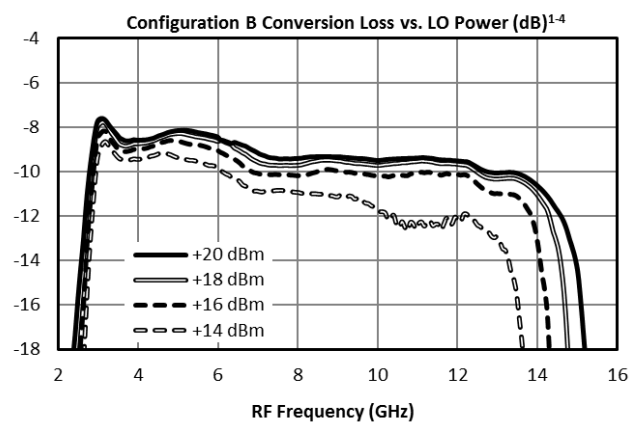
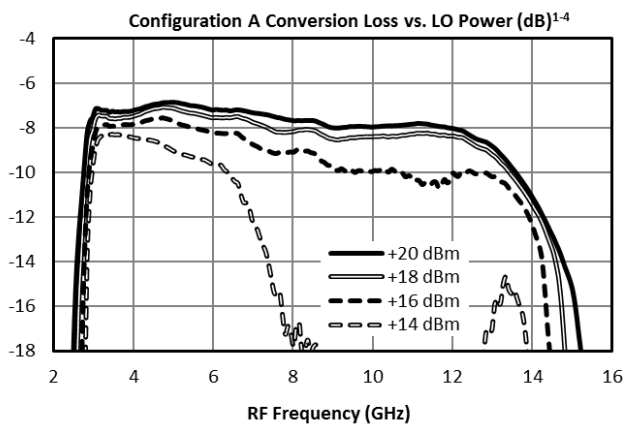
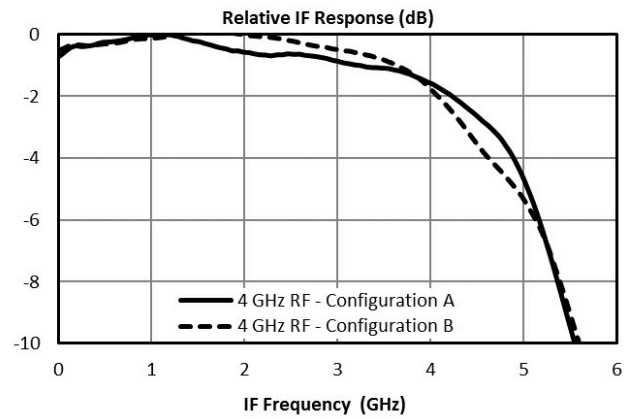
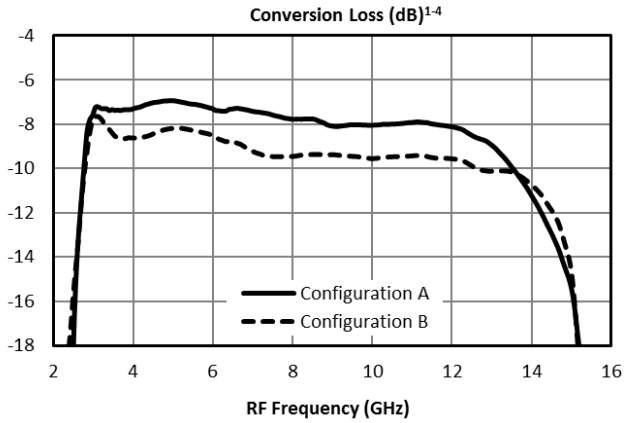


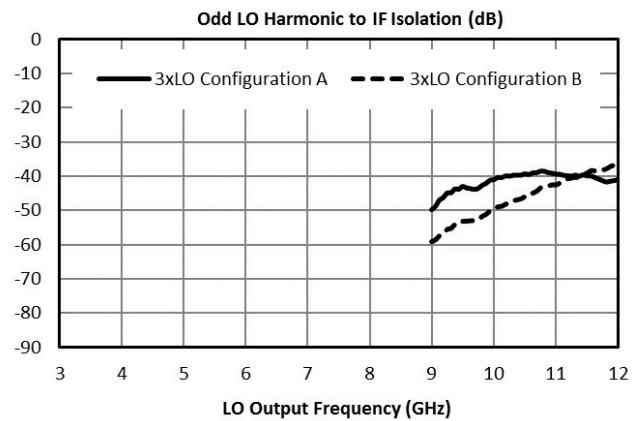
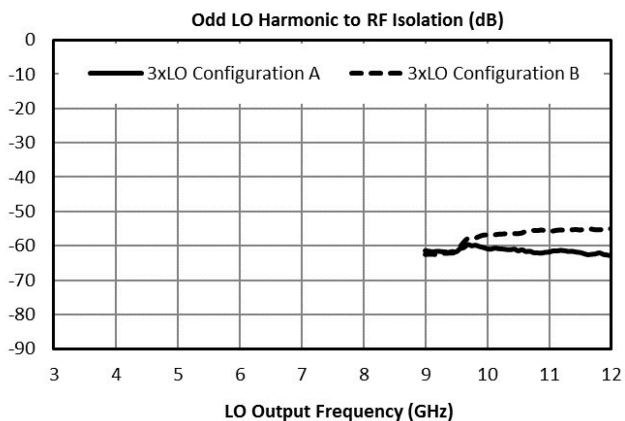
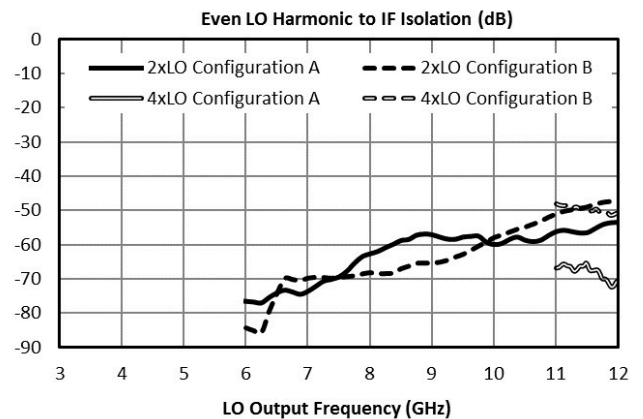
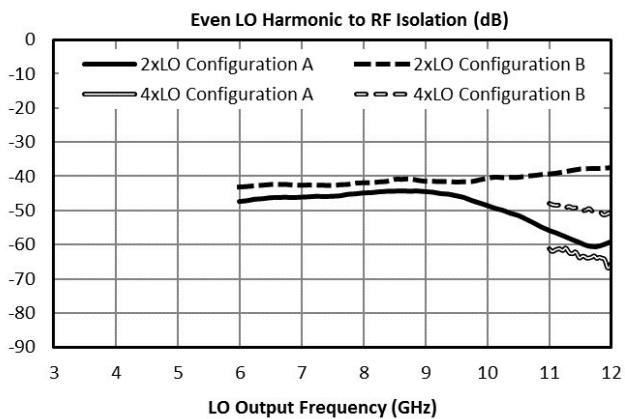
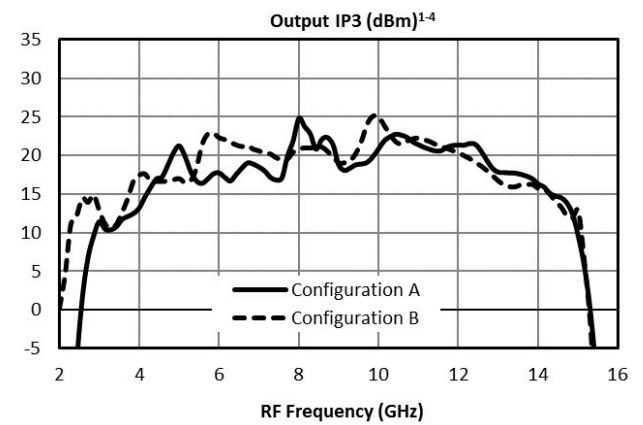
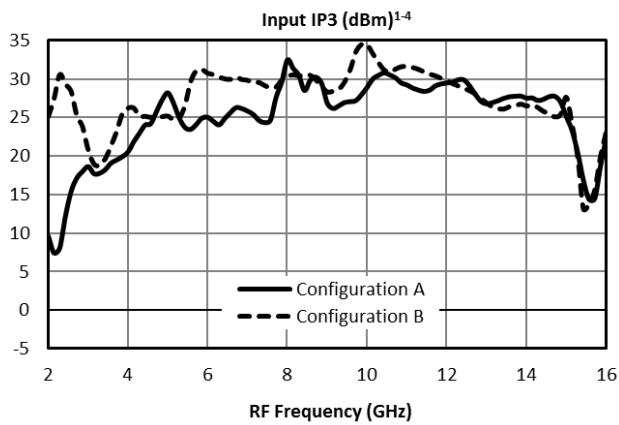
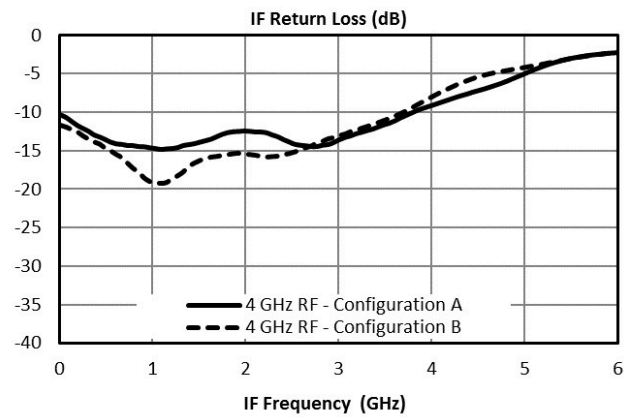
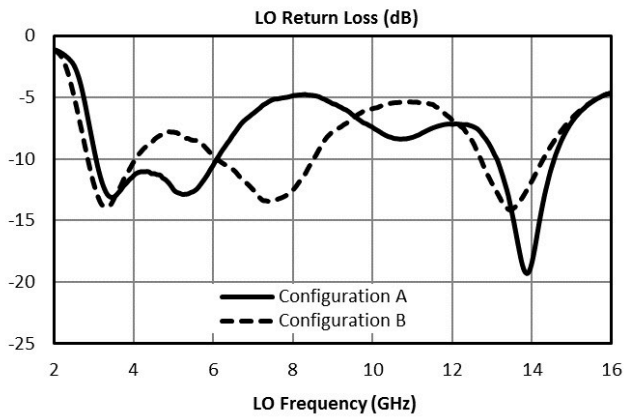


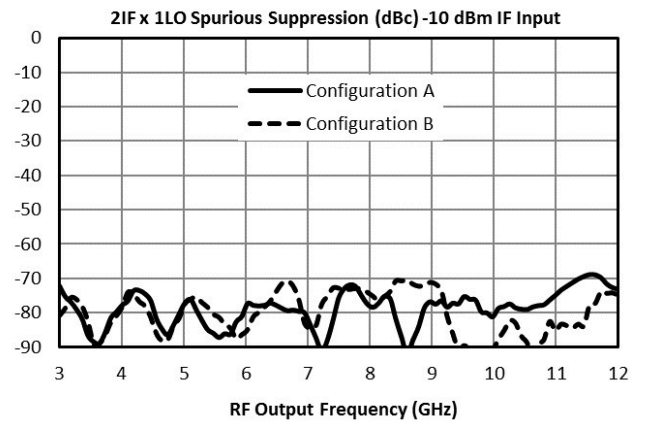
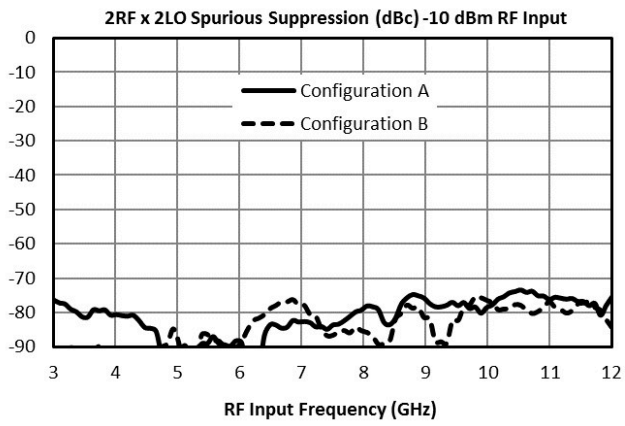


MM1-0312SS - Typical Performance

Performance plots for the connectorized module are shown for measurements where directly probed measurements of the die are unavailable. Note that the following measurements include losses from connectors and microstrip traces.







Spur Table

Downconversion Spurious Suppression

Spurious data is taken by selecting RF and LO frequencies (+mLO+nRF) within the 3 to 12 GHz RF/LO bands, which create a 91 MHz IF spurious output. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by (n-1), where “n” is the RF spur order. For example, the 2RFx2LO spur is 82 dBc for the A configuration for a -10 dBm input, so a -20 dBm RF input creates a spur that is (2-1) x (-10 dB) dB lower, or 92 dBc.

Typical Downconversion Spurious Suppression (dBc): A Configuration (B Configuration), Sine Wave LO ⁵

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xRF	29 (20)	Reference	33 (47)	17 (15)	35 (45)	N/A
2xRF	72 (75)	67 (60)	82 (85)	76 (64)	82 (91)	75 (74)
3xRF	79 (75)	72 (76)	96 (98)	80 (86)	88 (99)	74 (80)
4xRF	N/A	120 (118)	123 (121)	117 (118)	121 (121)	124 (120)
5xRF	N/A	135 (134)	136 (136)	131 (132)	135 (136)	128 (128)

Specifications are subject to change without notice. Contact Marki Microwave for the most recent specifications and data sheets.

Upconversion Spurious Suppression

Spurious data is taken by mixing a 91 MHz IF with LO frequencies (+mLO+nIF), which creates an RF within the 3 to 12 GHz RF band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by (n-1), where “n” is the IF spur order. For example, the 2IFx1LO spur is typically 79 dBc for the A configuration for a -10 dBm input, so a -20 dBm IF input creates a spur that is (2-1) x (-10 dB) dB lower, or 89 dBc.

Typical Upconversion Spurious Suppression (dBc): A Configuration (B Configuration), Sine Wave LO ⁵

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xIF	26 (26)	Reference	36 (42)	13 (12)	35 (46)	21 (23)
2xIF	68 (61)	79 (80)	73 (63)	76 (81)	76 (67)	79 (85)
3xIF	86 (93)	80 (81)	89 (98)	79 (79)	89 (97)	78 (81)
4xIF	114 (102)	122 (123)	119 (114)	127 (126)	125 (115)	124 (124)
5xIF	120 (134)	131 (133)	137 (136)	132 (136)	137 (136)	132 (130)

Specifications are subject to change without notice. Contact Marki Microwave for the most recent specifications and data sheets.

Die Mounting Recommendations

Mounting and Bonding Recommendations

Marki MMICs should be attached directly to a ground plane with conductive epoxy. The ground plane electrical impedance should be as low as practically possible. This will prevent resonances and permit the best possible electrical performance. Datasheet performance is only guaranteed in an environment with a low electrical impedance ground.

Mounting - To epoxy the chip, apply a minimum amount of conductive epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip. Cure epoxy according to manufacturer instructions.

Wire Bonding - Ball or wedge bond with 0.025 mm (1 mil) diameter pure gold wire. Thermosonic wirebonding with a nominal stage temperature of 150 °C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Use the minimum level of ultrasonic energy to achieve reliable wirebonds. Wirebonds should be started on the chip and terminated on the package or substrate. All bonds should be as short as possible <0.31 mm (12 mils).

Circuit Considerations – 50 Ω transmission lines should be used for all high frequency connections in and out of the chip. Wirebonds should be kept as short as possible, with multiple wirebonds recommended for higher frequency connections to reduce parasitic inductance. In circumstances where the chip more than .001” thinner than the substrate, a heat spreading spacer tab is optional to further reduce bondwire length and parasitic inductance.

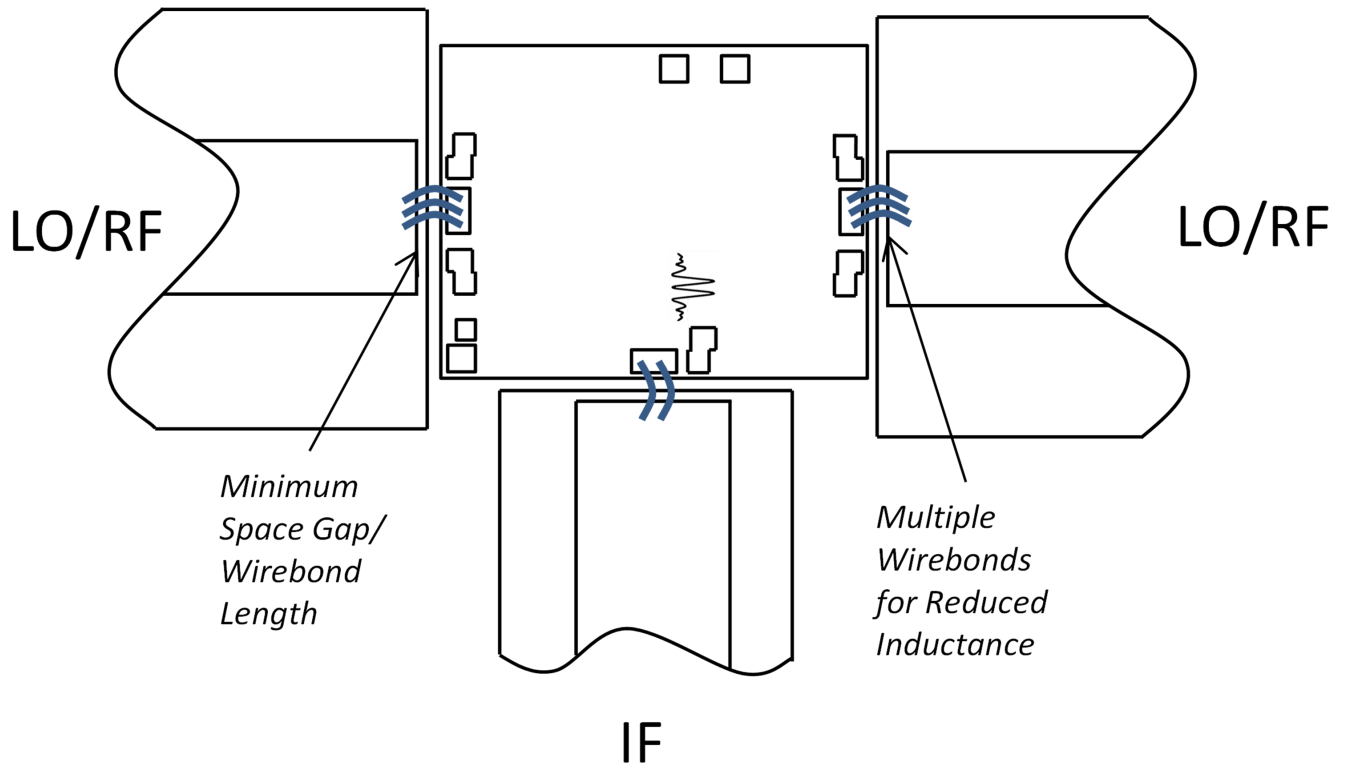
Handling Precautions

General Handling: Chips should be handled with care using tweezers or a vacuum collet. Users should take precautions to protect chips from direct human contact that can deposit contaminants, like perspiration and skin oils on any of the chip's surfaces.

Static Sensitivity: GaAs MMIC devices are subject to static discharge, and should be handled, assembled, tested, and transported only in static protected environments.

Cleaning and Storage: Do not attempt to clean the chip with a liquid cleaning system or expose the bare chips to liquid. Once the ESD sensitive bags the chips are stored in are opened, chips should be stored in a dry nitrogen atmosphere.

Bonding Diagram



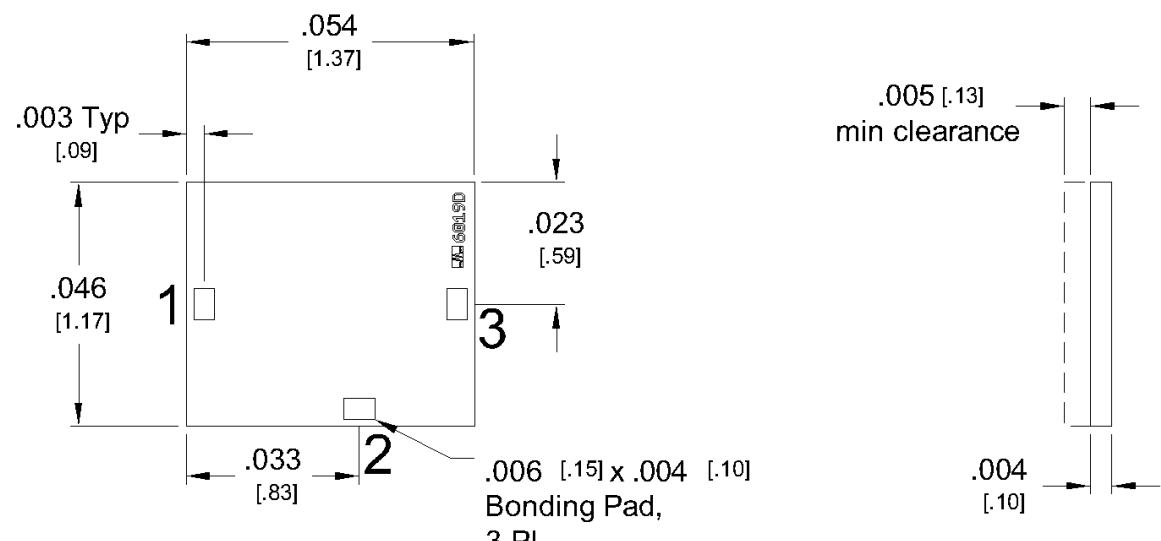
Mechanical Data

Outline Drawing

Download : [Outline 2D Drawing](#)


Function	Configuration A Port Number	Configuration B Port Number
LO	1	3
IF	2	2
RF	3	1

PROJECTION		REVISIONS		
REV.	DESCRIPTION	DATE	APPROVALS	
C	ECN 24096 remove gel-pak info.	10/16/24	AT	



Notes:

- CH substrate is .004 Thick GaAs.
- I/O traces and ground plane finish is 2 microns Au.
- Tolerance for X, Y dimensions is ± 0.002 in.
Tolerance for Z dimension is ± 0.0005 in.
Tolerance for pad location is ± 0.0001 in.

<small>JUL 23 09 16 W 82 98 20 123 S 10 10 010 1/12 1 10 10 10 T 10 10 10 10 10 10 10 10</small>		NOTES: DRAWN BY: STR DATE: 7/7/08		 www.markimicrowave.com	
MATERIAL: 210-06018-1RM				Outline MMIC Mixer	
FINISH:				SIZE: A CAGE CODE: 0UC32	DWG. NO.: MM1-0312SCH-2
<small>Proprietary: This document was originated by and is the property of Marki Microwave. Unauthorized disclosure is prohibited.</small>		DO NOT SCALE DRAWING		SCALE: 40X	SHEET: 1 OF 1

Notes

1. Mixer Conversion Loss Plot IF frequency is 100 MHz.
2. Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.
3. Conversion Loss typically degrades less than 0.5 dB at +100°C and improves less than 0.5 dB at -55°C.
4. Unless otherwise specified, data is taken with +20 dBm lowside LO drive.
5. Specifications are subject to change without notice. Contact Marki Microwave for the most recent specifications and data sheets.
6. Catalog mixer circuits are continually improved. Configuration control requires custom mixer model numbers and specifications.

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

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