

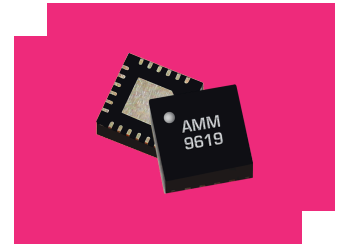
AMM-9619PSM

2 - 26 GHz GaAs Surface Mount LO Driver Amplifier

DEVICE OVERVIEW

General Description

The AMM-9619PSM is a surface-mount amplifier suitable for use as a single tone driver or general-purpose gain block. It can drive an L or H diode mixer from 2 to 26 GHz. This amplifier also has exceptionally low input and output reflections, and a low 1.6 dB typical noise figure from 8 to 18 GHz. Additionally, this amplifier exhibits a positive gain slope to equalize frequency dependent losses. The AMM-9619PSM is packaged in a compact 4 mm QFN for surface mount integration onto printed circuit boards.



[Download s-parameters here](#)

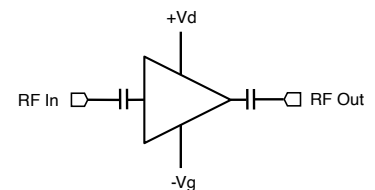
Features

- +14 dB Small Signal Gain with Positive Slope
- +1.6 dB NF from 8 to 18 GHz
- Excellent return losses
- Compact 4mm QFN package

Applications

- Mobile test and measurement equipment
- Radar and satellite communications
- Driver Amplifier for H and S - Diode Mixers
- Electronic Warfare

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
AMM-9619PSM	2 - 26 GHz GaAs Surface Mount LO Driver Amplifier	QFN	REACH RoHS	Released	EAR99
EVB-AMM-9619P	Evaluation Board, 2-26GHz Surface Mount LO Driver Amplifier	EVB	REACH RoHS	Released	EAR99

AMM-9619PSM

2 - 26 GHz GaAs Surface Mount LO Driver Amplifier

Table Of Contents

- **Device Overview**
 - General Description
 - Features
 - Applications
 - Functional Block Diagram
- **Port Configuration and Functions**
 - Port Diagram
 - Port Functions
- **Revision History**
- **Specifications**
 - Absolute Maximum Ratings
 - Package Information
 - Recommended Operating Conditions
 - Sequencing Requirements
 - Electrical Specifications
 - Typical Performance Plots
- **Operation**
 - Application Circuit
 - Application Circuit Description
- **Mechanical Data**
 - Outline Drawing
- **Footprint Image**
- **Evaluation Board**
 - Evaluation Board Outline Drawing

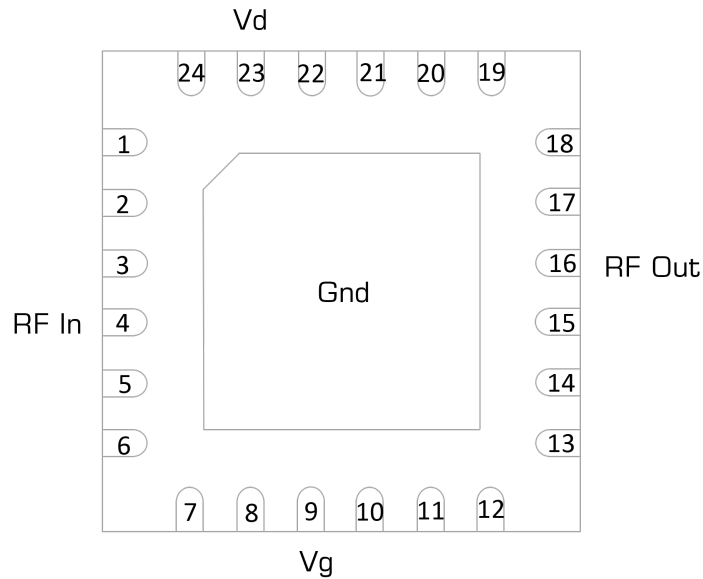
Revision History

Revision Code	Revision Date	Comment
-	2024-11-13	Initial Release

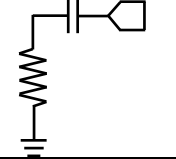
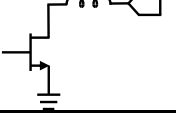
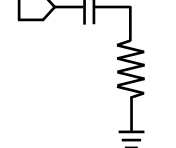
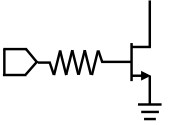
Port Configuration and Functions

Port Diagram

A port diagram of the AMM-9619PSM QFN package is shown below (X-ray view from the top).



Port Functions

Port	Function	Description	Equivalent Circuit for Package
1-3, 5-8, 10-15, 17-22, 24, Paddle	Ground	These pins should be connected to ground. Package ground paddle must be connected to a DC/RF ground potential with high thermal and electrical conductivity.	-
16	RF Output	Pin 16 is the RF Output port of the amplifier. It is DC blocked, and RF matched to 50 Ω.	
23	Vd	Pin 23 provides the main power supply for the amplifier. Apply gate bias voltage Vg before applying drain power supply.	
4	RF Input	Pin 4 is the RF Input port of the amplifier die. It is internally DC blocked and RF matched to 50 Ω.	
9	Vg	Pin 9 provides a required negative bias which controls the power supply currents to the amplifier. More negative voltages decrease supply current. Apply gate bias voltage Vg before applying drain power supply.	

Specifications

Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If any one of these limits are exceeded, the device may become inoperable or have a reduced lifetime.

Parameter	Maximum Rating	Unit
Drain Current (RF Applied)	130	mA
Drain Supply Voltage (Vd)	6	V
Gate Voltage (Vg)	-1	V
Maximum Operating Temperature for MTTF > 1E6 hours	85	°C
Maximum Storage Temperature	150	°C
Minimum Operating Temperature for MTTF > 1E6 hours	-55	°C
Minimum Storage Temperature	-65	°C
RF Input Power	20	dBm

Package Information

Parameter	Details	Rating
Dimensions	-	4 x 4 mm
Moisture Sensitivity Level	-	MSL 1

Recommended Operating Conditions

Datasheet operating parameters are taken and guaranteed using constant voltage biasing with TA = 25 °C, Vd = 5 V, Idq = 74 mA, and 50 Ω matched input and output. Adjust Vg from -1 V to -0.41 V max to achieve Idq = 74 mA typical.

Parameter	Min	Nominal	Max	Unit
Negative Bias Voltage (Vg)	-0.5	-0.4	-0.3	V
Positive DC Current (Id) (No RF Input)	-	74	-	mA
Positive DC Voltage (Vd)	-	5	-	V
Operating Temperature	-40	25	85	°C

Sequencing Requirements

Turn-on Procedure if required biases are unknown:

1. Apply -1 V to Vg.
2. Apply desired Vd.
3. Increase Vg voltage towards -0.41 V until Id = 74 mA.
4. Apply RF input power.

Turn-on Procedure if required biases are known:

1. Apply desired Vg (previously determined to produce 74 mA Idq).
2. Apply desired Vd.
3. Apply RF input power.

Turn-off Procedure:

1. Turn off RF input power.
2. Turn off Vd.
3. Turn off Vg.

Electrical Specifications

Unless otherwise specified, electrical specifications apply at TA=+25°C, Vd = 5 V, Idq=74 mA (where Idq is the drain current with no RF applied), Vg set as required to achieve Idq = 74 mA in a 50 Ω system.

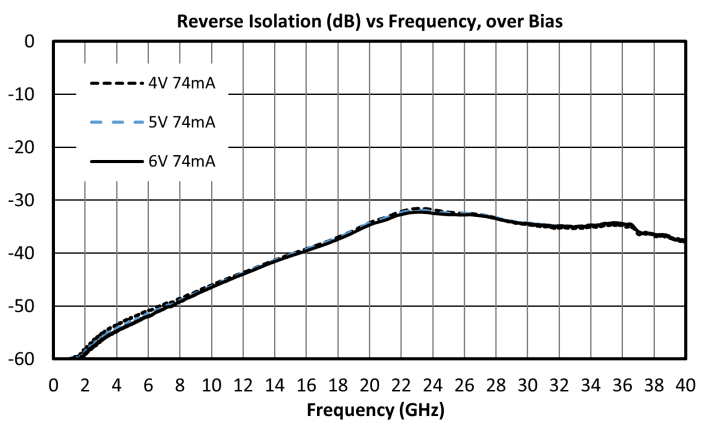
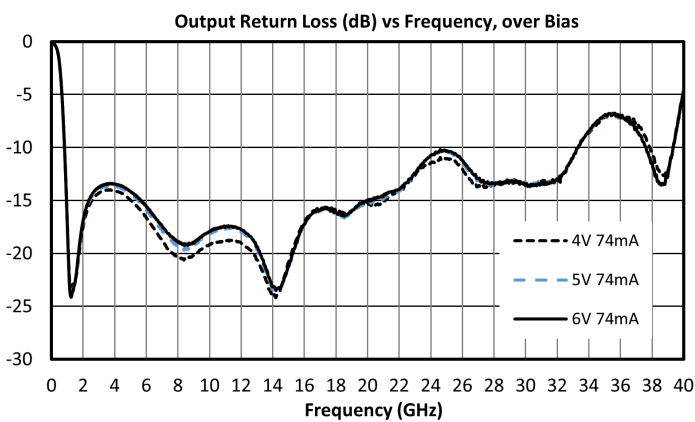
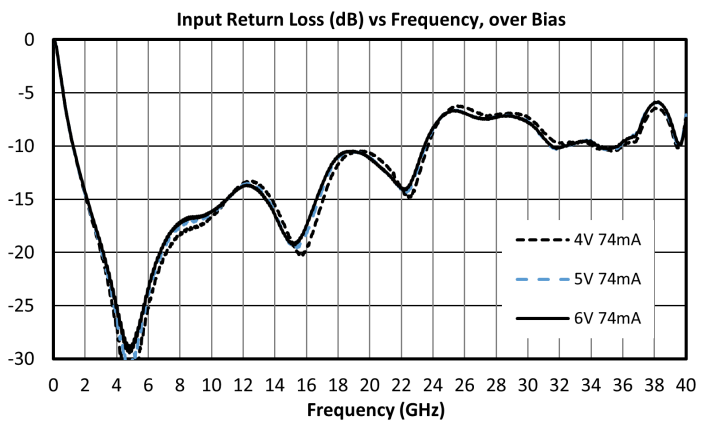
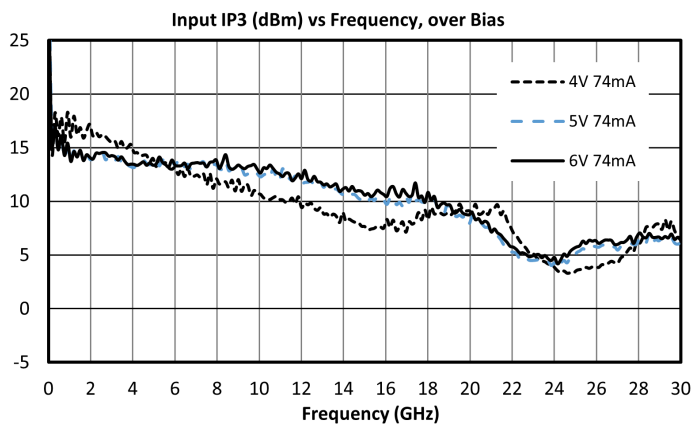
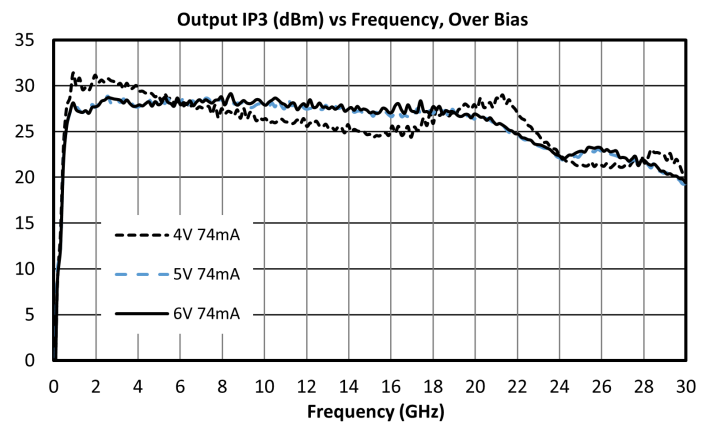
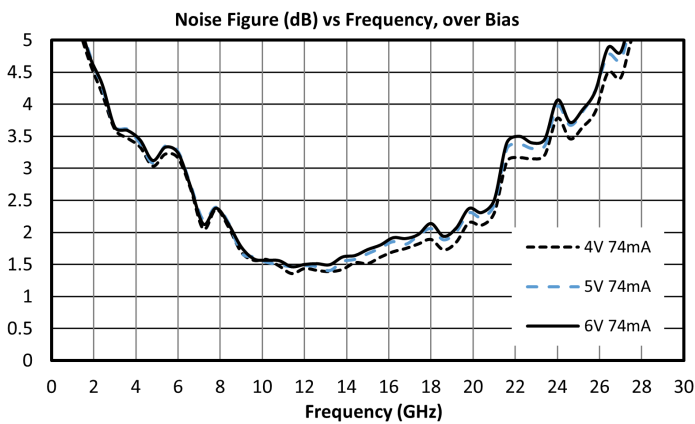
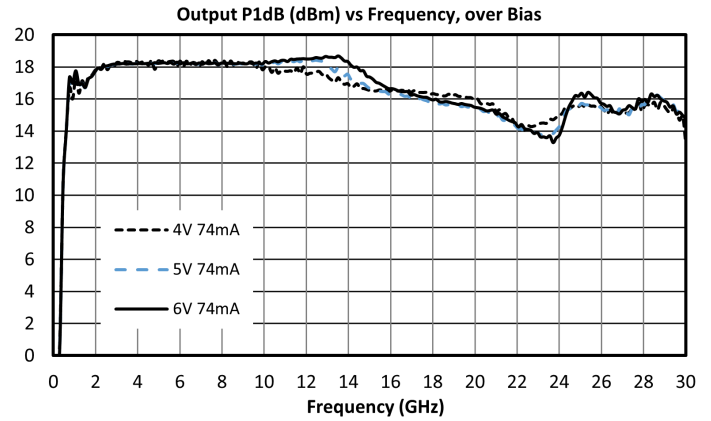
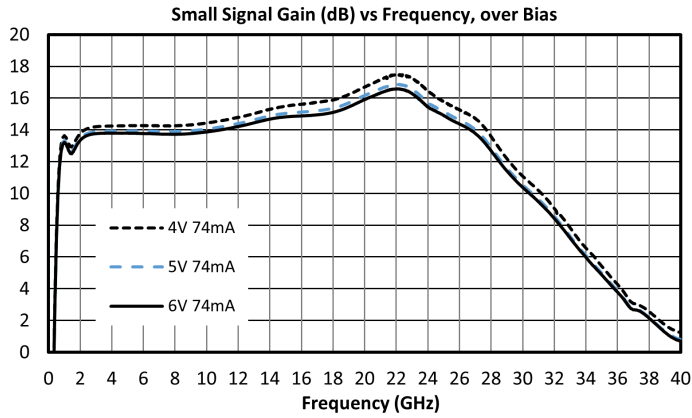
Parameter	Test Conditions	Minimum Frequency (GHz)	Maximum Frequency (GHz)	Min	Typ	Max	Unit
Input IP3	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -15 dBm per tone, 10 MHz tone spacing	18	26	-	5.8	-	dBm
Input IP3	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -15 dBm per tone, 10 MHz tone spacing	8	18	-	11.5	-	dBm
Input Return Loss	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	18	26	-	11	-	dB
Input Return Loss	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	8	18	-	16	-	dB
Output IP3	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -15 dBm per tone, 10 MHz tone spacing	18	26	-	25	-	dBm
Output IP3	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -15 dBm per tone, 10 MHz tone spacing	8	18	-	28	-	dBm
Output P1dB	Vd = 5 V, Vg set to achieve Idq = 74 mA	14	26	-	15.6	-	dBm
Output P1dB	Vd = 5 V, Vg set to achieve Idq = 74 mA	2	14	-	18.2	-	dBm
Output Return Loss	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	8	18	-	18	-	dB
Output Return Loss	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	18	26	-	14	-	dB
Reverse Isolation	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	8	18	-	43	-	dB
Reverse Isolation	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	2	8	-	46	-	dB
Reverse Isolation	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	18	26	-	33	-	dB
Small Signal Gain	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	8	18	-	14.5	-	dB
Small Signal Gain	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	18	26	-	16.1	-	dB
DC Supply Quiescent Current (Idq)	Vd = 5 V, Vg = -0.41V, No RF Applied	-	-	-	74	-	mA
Input IP3	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -15 dBm per tone, 10 MHz tone spacing	2	8	-	13.5	-	dBm
Input Return Loss	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	2	8	-	17	-	dB
Noise Figure	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	18	26	-	3.3	-	dB
Noise Figure	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	2	8	-	3.4	-	dB
Noise Figure	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	8	18	-	1.6	-	dB
Output IP3	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -15 dBm per tone, 10 MHz tone spacing	2	8	-	28	-	dBm
Output Return Loss	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	2	8	-	18	-	dB
Small Signal Gain	Vd = 5 V, Vg set to achieve Idq = 74 mA, Pin = -20 dBm	2	8	-	14	-	dB

AMM-9619PSM

2 - 26 GHz GaAs Surface Mount LO Driver Amplifier

Typical Performance Plots

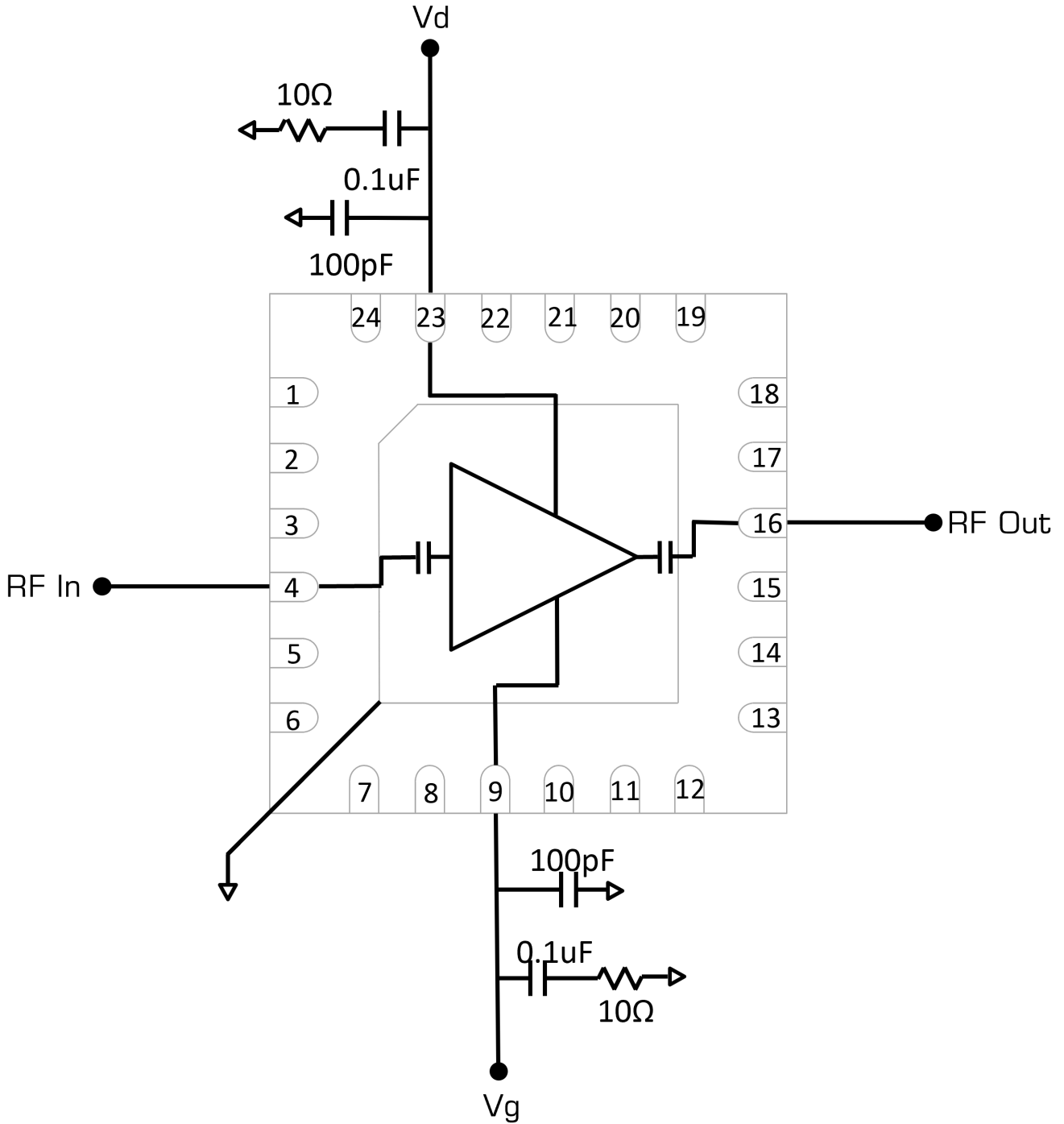
Performance plots have pcb trace losses de-embedded.



AMM-9619PSM

2 - 26 GHz GaAs Surface Mount LO Driver
Amplifier

Application Circuit



AMM-9619PSM

2 - 26 GHz GaAs Surface Mount LO Driver Amplifier

Application Circuit Description

The AMM-9619PSM includes DC blocking capacitors on the RF input and output pins. The only external components required are supply bypass capacitors on the Vd and Vg lines.

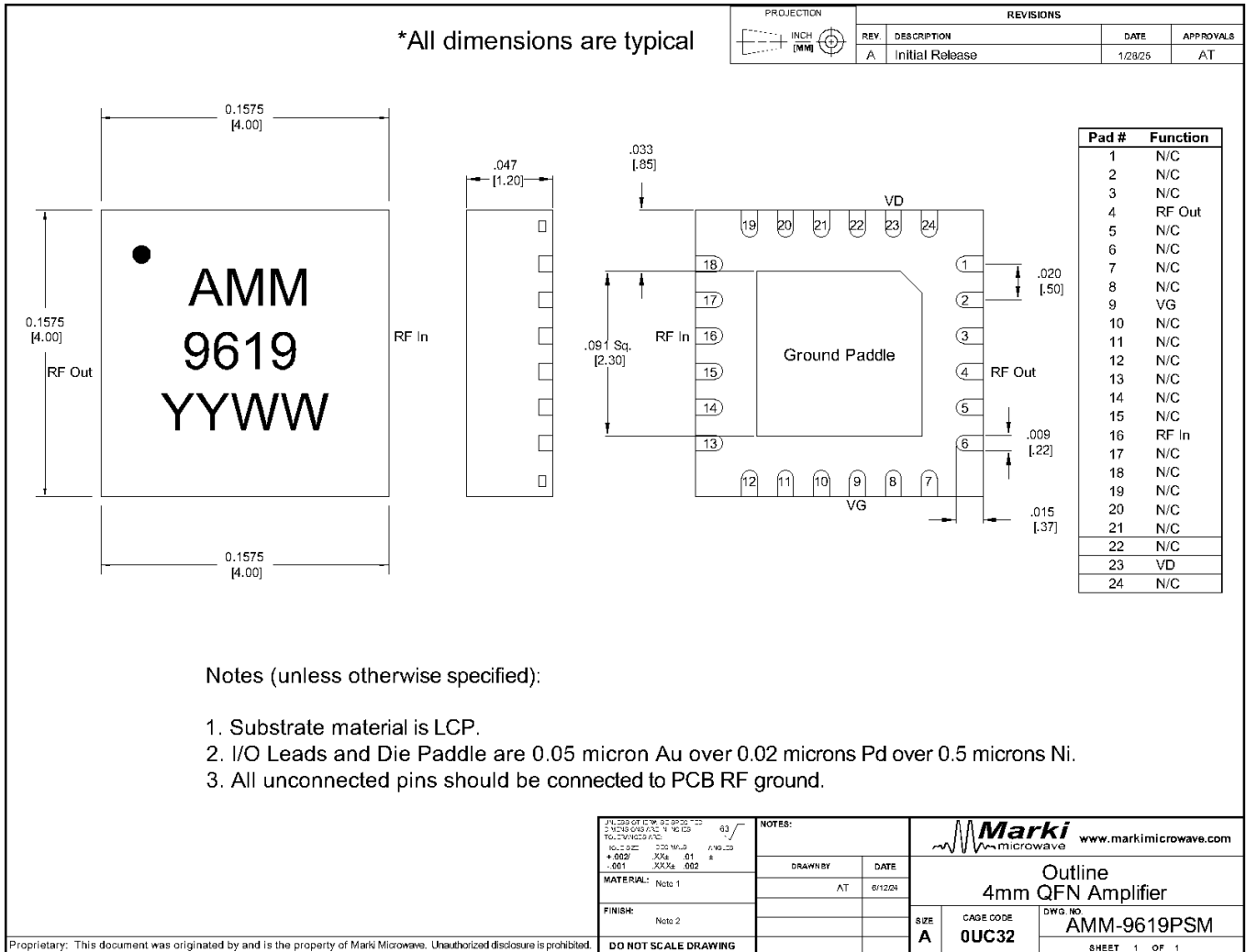
AMM-9619PSM

2 - 26 GHz GaAs Surface Mount LO Driver Amplifier

Mechanical Data

Outline Drawing

Download : [Outline 2D Drawing](#) | [Outline 3D Drawing](#) | [Outline 3D STP](#)

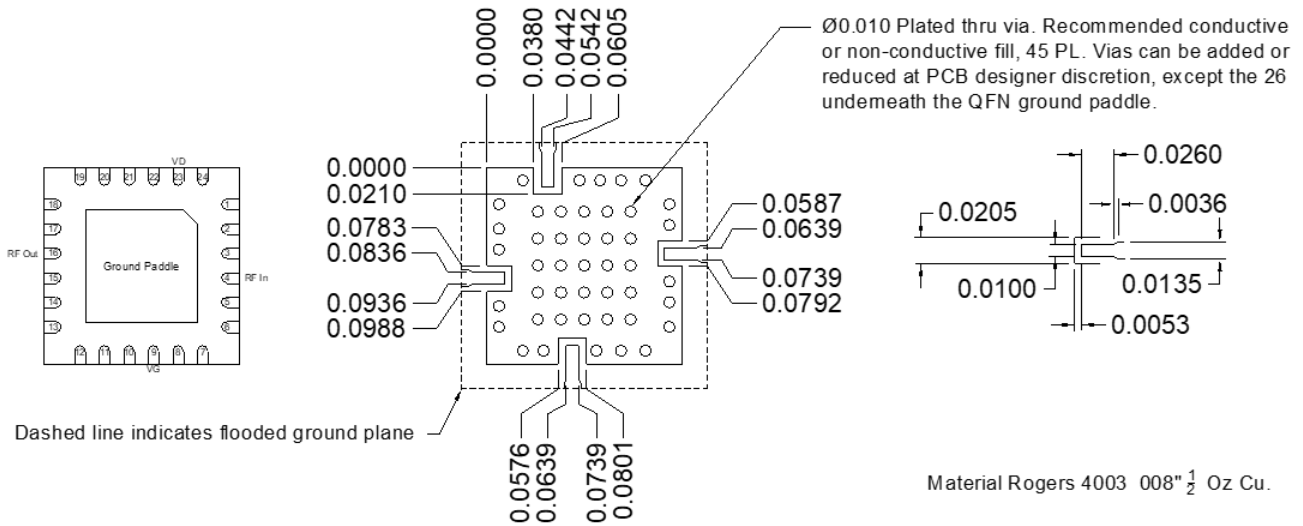


AMM-9619PSM

2 - 26 GHz GaAs Surface Mount LO Driver Amplifier

Footprint Image

Download : [Footprint Drawing](#)



DISCLAIMER

MARKI MICROWAVE, INC., ("MARKI") PROVIDES TECHNICAL SPECIFICATIONS AND DATA (INCLUDING DATASHEETS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, AND OTHER INFORMATION AND RESOURCES "AS IS" AND WITH ALL FAULTS. MARKI DISCLAIMS ALL WARRANTIES, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT. These resources are intended for developers skilled in the art designing with Marki products. You are solely responsible for (1) selecting the appropriate products for your application, (2) designing, validating, and testing your application, and (3) ensuring your application meets applicable standards and other requirements. Marki makes no guarantee regarding the suitability of its products for any particular purpose, nor does Marki assume any liability whatsoever arising out of your use or application of any Marki product.

Marki grants you permission to use these resources only for development of an application that uses Marki products. Other reproduction or use of these resources is strictly prohibited. No license is granted to any other Marki intellectual property or to any third-party intellectual property. Marki reserves the right to make changes to the product(s) or information contained herein without notice.

MARKI MICROWAVE and T3 MIXER are trademarks or registered trademarks of Marki Microwave, Inc. All other trademarks used are the property of their respective owners.

© 2024, Marki Microwave, Inc