

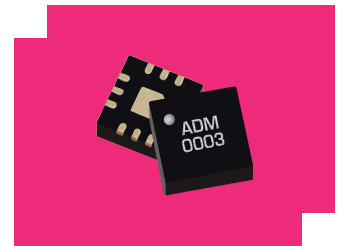
ADM-10713PSM

12-18 GHz Banded Low Noise Amplifier

DEVICE OVERVIEW

General Description

The ADM-10713PSM is a broadband low noise amplifier covering 12 to 18 GHz. Designed for ultra-low power consumption, it operates from a single 3 V supply at only 8 mA bias current while delivering 27.3 dB small-signal gain with a typical noise figure of just 1.6 dB. Packaged in a compact 3 mm QFN package, the ADM-10713PSM is ideal for front-end applications that demand high gain with minimal added noise and minimal DC power consumption. Measured small-signal S-parameters are provided to support accurate system simulations.



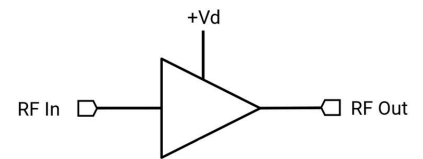
Features

- High Gain, 27.3 dB Typical
- Low Noise Figure, 1.6 dB Typical
- Low DC Power Consumption, 3 V at 8 mA
- Single Supply Voltage
- Small 3x3mm QFN Package

Applications

- SATCOM
- Phased array systems

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
ADM-10713PSM	12-18 GHz Banded Low Noise Amplifier	Plastic QFN	REACH RoHS	Released	EAR99
EVB-ADM-10713P	Evaluation Board, 12-18 GHz Banded Low Noise Amplifier	EVB	REACH RoHS	Released	EAR99

Table Of Contents

- **Device Overview**
 - General Description
 - Features
 - Applications
 - Functional Block Diagram
- **Port Configuration and Functions**
 - Port Diagram
 - Port Functions
- **Revision History**
- **Specifications**
 - Absolute Maximum Ratings
 - Package Information
 - Recommended Operating Conditions
 - Sequencing Requirements
 - Electrical Specifications
 - Typical Performance Plots (vs Bias)
 - Typical Performance Plots (vs Temperature)
- **Operation**
 - Application Information
 - Application Circuit
 - Application Circuit Description
- **Mechanical Data**
 - Outline Drawing
- **Footprint Image**
- **Evaluation Board**
 - Evaluation Board Outline Drawing

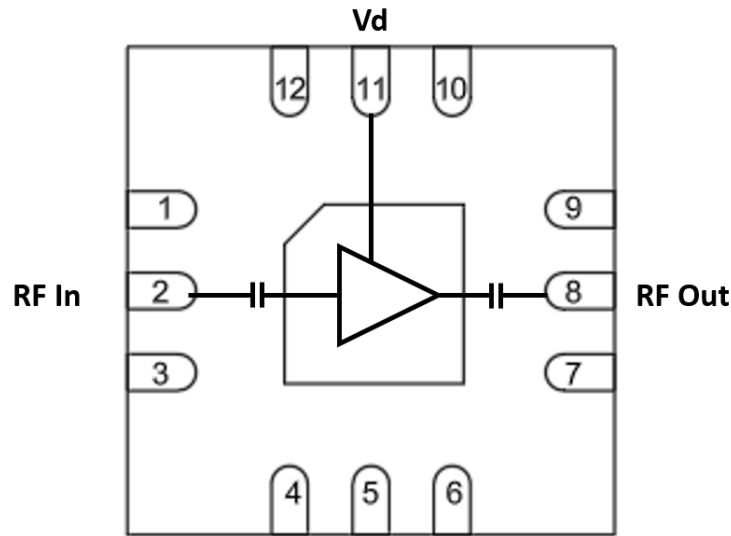
Revision History

Revision Code	Revision Date	Comment
-	2025-10-20	Initial Release

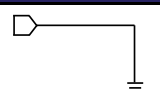
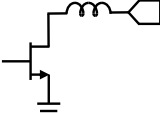
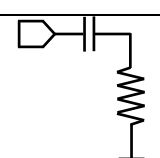
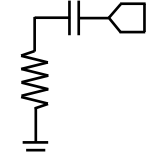
Port Configuration and Functions

Port Diagram

A port diagram of the ADM-10713PSM is shown below.



Port Functions

Port	Function	Description	DC Equivalent Circuit
GND	Ground	Ground paddle and non-connected pins must be connected to a DC/RF ground potential with high thermal and electrical conductivity, and low inductance.	
Pin 11	Vd	Pin 11 is the DC bias supply for the amplifier. The voltage at this pin should be set to 3V for normal operation. This part requires an off-chip bypass capacitor of 0.1uF installed at this pin as close to the IC as possible. See applications circuit.	
Pin 1,3,4,5,6,7,9,10,12	Ground	These pins are not internally connected to the amplifier die. It is recommended to connect these pins to ground to provide RF isolation and mechanical stability. See the recommended landing pattern for details. Datasheet performance was measured with these pins connected to GND.	-
Pin 2	RF Input	Pin 2 is the amplifier's RF input pin. This port is internally matched to 50 Ohms and is internally DC blocked.	
Pin 8	RF Output	Pin 8 is the amplifier's RF output pin. This port is internally matched to 50 Ohms and is internally DC blocked.	

Specifications

Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded, the device may be inoperable or have a reduced lifetime. This amplifier is designed and characterized in a 50Ω system, and operation in a reflective environment can cause performance degradation.

Parameter	Maximum Rating	Unit
Maximum Operating Temperature	85	°C
Maximum Storage Temperature	150	°C
Max Junction Temperature for MTTF > 1E6 hours	175	°C
Minimum Operating Temperature	-40	°C
Minimum Storage Temperature	-65	°C
Positive Drain Supply Current (with RF Input)	19	mA
Positive Drain Supply Voltage (Vd)	6	V
RF Input Power	10	dBm

Package Information

Parameter	Details	Rating
ESD	< 250 Volts	HBM Class 0
Dimensions	-	3 x 3 mm
Moisture Sensitivity Level	-	MSL 1

Recommended Operating Conditions

The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications. Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the Electrical Specifications. For limits, above which damage may occur, see Absolute Maximum Ratings .

Parameter	Min	Nominal	Max	Unit
Power Supply DC Voltage	3	3	6	V
Ambient Temperature	-40	25	85	°C
Positive DC Current (Id) (No RF Input)	8	8	19	mA

Sequencing Requirements

There are no sequencing requirements to power up or power down the amplifier.

Electrical Specifications

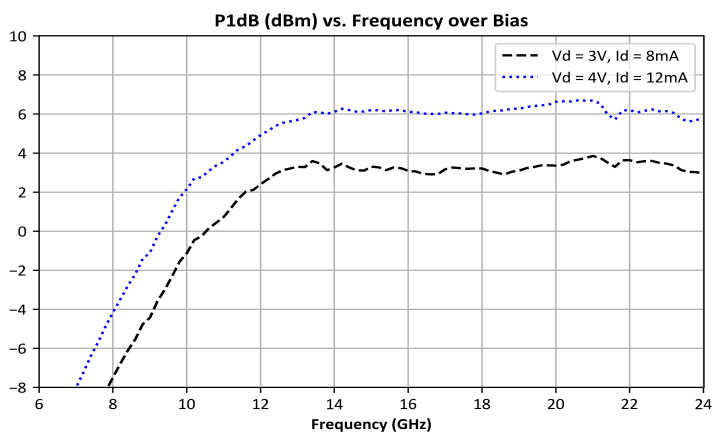
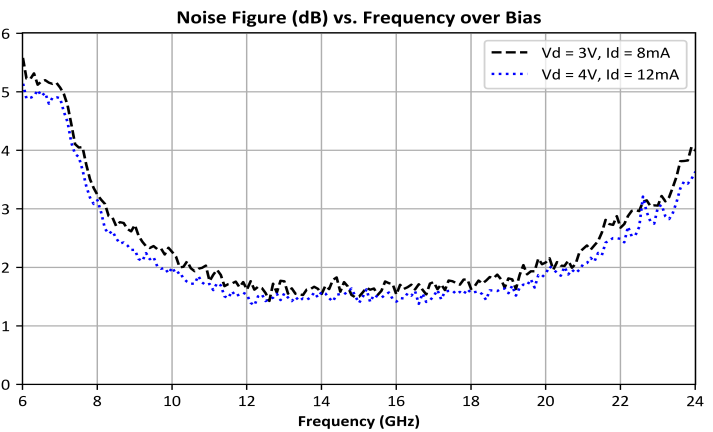
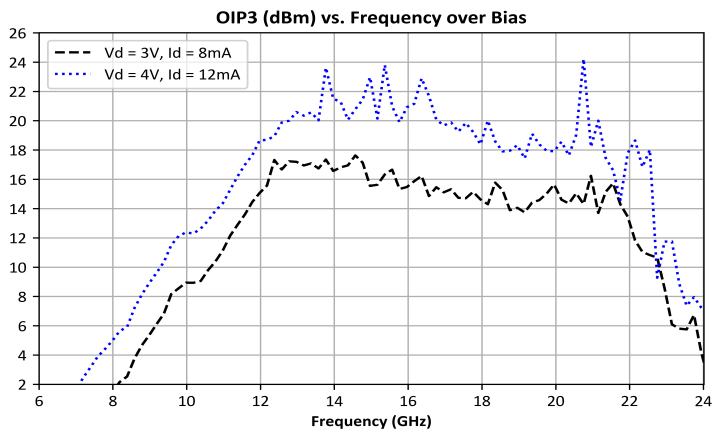
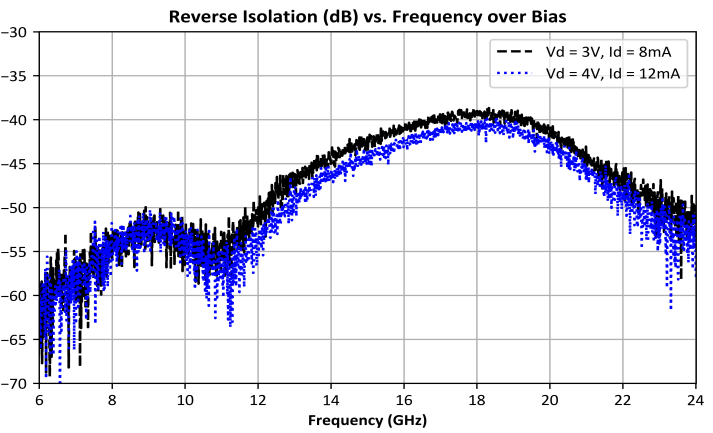
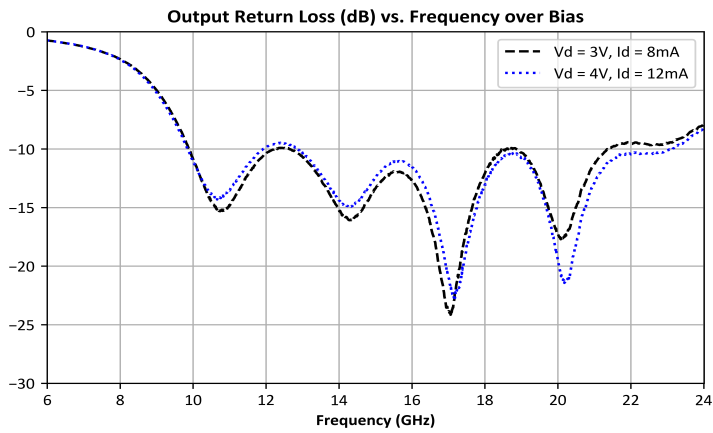
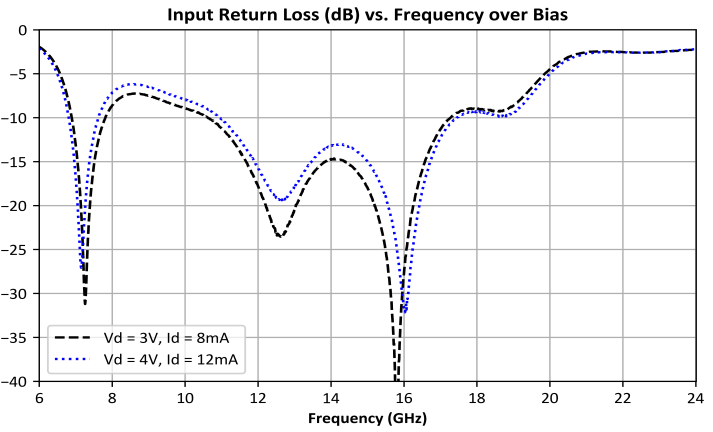
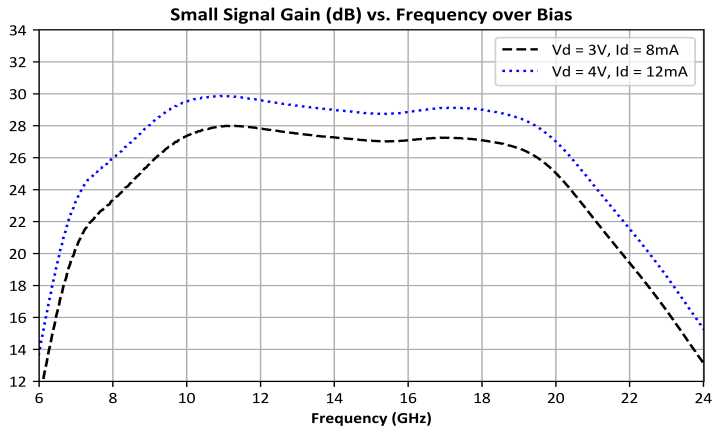
The electrical specifications apply at TA=+25°C in a 50Ω system. QFNs are 100% RF tested.

Parameter	Test Conditions	Minimum Frequency (GHz)	Maximum Frequency (GHz)	Min	Typ	Max	Unit
Small Signal Gain	3V bias, -30 dBm Input Power	12	18	-	27.3	-	dB
Input Return Loss	3V bias, -30 dBm Input Power	12	18	-	18	-	dB
Output Return Loss	3V bias, -30 dBm Input Power	12	18	-	14	-	dB
Reverse Isolation	3V bias, -30 dBm Input Power	12	18	-	43	-	dB
Noise Figure	3V bias, -30 dBm Input Power	12	18	-	1.6	-	dB
Output P1dB	3V bias	12	18	-	3	-	dBm
Output IP3	3V bias	12	18	-	15	-	dBm
Drain Current, Id ¹	3V bias	-	-	-	8	-	mA

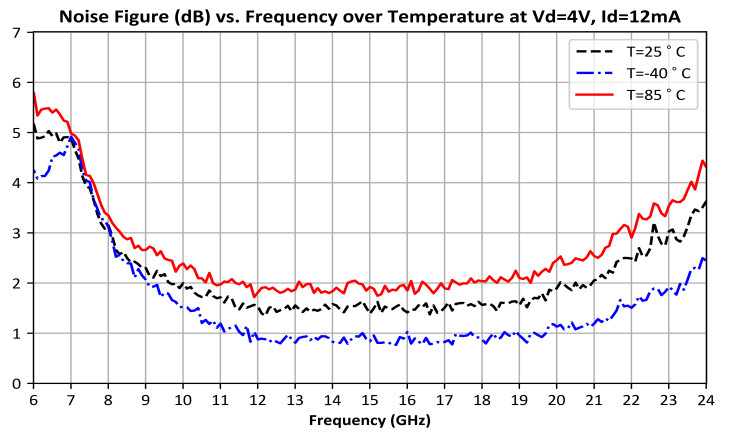
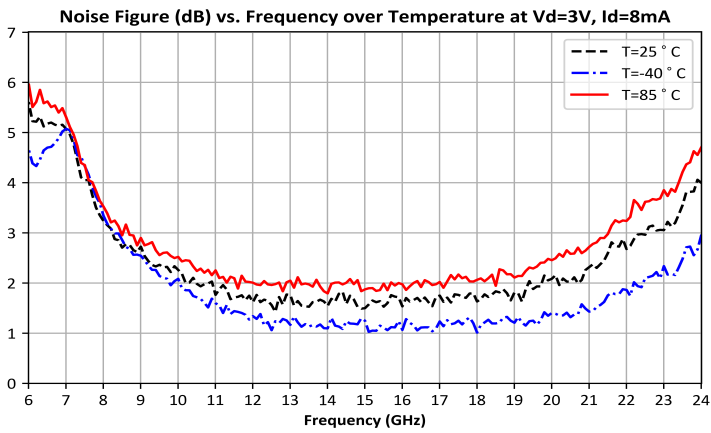
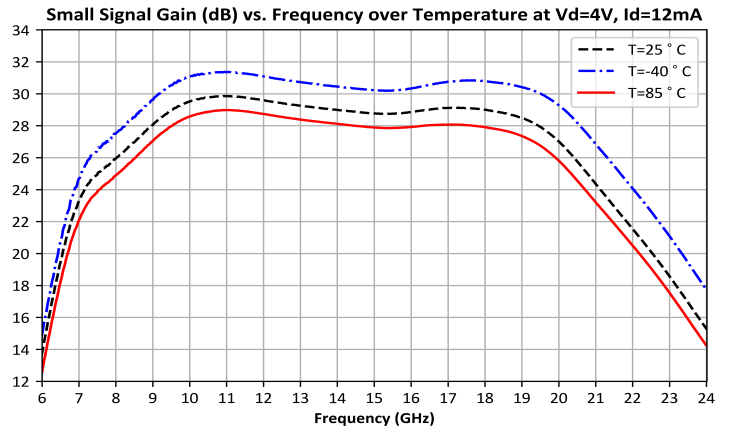
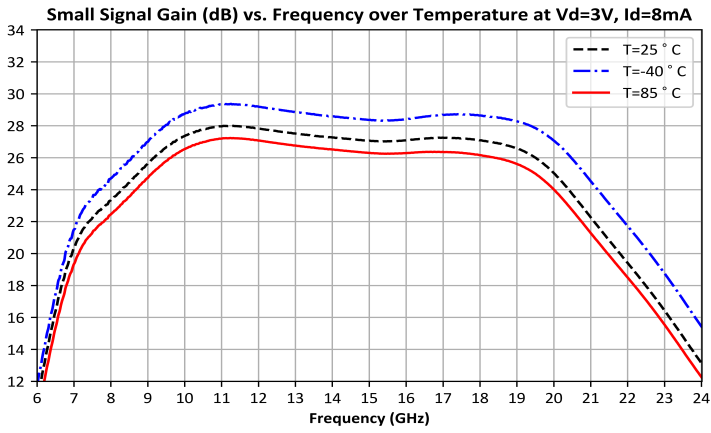
^[1] Bias conditions for Id tested with no RF input power. Bias conditions presented as Vd.

Typical Performance Plots (vs Bias)

Measurement data de-embedded using standard evaluation board and external wideband test equipment bias tees.



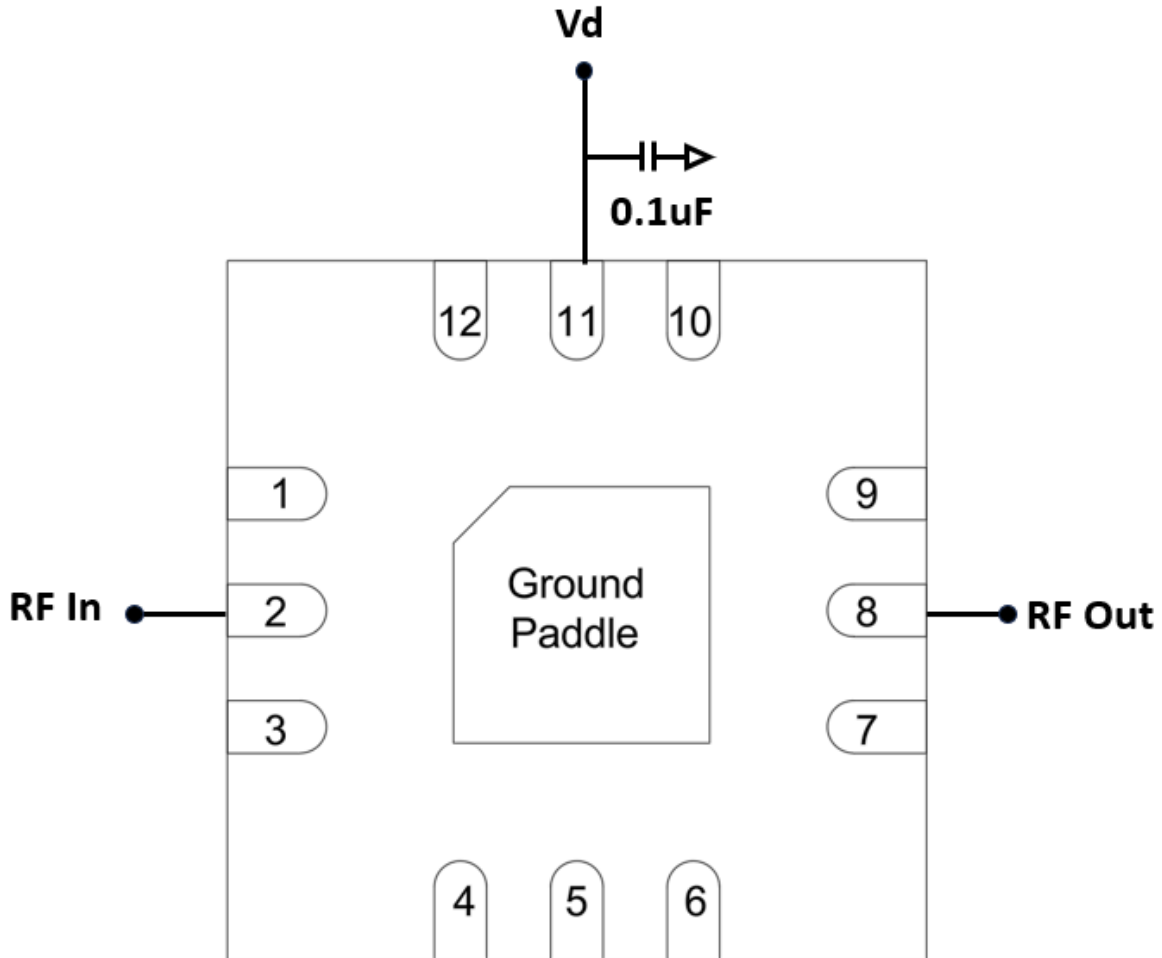
Typical Performance Plots (vs Temperature)



Application Information

Below is the recommended application circuit for the ADM-10713PSM. This is the configuration that is used to characterize this device. However, each PCB layout and environment are different which may require minor modifications of the biasing network. Please contact support@markimicrowave.com for more information.

Application Circuit

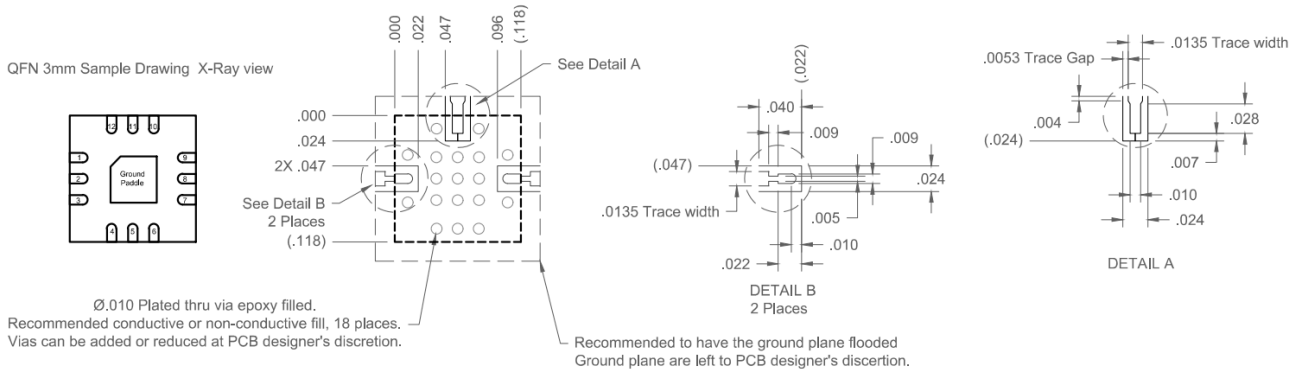


Application Circuit Description

Above is the recommended application circuit for the ADM-10713PSM. DC drain voltage is supplied to the amplifier across a 0.1uF bypass capacitor to the Vd pin. The RF input and output ports are internally DC blocked.

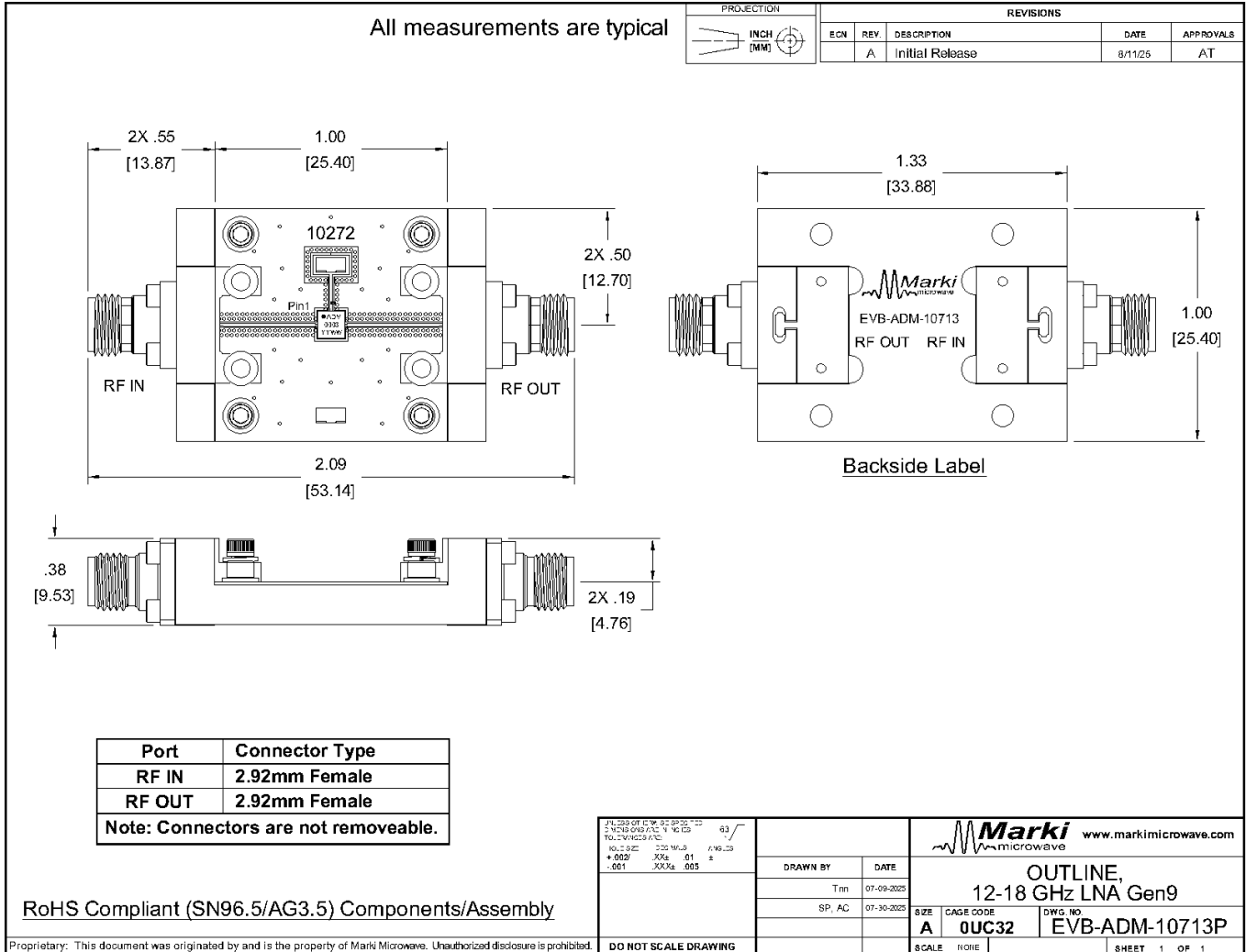
Footprint Image

Download : [Footprint Drawing](#)



The landing pattern is to be used on Rogers 4003,
0.008" thick, ½ Oz Cu.

Evaluation Board - Outline Drawing



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