Sales: 408-778-9952 | General: 408-778-4200 | Fax: 408-778-4300
Sales & Customer Support: sales@markimicrowave.com
Tech Support: support@markimicrowave.com

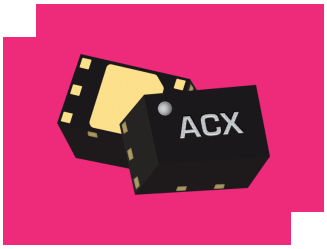
AKA-1500PSM

DC – 14 GHz Broadband InGaP SMT Amplifier

DEVICE OVERVIEW

General Description

The AKA-1500PSM is a low-cost, cascadable broadband InGaP HBT amplifier. This is a general-purpose gain block amplifier which provides high P1dB, high OIP3, and a very small form factor. The simple application circuit requires minimal external components, allowing it to be used in a variety of applications.



Download s-parameters here

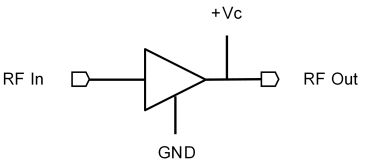
Features

- Small Form Factor: 1.3 x 2.0mm
- +28 dBm OIP3 up to 6 GHz
- 18.5 dB Gain at 2 GHz
- Positive Only, Single Supply Operation
- Low-Cost

Applications

- Mobile test and measurement equipment
- Radar and satellite communications
- 5G transceivers
- Driver Amplifier L-Diode Mixers

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
AKA-1500PSM	DC – 14 GHz Broadband InGaP SMT Amplifier	DFN	REACH RoHS	Released	EAR99
EVB-AKA-1500P	Evaluation Board, Broadband InGaP SMT DC – 14 GHz Amplifier	EVB	REACH RoHS	Released	EAR99

Table Of Contents

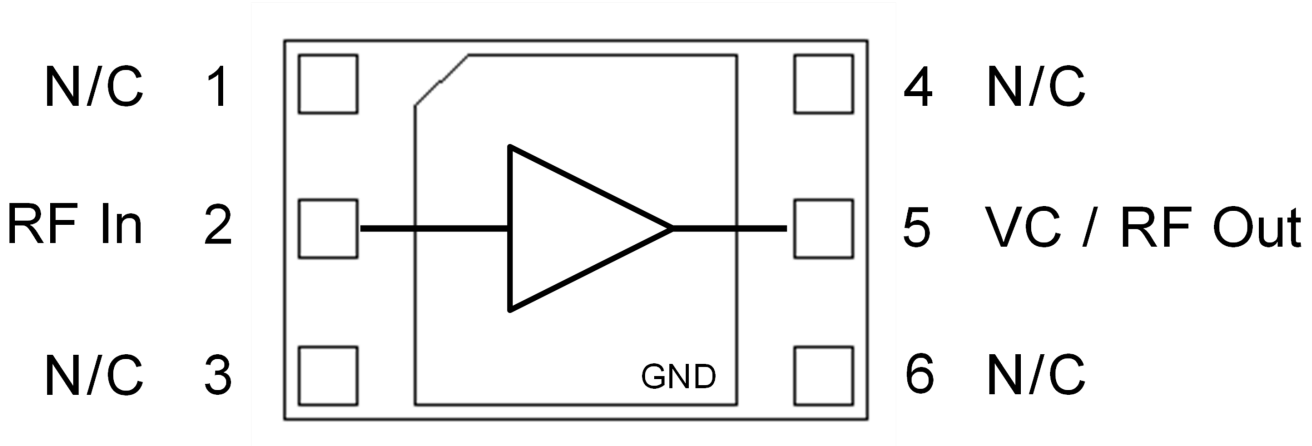
Revision History

Revision Code	Revision Date	Comment
-	2023-04-01	Datasheet Initial Release

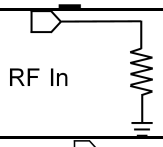
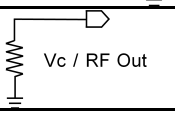
Port Configuration and Functions

Port Diagram

A port diagram of the AKA-1500PSM is shown below.



Port Functions

Port	Function	Description	Equivalent Circuit for Package
1, 3, 4, 6	N/C	These pins are internally no-connects and should be connected to DC/RF ground.	
2	RF Input	This is the RF Input port of the amplifier die. It is RF matched to 50 Ω and requires an external DC blocking capacitor.	
5	RF Output and Positive Collector Voltage Supply	This is the RF Output and positive supply voltage port Vc. It is RF matched to 50 Ω and is DC coupled. An external bias tee is required on this port.	
Paddle	Ground	Package ground paddle must be connected to a DC/RF ground potential with high thermal and electrical conductivity.	

Specifications

Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded, the device may become inoperable or have a reduced lifetime.

Parameter	Maximum Rating	Unit
Maximum Operating Temperature	85	°C
Maximum Storage Temperature	150	°C
Max Junction Temperature for MTTF > 1E6 Hours	150	°C
Minimum Operating Temperature	-40	°C
Minimum Storage Temperature	-65	°C
Positive Bias Current (Icc)	74	mA
Power Dissipation	308	mW
RF Input Power	10	dBm
θ_Jc, Junction to Case Thermal Resistance	212	°C/W

Package Information

Parameter	Details	Rating
ESD	250 to < 500 Volts	HBM Class 1A
Weight	Package name: DFN	0.007g
Dimensions	-	2.00 x 1.30 mm
Moisture Sensitivity Level	-	MSL 1

Recommended Operating Conditions

The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications. Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the electrical specifications. For limits, above which damage may occur, see Absolute Maximum Ratings.

Parameter	Min	Nominal	Max	Unit
Positive DC Device Voltage (Vd)	4.1	4.2	4.3	V
Positive DC Current (Icc)	35	50	60	mA
Ambient Temperature	-40	25	85	°C

Sequencing Requirements

There is no sequencing required to power up or power down the amplifier.

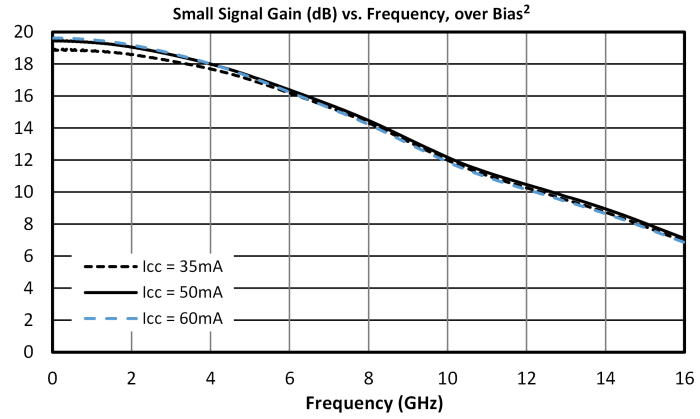
Electrical Specifications

Unless otherwise specified, electrical specifications apply at TA=+25°C, Vc = +4.2V in a 50Ω system. Typical performance data is measured from EVB unless otherwise stated. Min and Max limits are guaranteed at TA=+25°C.

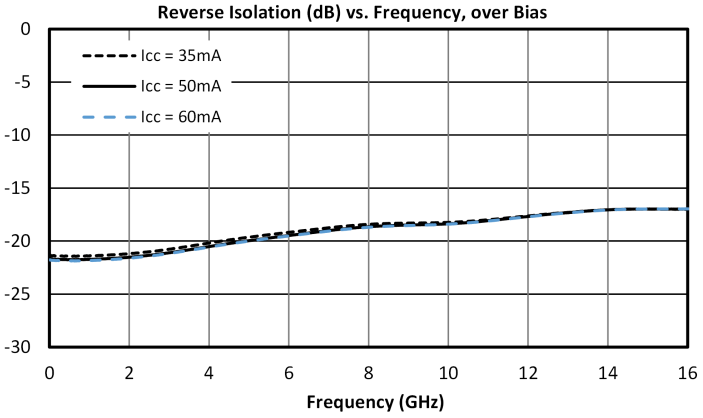
Parameter	Test Conditions	Minimum Frequency (GHz)	Maximum Frequency (GHz)	Min	Typ	Max	Unit
DC Supply Quiescent Current (Icc)	Vc = +4.2V, No RF Input	-	-	-	50	-	mA
Input Return Loss	Vc = +4.2 V, Icc = 50 mA	0.1	14	-	14	-	dB
Noise Figure	Vc = +4.2 V, Icc = 50 mA	0.1	6	-	4	-	dB
Noise Figure	Vc = +4.2 V, Icc = 50 mA	6	14	-	5	-	dB
Output IP3	Vc = +4.2 V, Icc = 50 mA	6	14	-	26	-	dBm
Output IP3	Vc = +4.2 V, Icc = 50 mA	0.1	6	-	28	-	dBm
Output P1dB	Vc = +4.2 V, Icc = 50 mA	2	6	14	16	-	dBm
Output P1dB	Vc = +4.2 V, Icc = 50 mA	6	14	11	13	-	dBm
Output Return Loss	Vc = +4.2 V, Icc = 50 mA	0.1	14	-	22	-	dB
Reverse Isolation	Vc = +4.2 V, Icc = 50 mA	0.1	14	-	19	-	dB
Small Signal Gain	Vc = +4.2 V, Icc = 50 mA	0.1	4	17	19	-	dB
Small Signal Gain	Vc = +4.2 V, Icc = 50 mA	12	14	7	9	-	dB
Small Signal Gain	Vc = +4.2 V, Icc = 50 mA	6	12	9	13	-	dB
Small Signal Gain	Vc = +4.2 V, Icc = 50 mA	4	6	15	17	-	dB

Typical Performance Plots

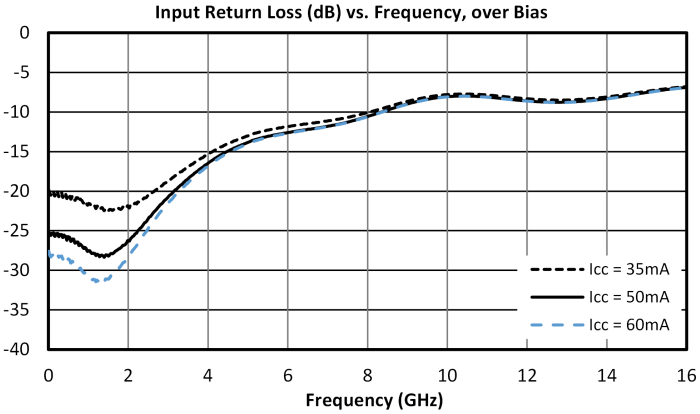
Small Signal Gain (dB) vs. Frequency, over Bias[2]



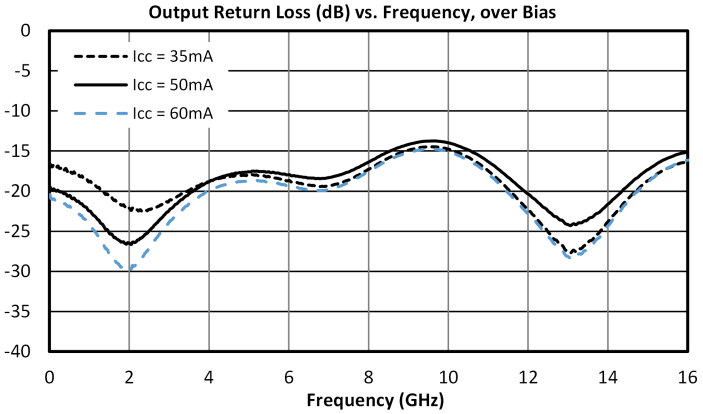
Reverse Isolation (dB) vs. Frequency, over Bias



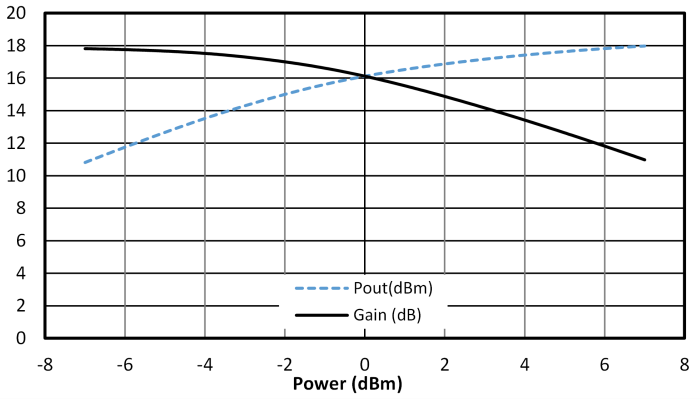
Input Return Loss (dB) vs. Frequency, over Bias



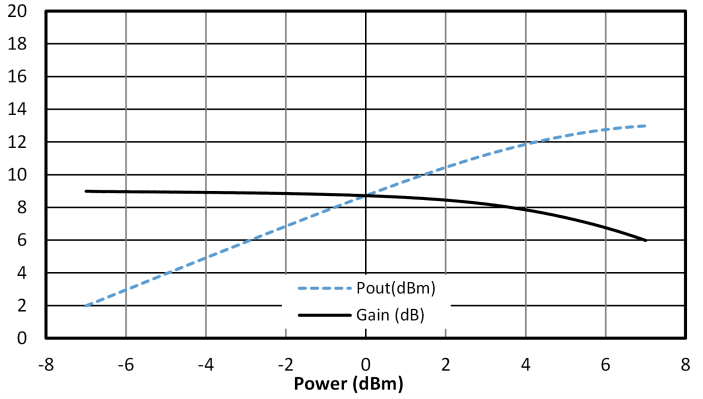
Output Return Loss (dB) vs. Frequency, over Bias



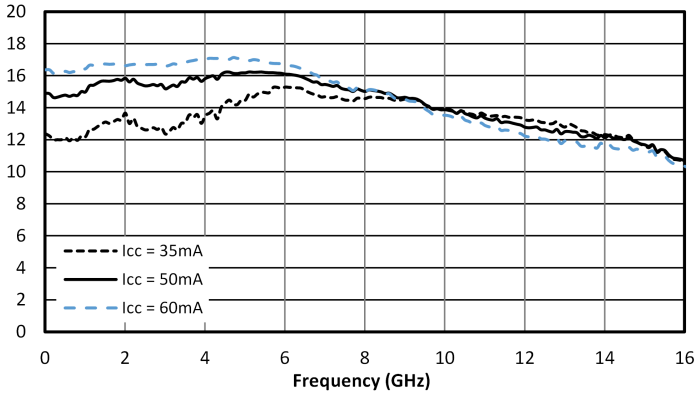
Output Power (dBm) and Gain (dB) vs. Input Power, F = 2 GHz



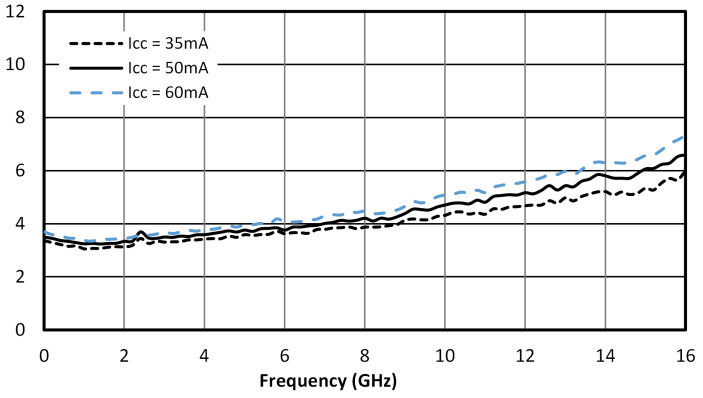
Output Power (dBm) and Gain (dB) vs. Input Power, F = 12 GHz

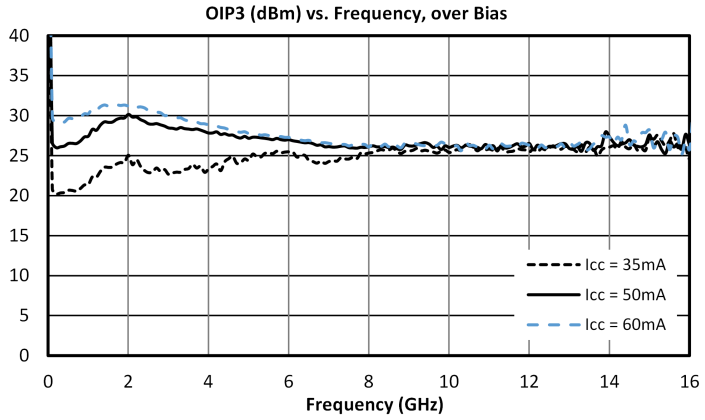


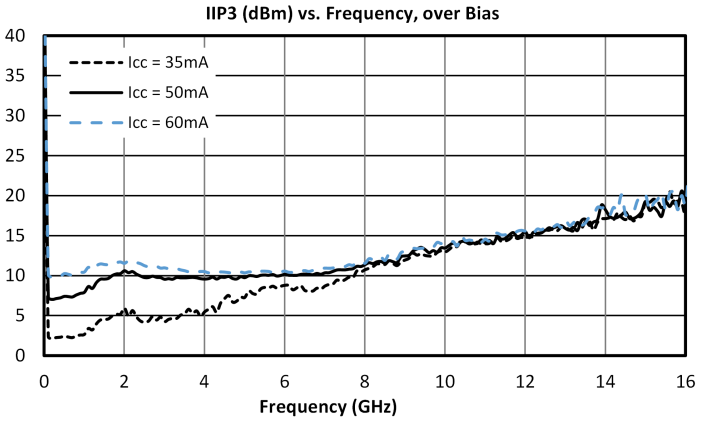
Output P1dB (dBm) vs. Frequency, over Bias[2]



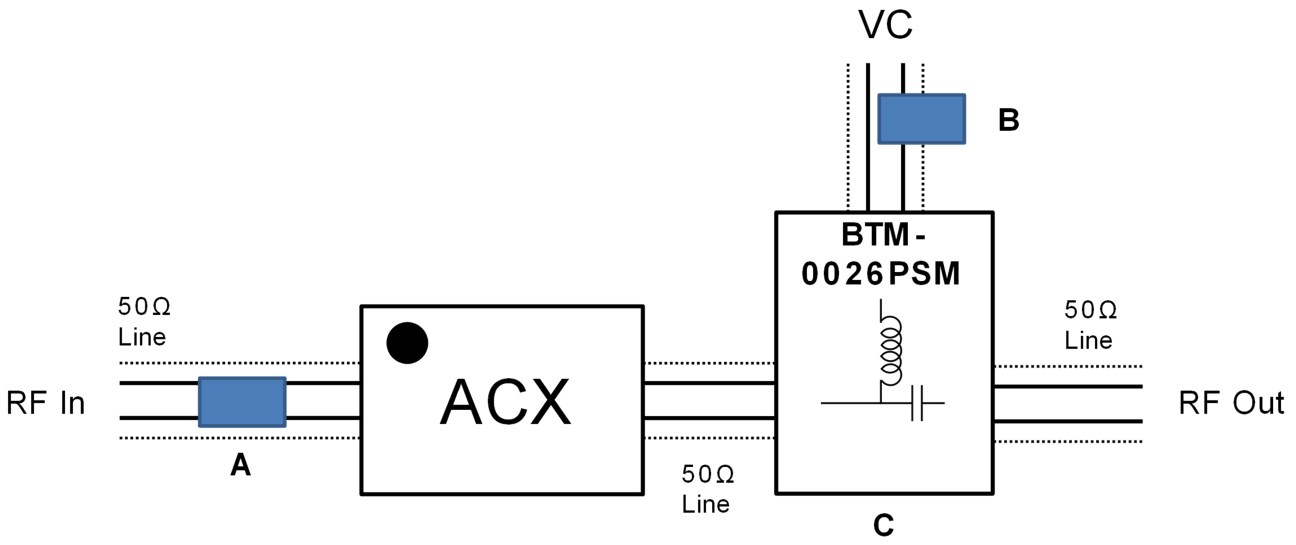
Noise Figure vs. Frequency, over Bias[2]







Application Circuit



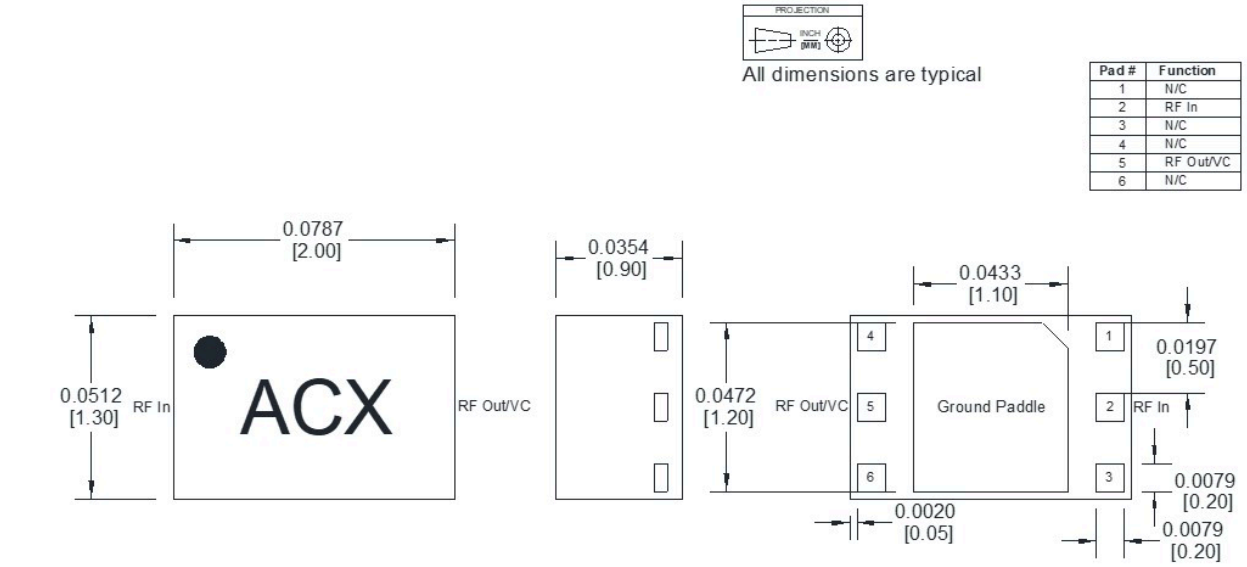
Application Circuit Description

The RF input and output should be soldered to 50Ω traces. This amplifier requires external DC blocking on the input and output in addition to positive collector biasing on the output.

Mechanical Data

Outline Drawing

Download : Outline 3D Drawing | Outline 3D STP



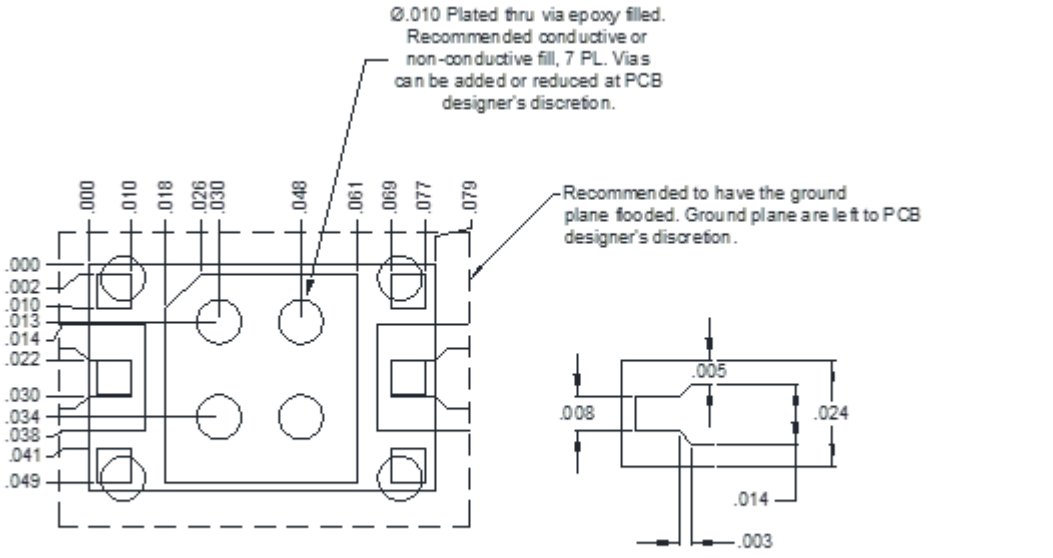
Pad #	Function
1	N/C
2	RF In
3	N/C
4	N/C
5	RF Out/VC
6	N/C

Notes (unless otherwise specified):

1. Substrate material is LCP.
2. I/O Leads and Die Paddle are (from base to finish):
   a. Ni: 0.5 microns MIN
   b. Pd: 0.02 microns MIN
   c. Au: 0.05 microns MAX
3. All unconnected pins should be connected to PCB RF ground.

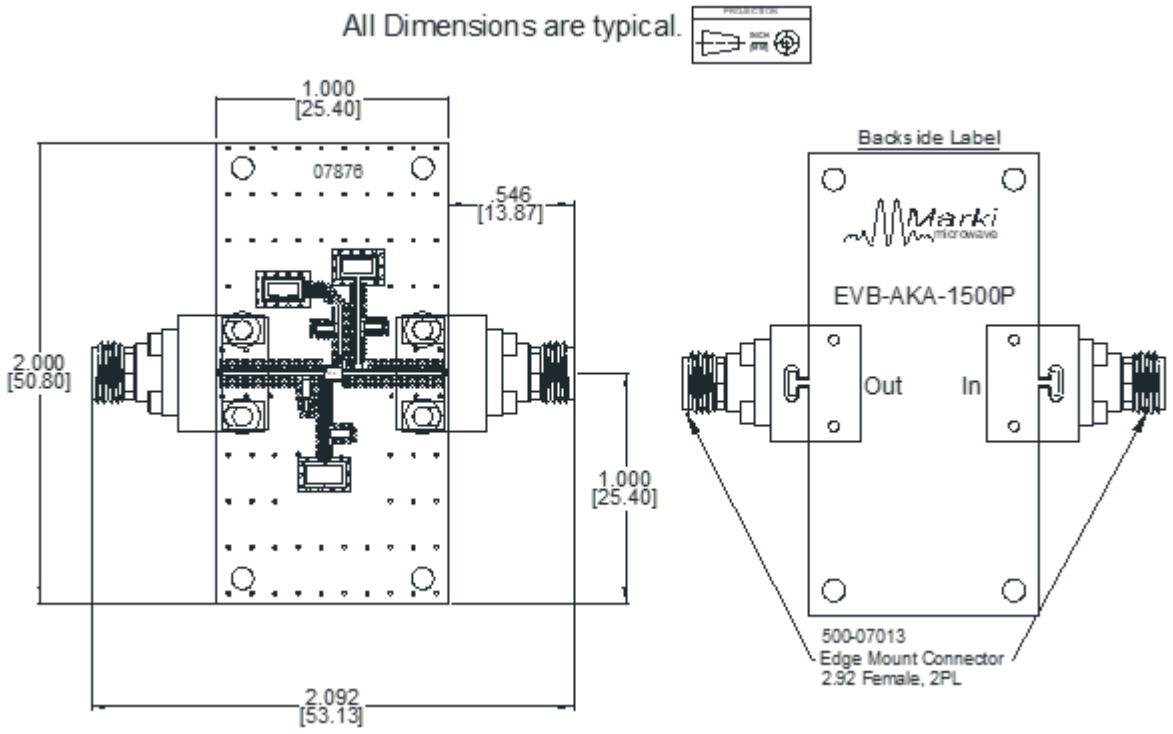
Footprint Image

Download : Footprint Drawing



The landing pattern is to be used on Rogers 4003, 0.008" thick, $\frac{1}{2}$ Oz Cu.

Evaluation Board - Outline Drawing



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