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Tech Support: support@markimicrowave.com

EVB-ADM-8624P

Evaluation Board, 0.2 - 20 GHz High Dynamic Range Gain Block Amplifier

DEVICE OVERVIEW

General Description

The EVB-ADM-8624P is a connectorized evaluation board for the ADM-8624PSM surface mount 0.5 - 18 GHz High Dynamic Range Gain Block Amplifier.

Download s-parameters here

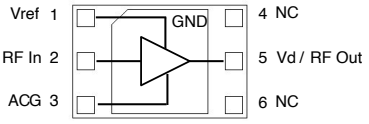
Features

N/A

Applications

N/A

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
ADM-8624PSM	0.2 - 20 GHz High Dynamic Range Gain Block	DFN	REACH RoHS	Released	EAR99
EVB-ADM-8624P	Evaluation Board, 0.2 - 20 GHz High Dynamic Range Gain Block Amplifier	EVB	REACH RoHS	Released	EAR99

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Evaluation Board, 0.2 - 20 GHz High Dynamic Range Gain Block Amplifier

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Specifications

Package Information

Parameter	Details	Rating
Dimensions	-	25.39x50.80 mm

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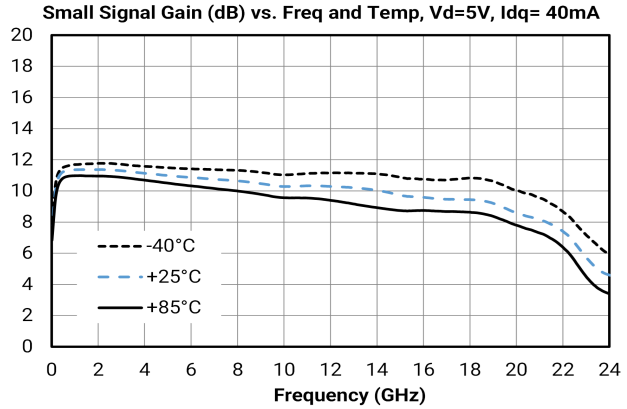
Evaluation Board, 0.2 - 20 GHz High Dynamic Range Gain Block Amplifier

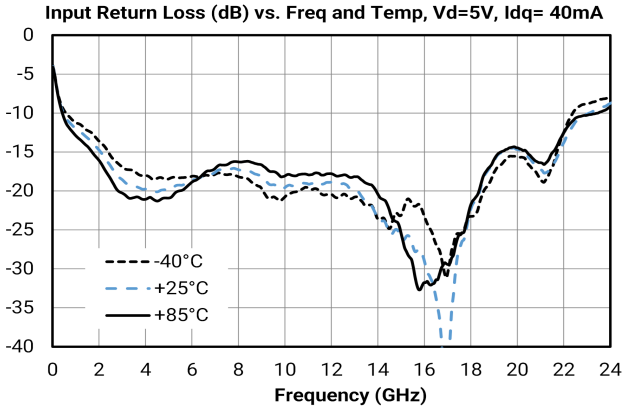
Typical Performance Plots

Measurements are taken at the evaluation board connectors.

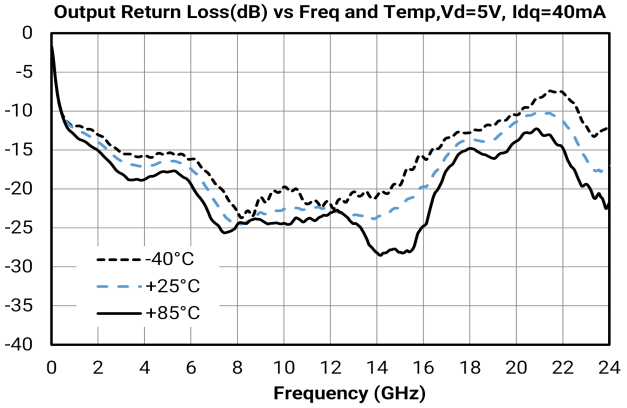
Small Signal Gain (dB) vs. Freq and Temp, Vd=5V, Idq= 40mA



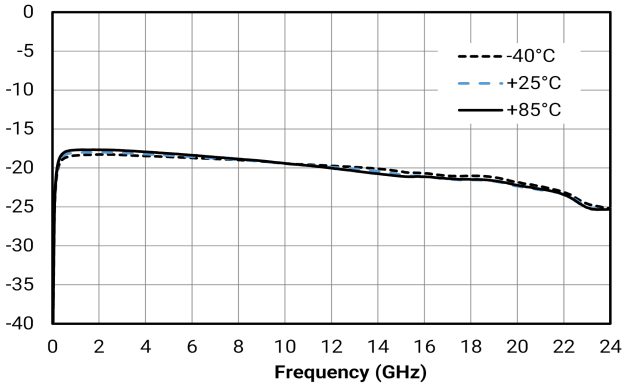
Input Return Loss (dB) vs. Freq and Temp, Vd=5V, Idq= 40mA



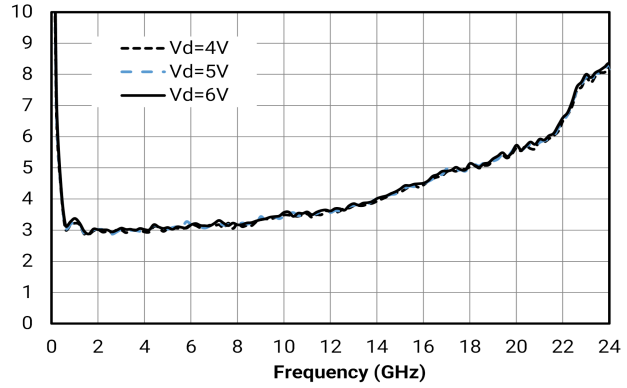
Output Return Loss(dB) vs Freq and Temp,Vd=5V, Idq=40mA



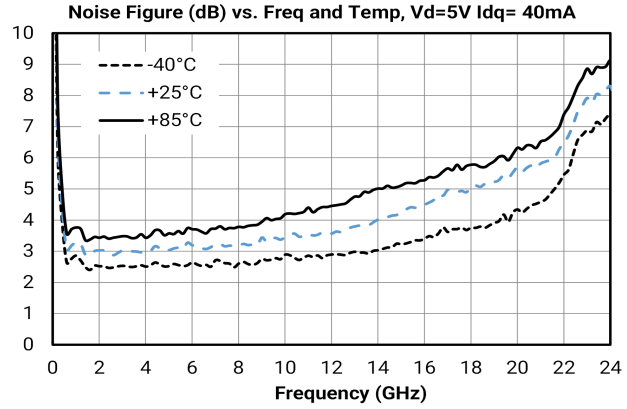
Reverse Isolation (dB) vs. Freq and Temp, Vd=5V, Idq= 40mA



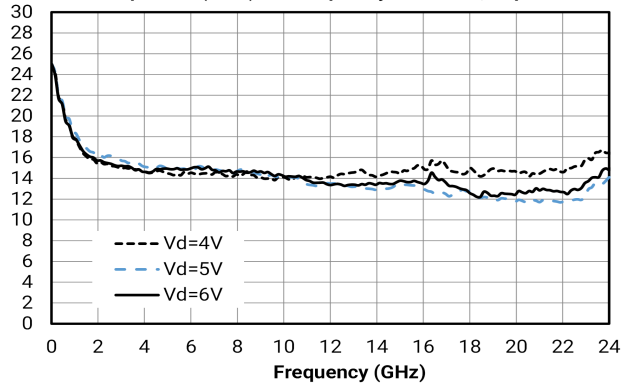
Noise Figure (dB) vs. Freq and Bias, Idq= 40mA



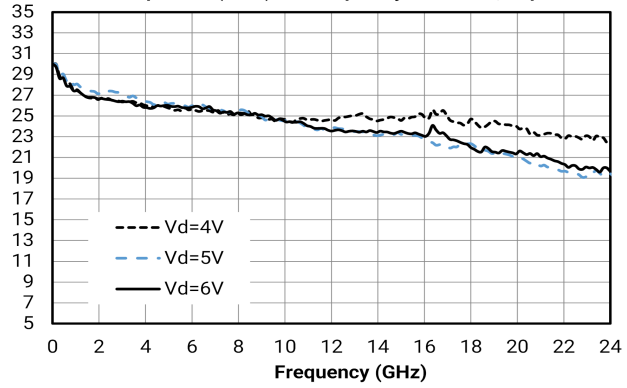
Noise Figure (dB) vs. Freq and Temp, Vd=5V Idq= 40mA



Input IP3 (dBm) vs. Frequency Over Bias, Idq= 40mA



Output IP3 (dBm) vs. Frequency Over Bias, Idq= 40mA



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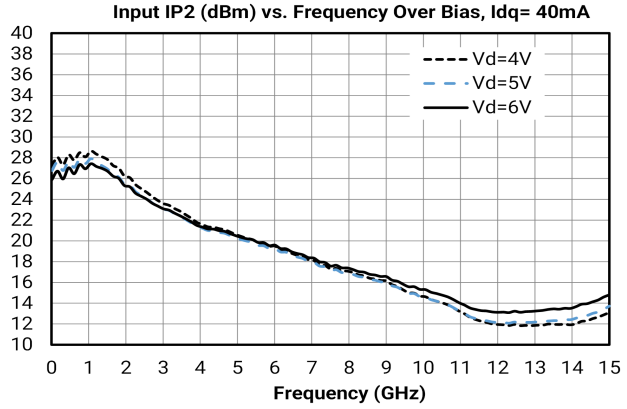
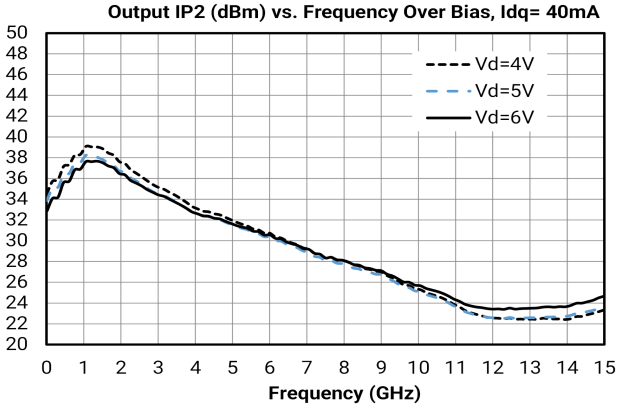
Evaluation Board, 0.2 - 20 GHz High Dynamic Range Gain Block Amplifier

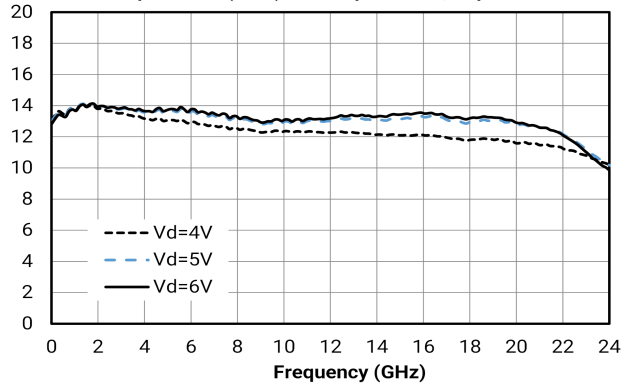
Input IP2 (dBm) vs. Frequency Over Bias, Idq= 40mA



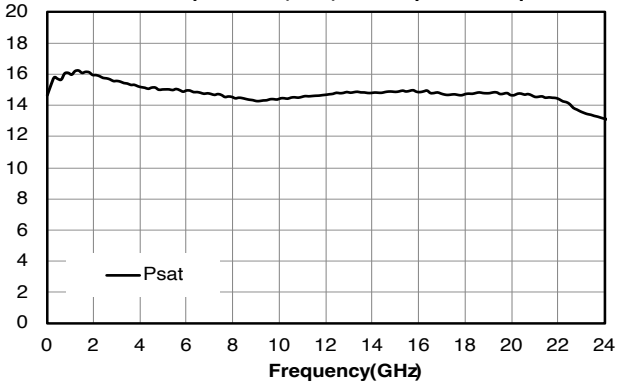
Output IP2 (dBm) vs. Frequency Over Bias, Idq= 40mA



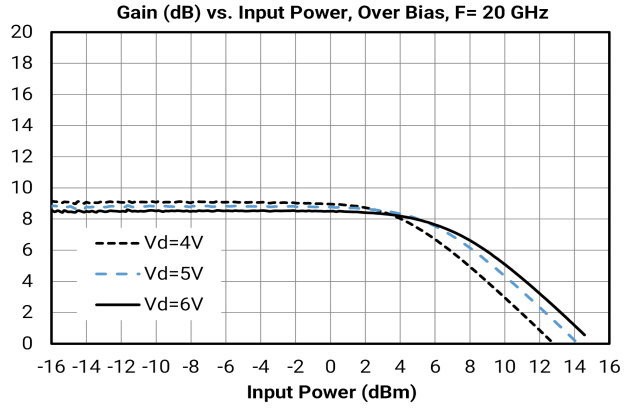
Output P1dB (dBm) vs. Freq and Bias, Idq=40mA



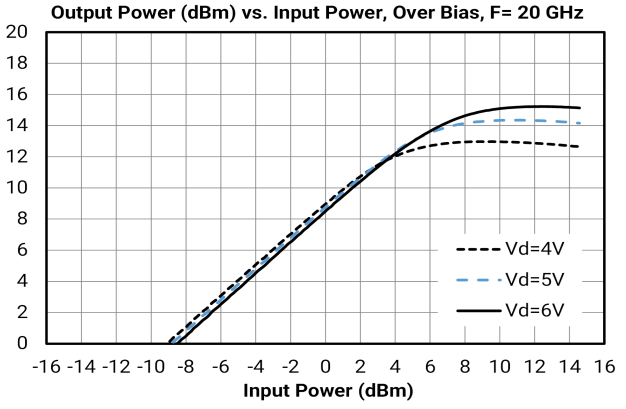
Saturated Output Power(dBm) vs. Freq, Vd=5V Idq=40mA



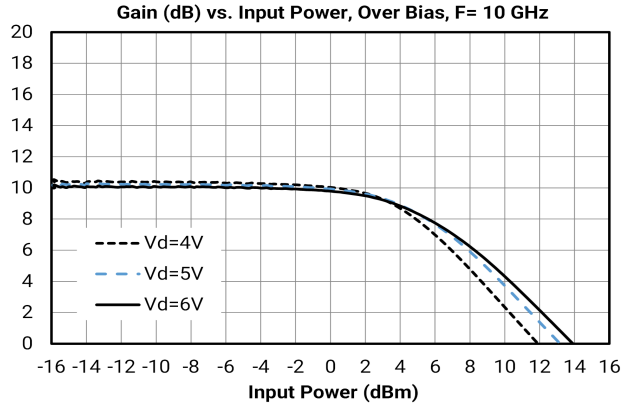
Gain (dB) vs. Input Power, Over Bias, F= 20 GHz



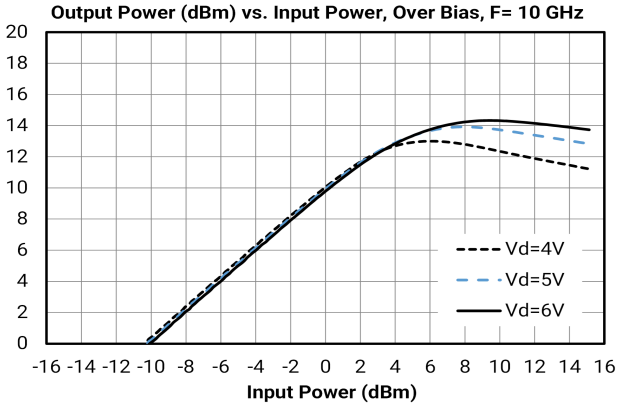
Output Power (dBm) vs. Input Power, Over Bias, F= 20 GHz



Gain (dB) vs. Input Power, Over Bias, F= 10 GHz



Output Power (dBm) vs. Input Power, Over Bias, F= 10 GHz



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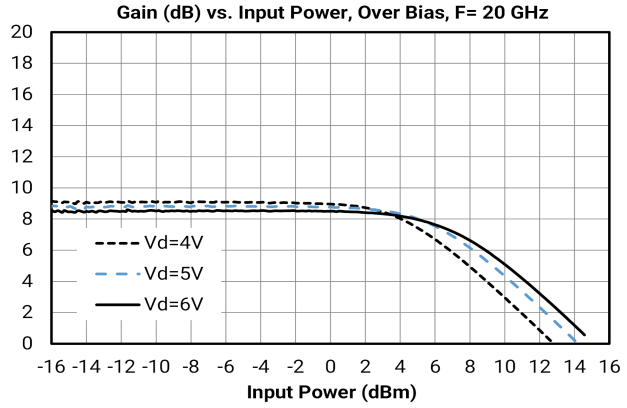
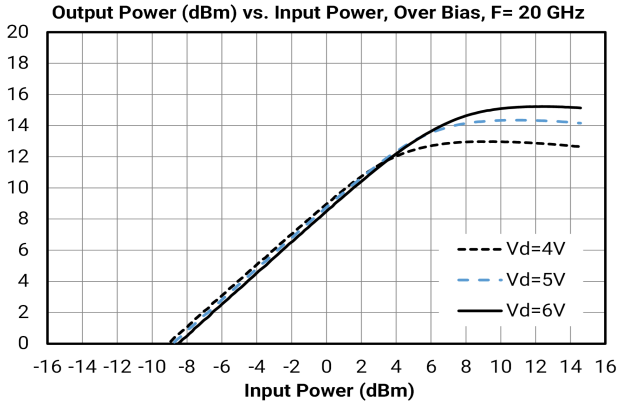
Evaluation Board, 0.2 - 20 GHz High Dynamic Range Gain Block Amplifier

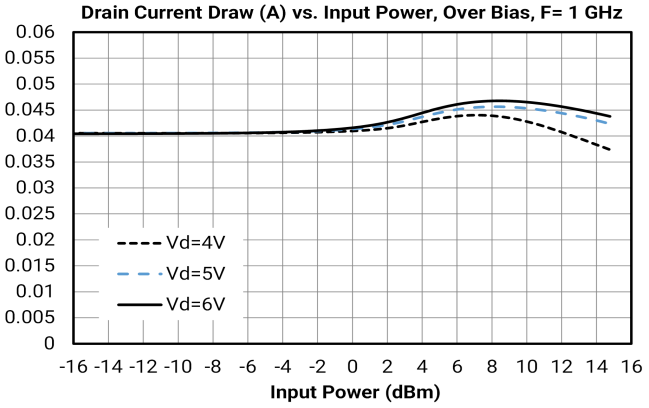
Gain (dB) vs. Input Power, Over Bias, F= 20 GHz



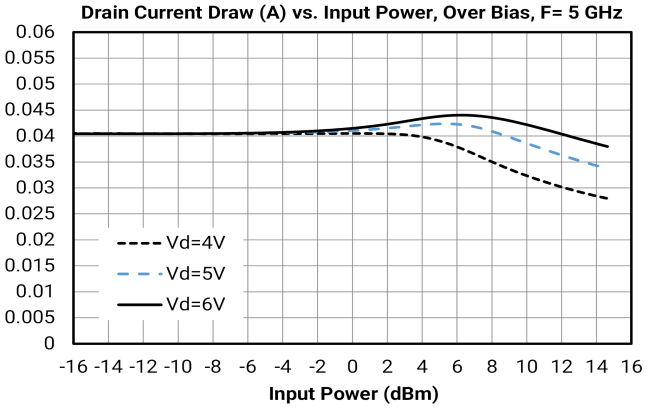
Output Power (dBm) vs. Input Power, Over Bias, F= 20 GHz



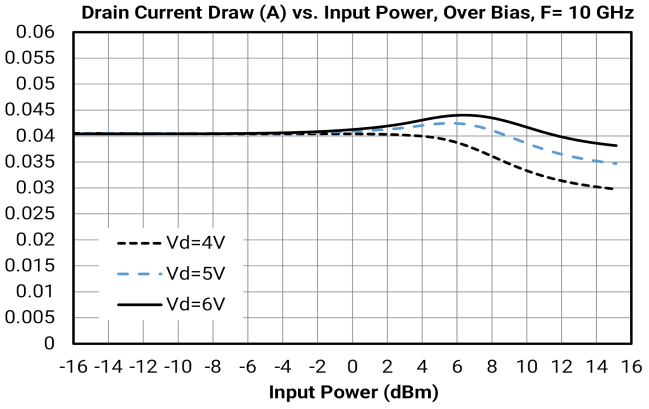
Drain Current Draw (A) vs. Input Power, Over Bias, F= 1 GHz



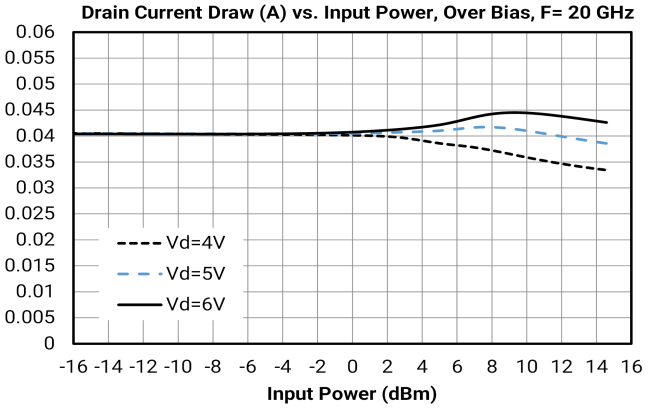
Drain Current Draw (A) vs. Input Power, Over Bias, F= 5 GHz



Drain Current Draw (A) vs. Input Power, Over Bias, F= 10 GHz

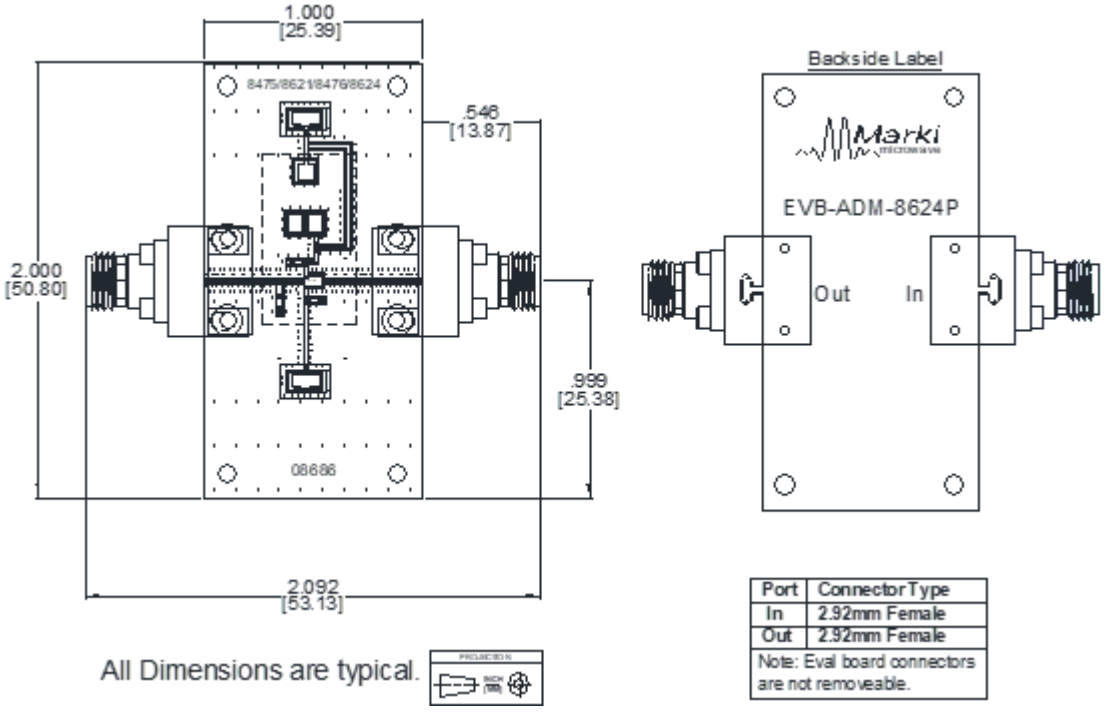


Drain Current Draw (A) vs. Input Power, Over Bias, F= 20 GHz



Mechanical Data

Outline Drawing



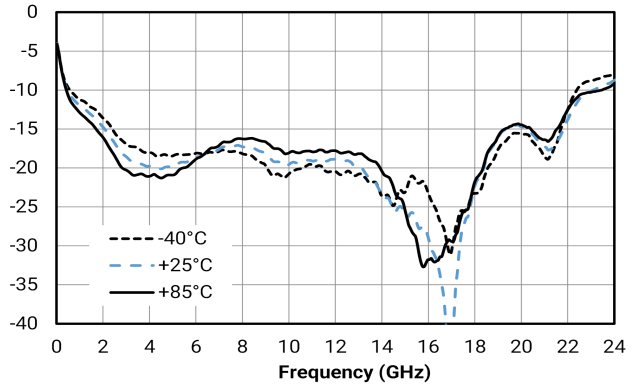
All Dimensions are typical.

Port	Connector Type
In	2.92mm Female
Out	2.92mm Female
Note: Eval board connectors are not removeable.	

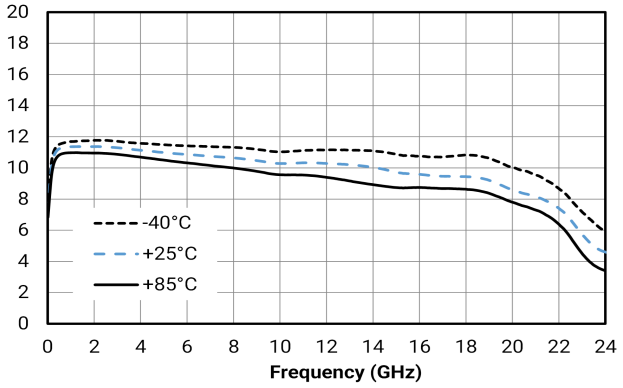
Evaluation Board - Typical Performance Plots

Measurements are taken at the evaluation board connectors.

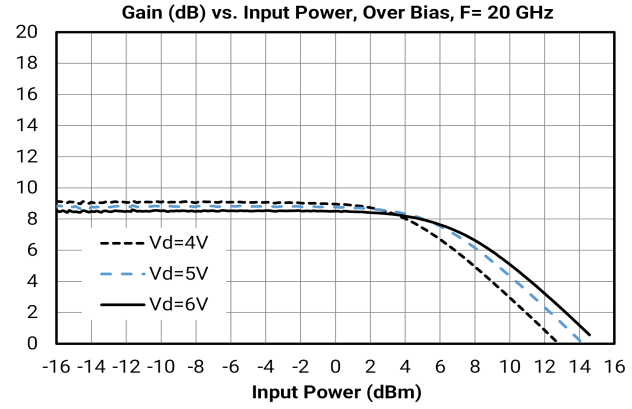
Input Return Loss (dB) vs. Freq and Temp, Vd=5V, Idq= 40mA



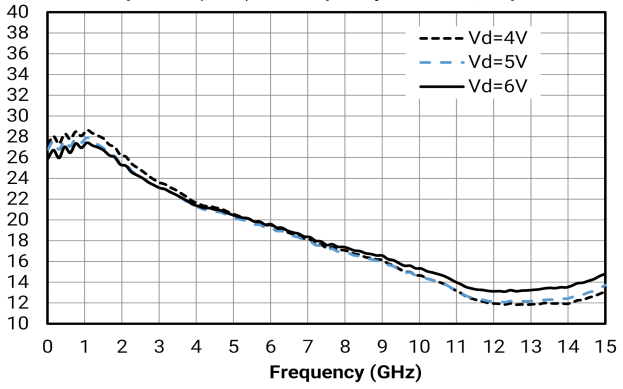
Small Signal Gain (dB) vs. Freq and Temp, Vd=5V, Idq= 40mA



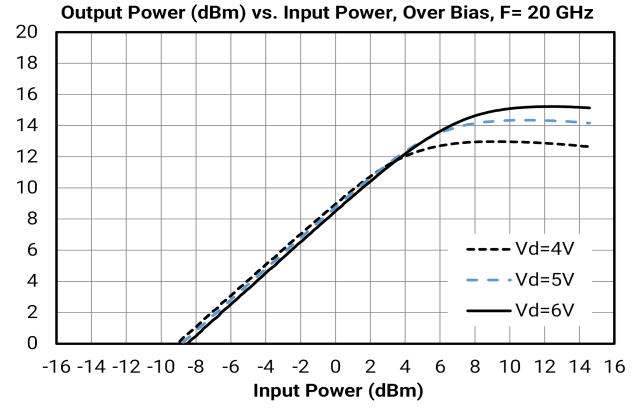
Gain (dB) vs. Input Power, Over Bias, F= 20 GHz



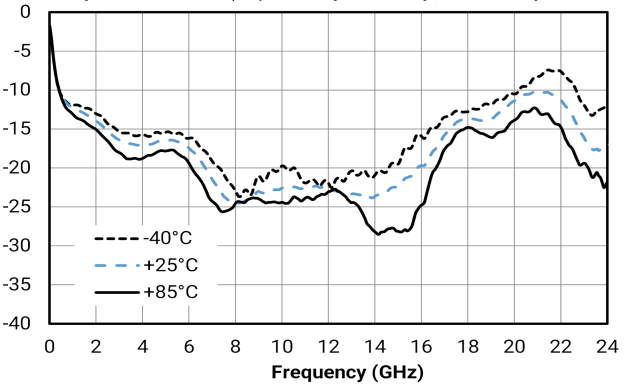
Input IP2 (dBm) vs. Frequency Over Bias, Idq= 40mA



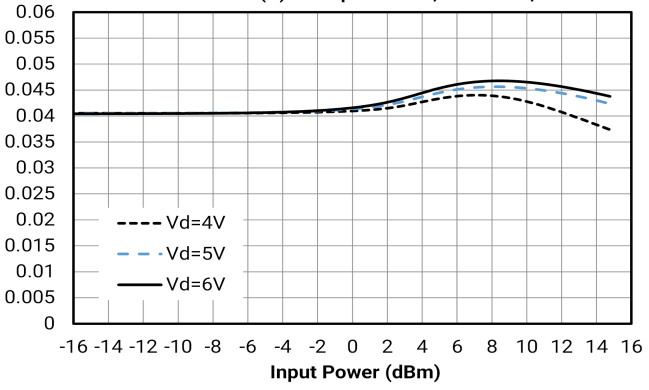
Output Power (dBm) vs. Input Power, Over Bias, F= 20 GHz



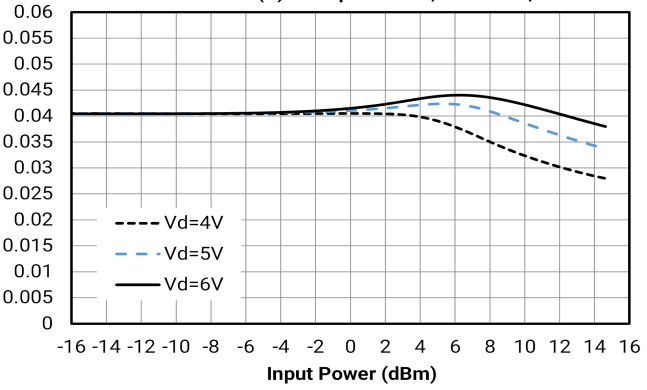
Output Return Loss(dB) vs Freq and Temp,Vd=5V, Idq=40mA



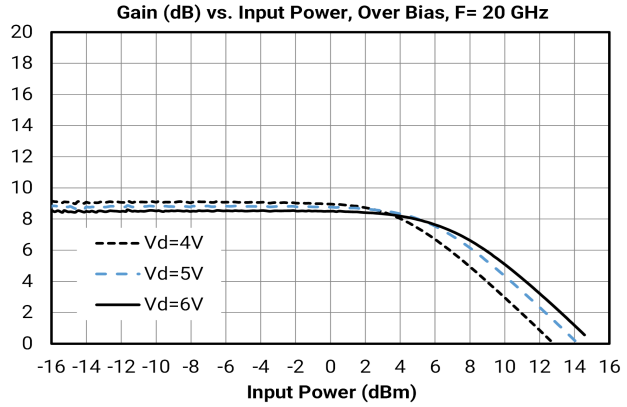
Drain Current Draw (A) vs. Input Power, Over Bias, F= 1 GHz



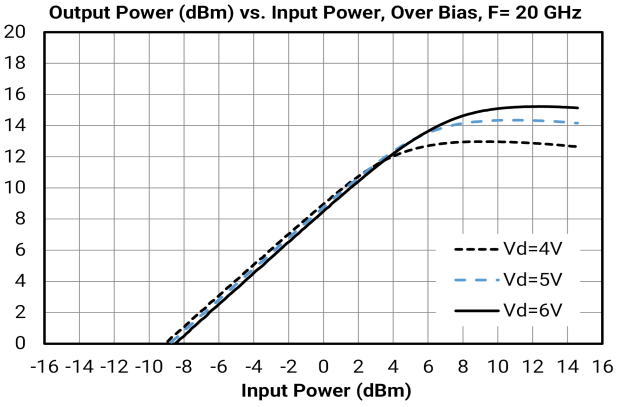
Drain Current Draw (A) vs. Input Power, Over Bias, F= 5 GHz



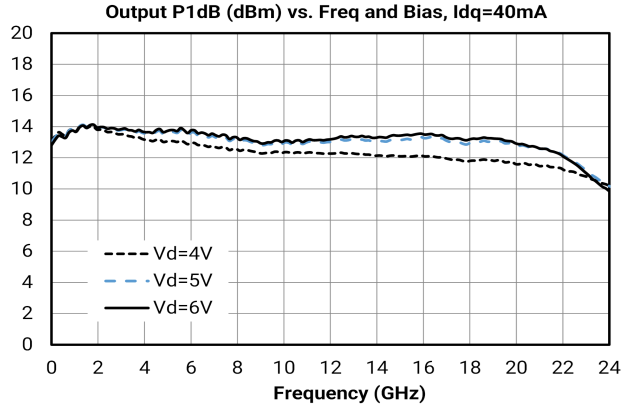
Gain (dB) vs. Input Power, Over Bias, F= 20 GHz



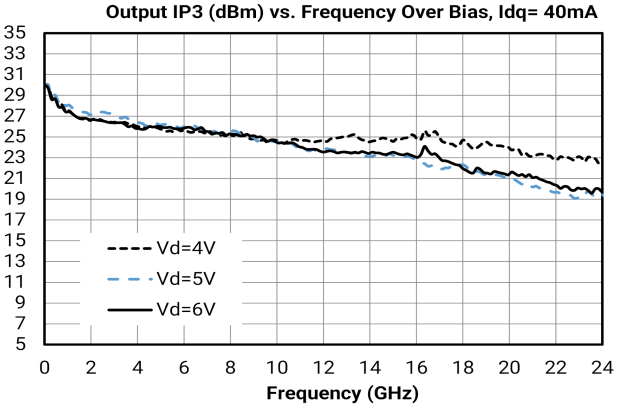
Output Power (dBm) vs. Input Power, Over Bias, F= 20 GHz



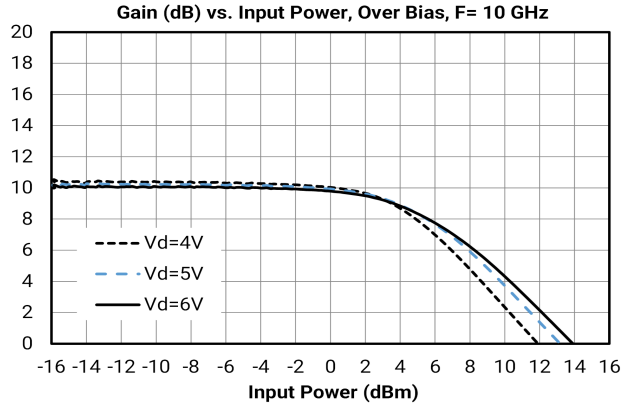
Output P1dB (dBm) vs. Freq and Bias, Idq=40mA



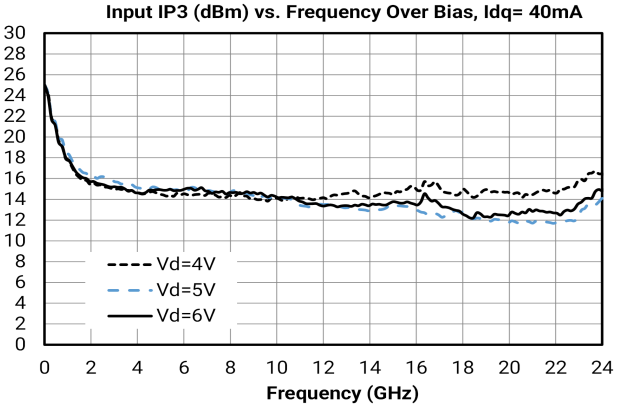
Output IP3 (dBm) vs. Frequency Over Bias, Idq= 40mA



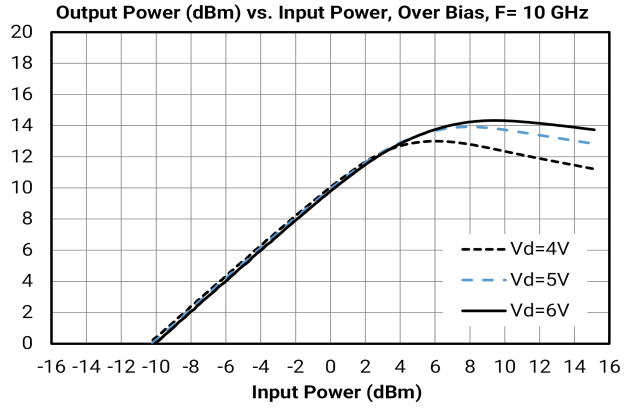
Gain (dB) vs. Input Power, Over Bias, F= 10 GHz



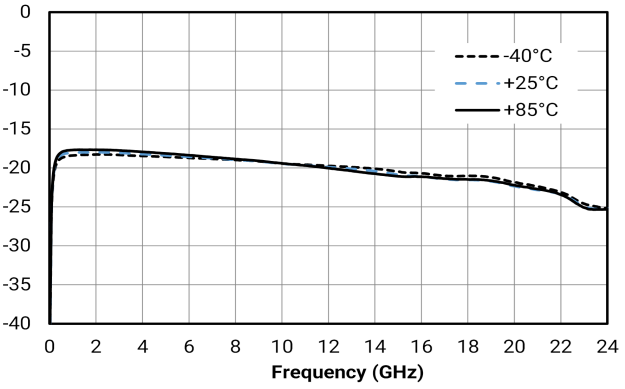
Input IP3 (dBm) vs. Frequency Over Bias, Idq= 40mA



Output Power (dBm) vs. Input Power, Over Bias, F= 10 GHz



Reverse Isolation (dB) vs. Freq and Temp, Vd=5V, Idq= 40mA



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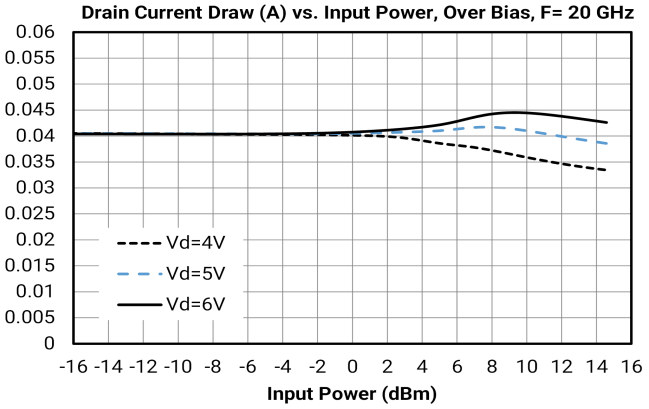
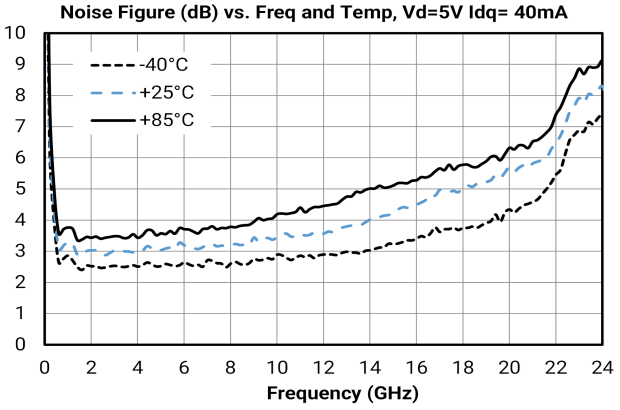
Evaluation Board, 0.2 - 20 GHz High Dynamic Range Gain Block Amplifier

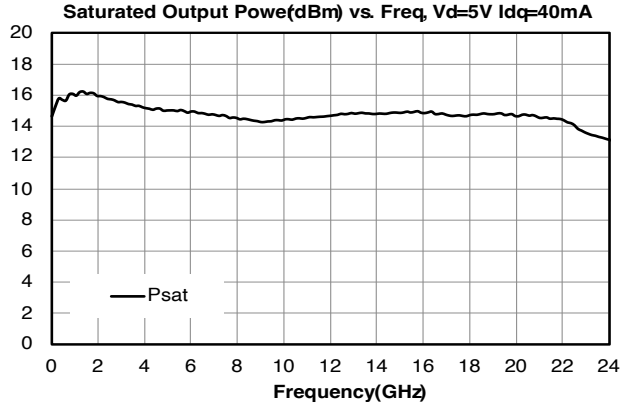
Drain Current Draw (A) vs. Input Power, Over Bias, F= 20 GHz



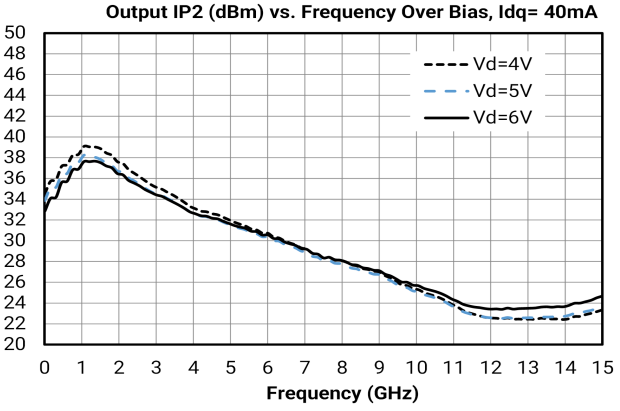
Noise Figure (dB) vs. Freq and Temp, Vd=5V Idq= 40mA



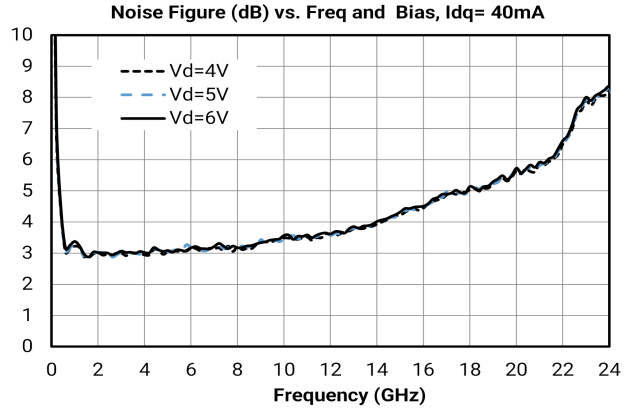
Saturated Output Powe(dBm) vs. Freq, Vd=5V Idq=40mA



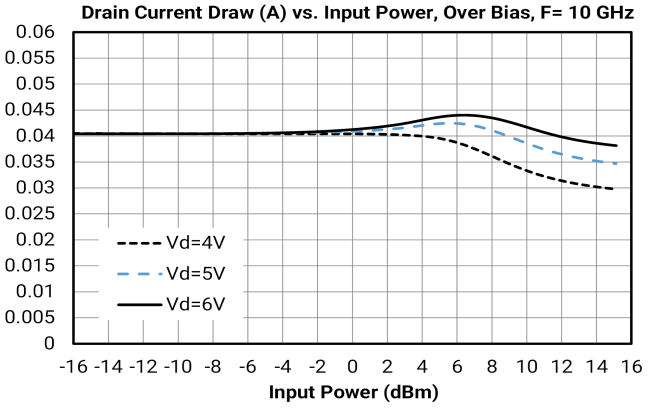
Output IP2 (dBm) vs. Frequency Over Bias, Idq= 40mA



Noise Figure (dB) vs. Freq and Bias, Idq= 40mA



Drain Current Draw (A) vs. Input Power, Over Bias, F= 10 GHz



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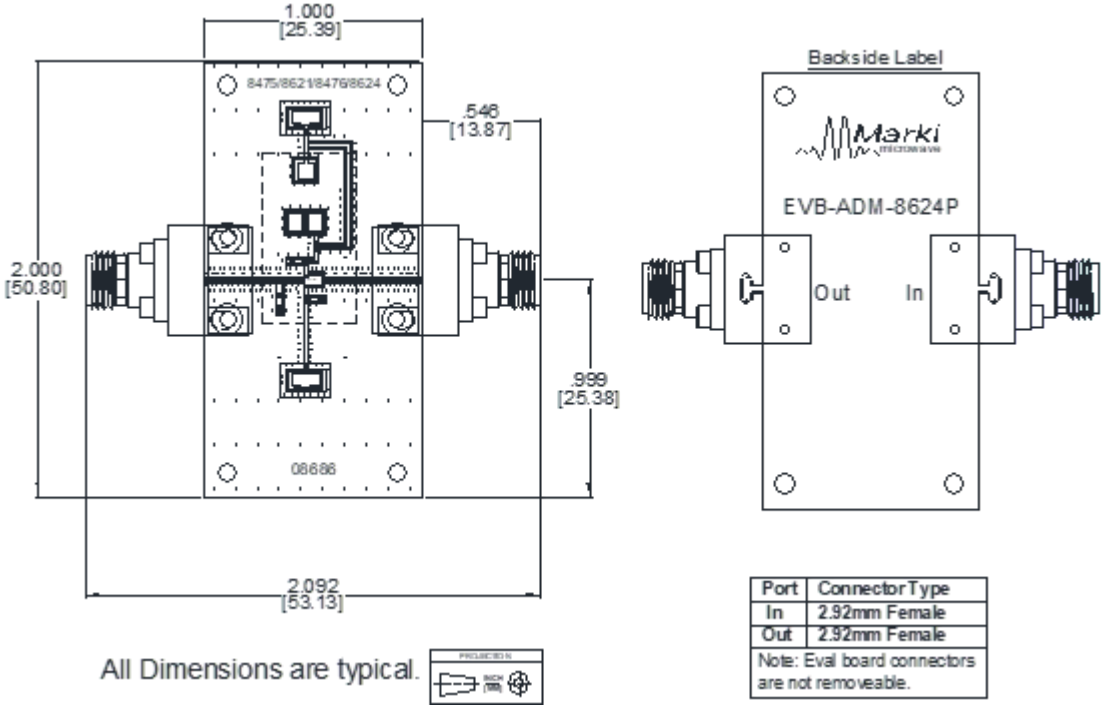
Evaluation Board, 0.2 - 20 GHz High Dynamic Range Gain Block Amplifier

Evaluation Board - Outline Drawing



All Dimensions are typical.

Port	Connector Type
In	2.92mm Female
Out	2.92mm Female
Note: Eval board connectors are not removeable.	

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