

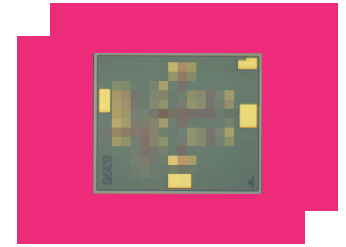
MM1-30100LCH-2

GaAs MMIC Double Balanced mmWave Mixer

DEVICE OVERVIEW

General Description

MM1-30100L is a GaAs MMIC double balanced mixer that features excellent conversion loss, superior isolations, and spurious performance across an incredibly broad bandwidth. The MM1-30100L works well as both an up and down converter from the Ka band through mmWave/G band. The MM1-30100L is recommended for mmWave frequency conversion applications that require high linearity. It is available as both wire bondable die and as a connectorized module.



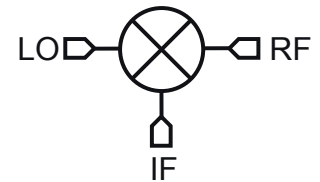
Features

- Ultra broadband, high linearity mmWave frequency conversion
- High LO to RF isolation
- Up or down conversion

Applications

- mmWave Frequency Conversion
- mmWave Tuner Mixer
- Test and Measurement Equipment
- Automotive Radar
- 5G Test Receivers

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Connectors	Green Status	Product Lifecycle	Export Classification
<u>MM1-30100LM</u>	GaAs MMIC Double Balanced mmWave Mixer	M	<u>Standard</u>	REACH RoHS	Released	EAR99
MM1-30100LCH-2	GaAs MMIC Double Balanced mmWave Mixer	CH	-	REACH RoHS	Released	EAR99

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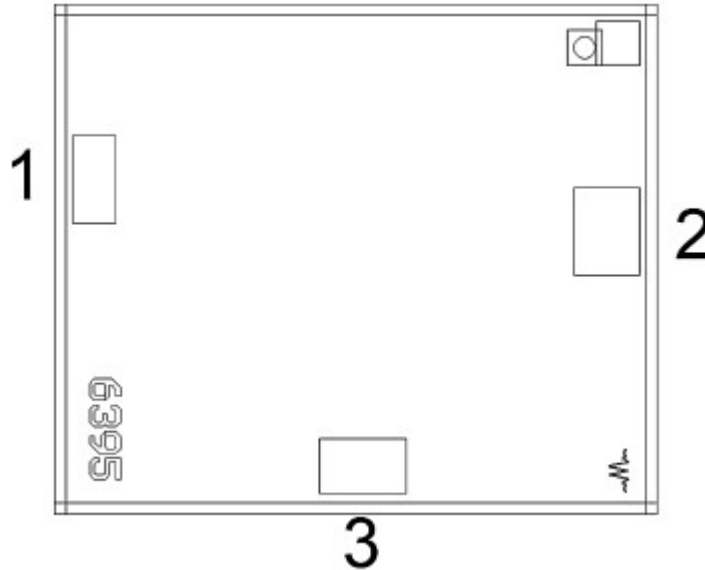
Revision History

Revision Code	Revision Date	Comment
-	2022-05-01	Datasheet Initial Release

Port Configuration and Functions

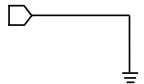

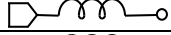
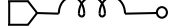
Port Diagram

A top-down view of the MM1-30100L's CH package outline drawing is shown below. The MM1-30100L has the input and output ports given in Port Functions. The MM1-30100L can be used in either an up or down conversion. For configuration A, input the LO into port 3, use port 1 for the RF, and port 2 for the IF. For configuration B, input the LO into port 1, use port 3 for the RF, and port 2 for the IF.

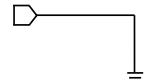
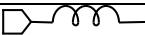




Port Functions

Configuration A

Port	Function	Description	Equivalent Circuit for Package
GND	Ground	CH package ground path is provided through the substrate and ground bond pads.	
Port 1	RF	Port 1 is DC open for the CH and M packages.	
Port 2	IF	Port 2 is DC open for the CH and M packages.	
Port 3	LO	Port 3 is DC open for the CH and M packages.	

Configuration B

Port	Function	Description	Equivalent Circuit for Package
GND	Ground	CH package ground path is provided through the substrate and ground bond pads.	
Port 1	LO	Port 1 is DC open for the CH and M packages.	
Port 2	IF	Port 2 is diode connected for the CH and M package.	
Port 3	RF	Port 3 is DC open for the CH and M packages.	

Specifications

Absolute Maximum Ratings

The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications. Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the electrical specifications. For limits, above which damage may occur, see Absolute Maximum Ratings.

Parameter	Maximum Rating	Unit
Maximum Operating Temperature	100	°C
Maximum Storage Temperature	125	°C
Minimum Operating Temperature	-55	°C
Minimum Storage Temperature	-65	°C
Power Handling, at any Port	30	dBm

Package Information

Parameter	Details	Rating
ESD	250 to < 500 Volts	HBM Class 1A
Dimensions	-	1.38 x 1.17 mm

Recommended Operating Conditions

The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications. Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the electrical specifications. For limits, above which damage may occur, see Absolute Maximum Ratings.

Parameter	Min	Nominal	Max	Unit
LO Input Power	14	16	-	dBm
Ambient Temperature	-55	25	100	°C

Sequencing Requirements

There is no requirement to apply power to the ports in a specific order. However, it is recommended to provide a 50Ω termination to each port before applying power. This is a passive diode mixer that requires no DC bias.

Electrical Specifications

The electrical specifications apply at TA=+25°C in a 50Ω system. Typical data shown is for the connectorized M package mixer used in the forward direction with a sine wave LO input. Specifications shown for configuration A. Min and Max limits apply only to our connectorized units and are guaranteed at TA=+25°C. All bare die are 100% DC tested and visually inspected.

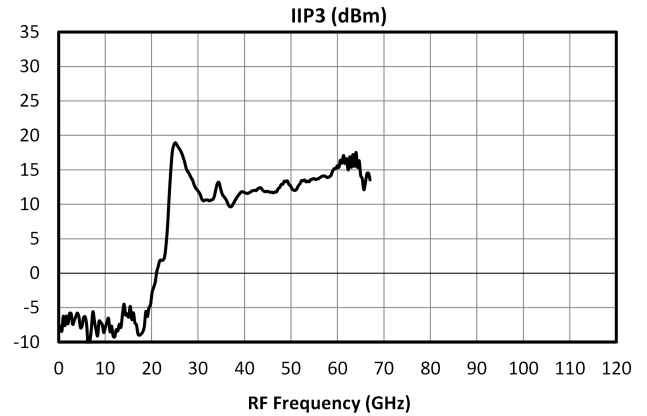
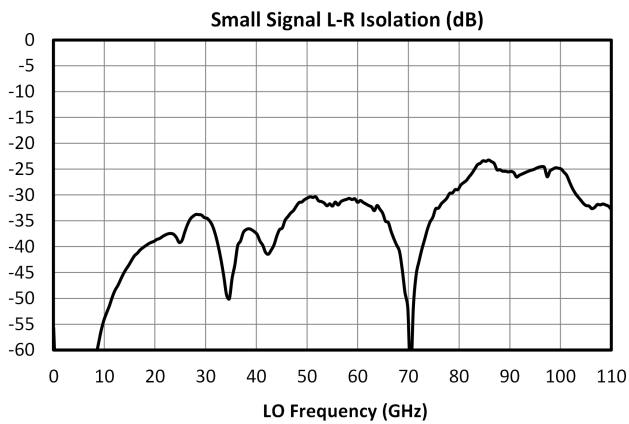
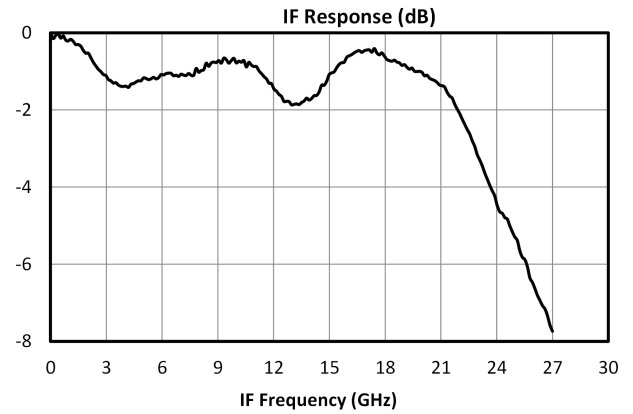
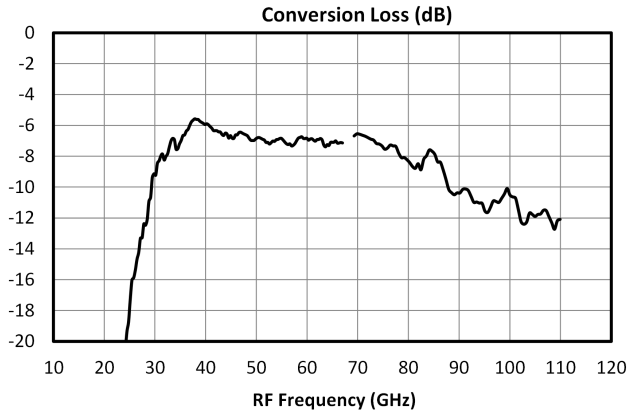
Parameter	Port Configuration	Test Conditions	Min	Typ	Max	Unit
Conversion Loss ¹	-	RF/LO = 30 - 130 GHz I = DC - 0.2 GHz	-	8.5	-	dB
IF Frequency Range	-	-	0	-	20	GHz
LO Frequency Range	-	-	30	-	100	GHz
Noise Figure ²	-	RF/LO = 30 - 130 GHz I = DC - 0.2 GHz	-	8.5	-	dB
RF Frequency Range	-	-	30	-	100	GHz

^[1] Measured as a down converter to a fixed 91MHz IF.

^[2] Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.

Typical Performance Plots

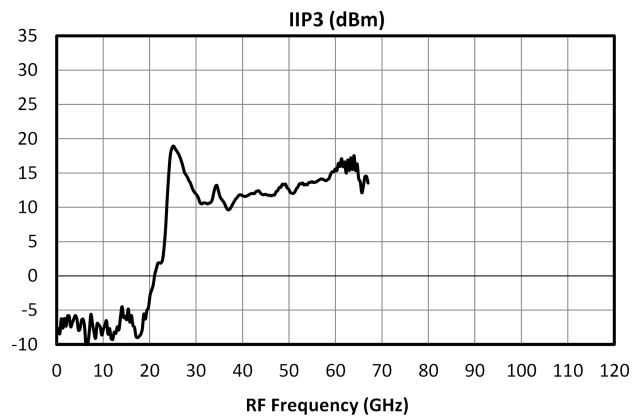
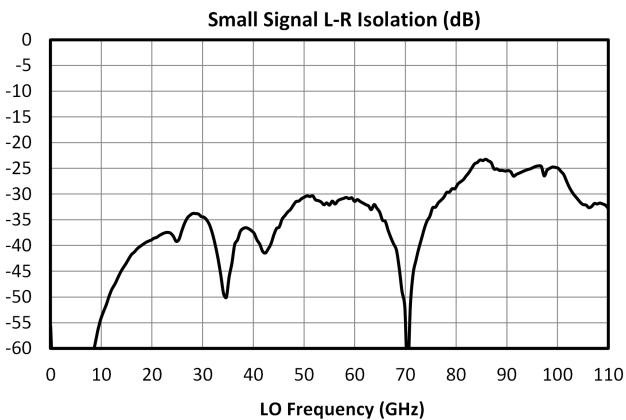
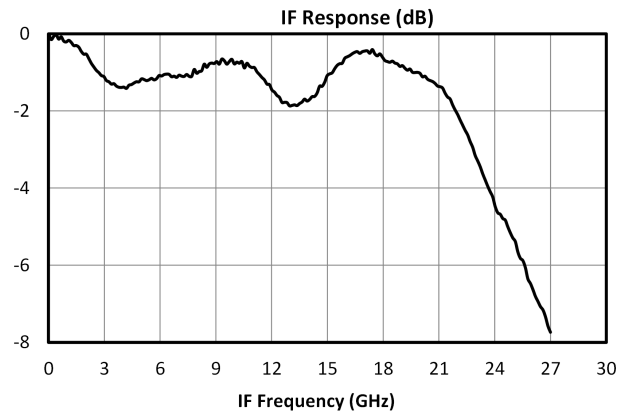
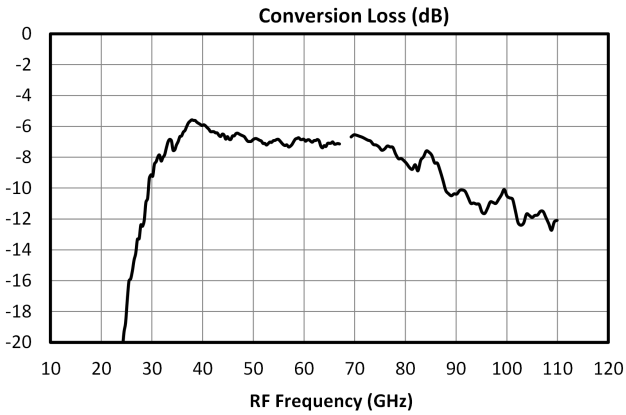
Due to difficulty generating strong LO signals at mmWave frequencies LR Isolation is measured with a small signal LO. Isolation will improve with a stronger LO signal. Discontinuity in conversion loss measurement is due to splitting the measurement into multiple bands. IIP3 is only measured up to 67GHz due to limitations in measurement setup.



MM1-30100LM - Typical Performance Plots

Due to difficulty generating strong LO signals at mmWave frequencies LR Isolation is measured with a small signal LO. Isolation will improve with a stronger LO signal. Discontinuity in conversion loss measurement is due to splitting the measurement into multiple bands. IIP3 is only measured up to 67GHz due to limitations in measurement setup.

Performance plots for the connectorized module are shown for measurements where directly probed measurements of the die are unavailable. Note that the following measurements include losses from connectors and microstrip traces.



Die Mounting Recommendations

Mounting and Bonding Recommendations

Marki MMICs should be attached directly to a ground plane with conductive epoxy. The ground plane electrical impedance should be as low as practically possible. This will prevent resonances and permit the best possible electrical performance. Datasheet performance is only guaranteed in an environment with a low electrical impedance ground.

Mounting - To epoxy the chip, apply a minimum amount of conductive epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip. Cure epoxy according to manufacturer instructions.

Wire Bonding - Ball or wedge bond with 0.025 mm (1 mil) diameter pure gold wire. Thermosonic wirebonding with a nominal stage temperature of 150 °C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Use the minimum level of ultrasonic energy to achieve reliable wirebonds. Wirebonds should be started on the chip and terminated on the package or substrate. All bonds should be as short as possible <0.31 mm (12 mils).

Circuit Considerations – 50 Ω transmission lines should be used for all high frequency connections in and out of the chip. Wirebonds should be kept as short as possible, with multiple wirebonds recommended for higher frequency connections to reduce parasitic inductance. In circumstances where the chip more than .001” thinner than the substrate, a heat spreading spacer tab is optional to further reduce bondwire length and parasitic inductance.

Handling Precautions

General Handling

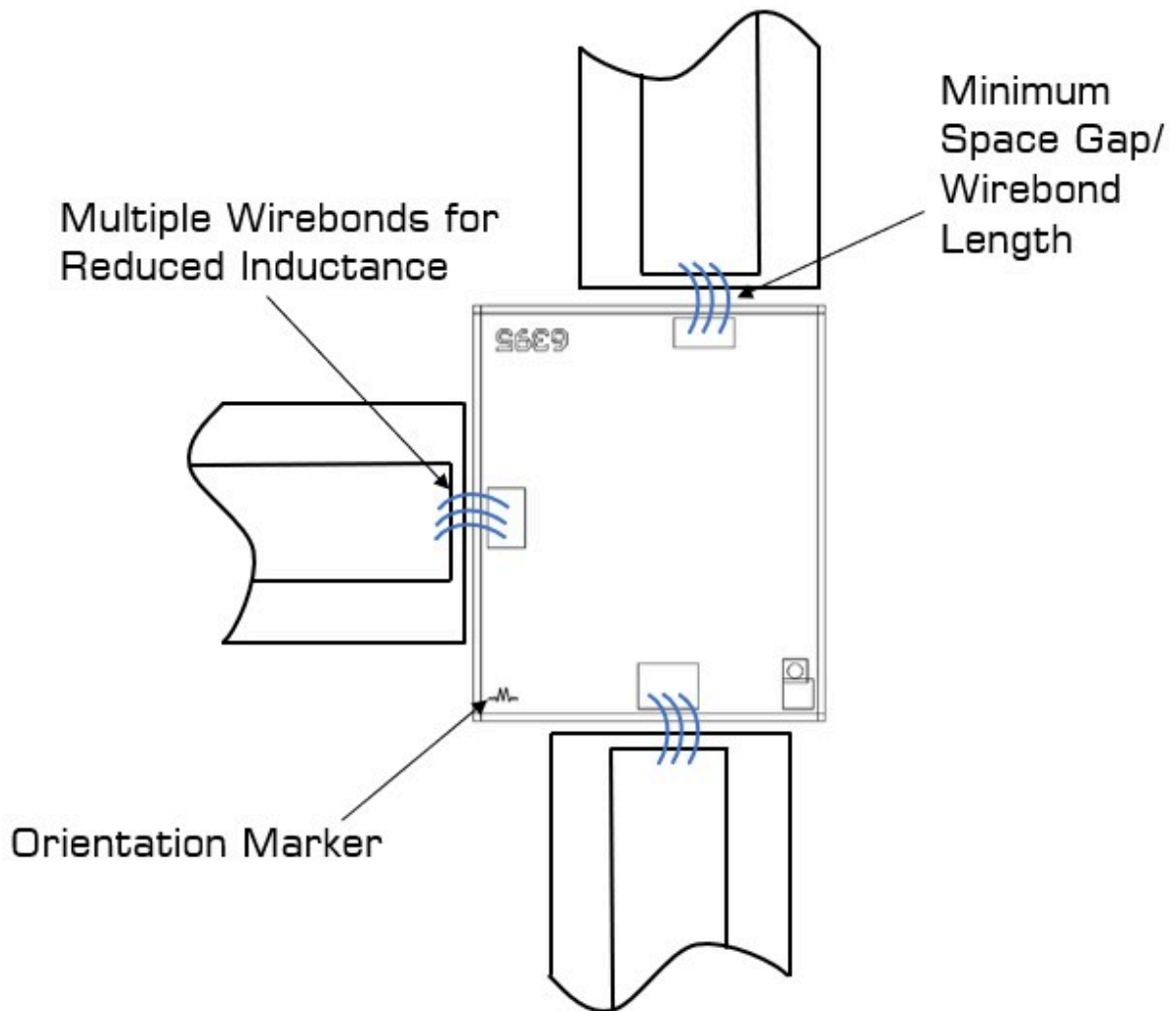
Chips should be handled with care using tweezers or a vacuum collet. Users should take precautions to protect chips from direct human contact that can deposit contaminants, like perspiration and skin oils on any of the chip's surfaces.

Static Sensitivity

GaAs MMIC devices are sensitive to ESD and should be handled, assembled, tested, and transported only in static protected environments.

Cleaning and Storage: Do not attempt to clean the chip with a liquid cleaning system or expose the bare chips to liquid. Once the ESD sensitive bags the chips are stored in are opened, chips should be stored in a dry nitrogen atmosphere.

Bonding Diagram



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