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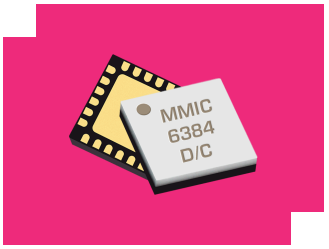
MM1-0212SSM-2

GaAs MMIC Double Balanced Mixer

DEVICE OVERVIEW

General Description

The MM1-0212SSM is a highly linear GaAs MMIC double balanced mixer. MM1-0212SSM is a low frequency, high linearity S band mixer that works well as both an up and down converter through X band. This mixer offers low conversion loss and high LO to RF isolations at the nominal LO drive. The sister MM1-0212HSM and MM1-0212LSM are recommended for applications which need LO operation at lower powers. The MM1-0212SSM is available in a 4X4 mm QFN package. Evaluation boards are available.



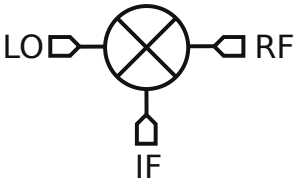
Features

- High nominal +28 dBm IIP3
- High LO to RF isolation

Applications

- Test and Measurement Equipment

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Packing Size	Green Status	Product Lifecycle	Export Classification
MM1-0212SSM-2	GaAs MMIC Double Balanced Mixer	QFN	-	REACH RoHS	Released	EAR99
EVAL-MM1-0212S	Evaluation Board, GaAs MMIC Double Balanced Mixer	EVAL	-	REACH RoHS	Released	EAR99
MM1-0212S-2-TR	Tape and Reel, GaAs MMIC Double Balanced Mixer	QFN	7"	REACH RoHS	Released	EAR99

Table Of Contents

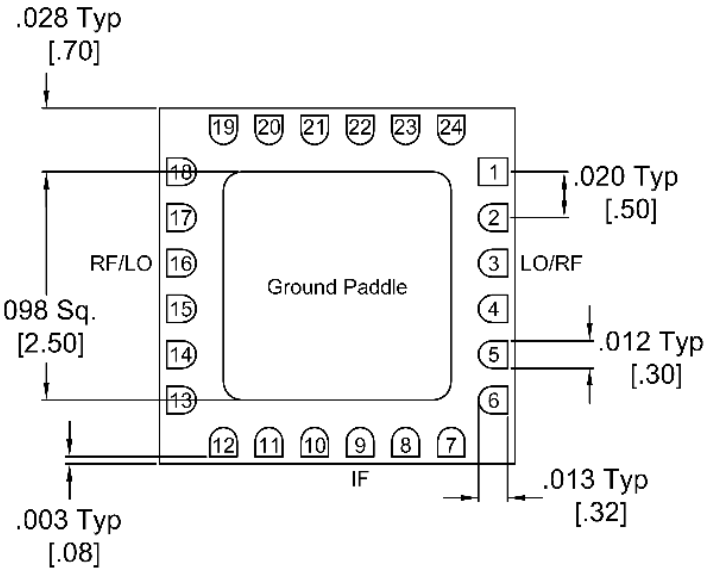
Revision History

Revision Code	Revision Date	Comment
-	2018-07-01	Datasheet Initial Release
A	2018-11-01	Section 5.1, Note 2
B	2019-01-01	Added max power/current spec, ESD rating

Port Configuration and Functions

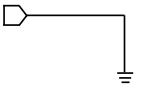
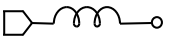
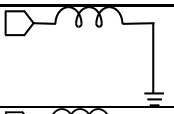
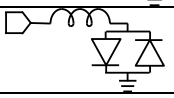
Port Diagram

A bottom-up view of the MM1-0212SSM's SM package outline drawing is shown below. The MM1-0212SSM has the input and output ports given in Port Functions. The MM1-0212SSM can be used in either an up or down conversion. For configuration A, input the LO into Pin 3, use pin 16 for the RF, and pin 9 for the IF. For configuration B, input the LO into pin 16, use pin 3 for the RF, and pin 9 for the IF.

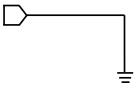
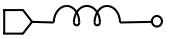
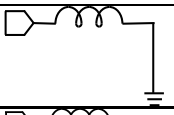
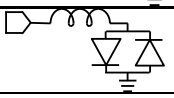


Port Functions

Configuration A

Port	Function	Description	Equivalent Circuit for Package
GND	Ground	SM package ground path is provided through the ground paddle.	
Pin 16	RF	Pin 16 is DC open and AC matched to 50 Ohms from 2 to 12 GHz. Blocking capacitor is optional.	
Pin 3	LO	Pin 3 is DC short and AC matched to 50 Ohms from 2 to 12 GHz. Blocking capacitor is optional.	
Pin 9	IF	Pin 9 is DC coupled to the diodes. Blocking capacitor is optional.	

Configuration B

Port	Function	Description	Equivalent Circuit for Package
GND	Ground	SM package ground path is provided through the ground paddle.	
Pin 16	LO	Pin 16 is DC open and AC matched to 50 Ohms from 2 to 12 GHz. Blocking capacitor is optional.	
Pin 3	RF	Pin 3 is DC short and AC matched to 50 Ohms from 2 to 12 GHz. Blocking capacitor is optional.	
Pin 9	IF	Pin 9 is DC coupled to the diodes. Blocking capacitor is optional.	

Specifications

Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded, the device may be inoperable or have a reduced lifetime.

Parameter	Maximum Rating	Unit
Maximum Operating Temperature	100	°C
Maximum Storage Temperature	125	°C
Minimum Operating Temperature	-55	°C
Minimum Storage Temperature	-65	°C
Pin 3 DC Current	30	mA
Pin 9 DC Current	30	mA
Power Handling, at any Port	30	dBm

Package Information

Parameter	Details	Rating
ESD	250 to < 500 Volts	HBM Class 1A
Dimensions	-	4 x 4 mm
Moisture Sensitivity Level	-	MSL 1

Recommended Operating Conditions

The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the electrical specifications. For limits, above which damage may occur, see Absolute Maximum Ratings.

Parameter	Min	Nominal	Max	Unit
LO Input Power	17	-	23	-
LO Input Power	17	-	23	dBm
Ambient Temperature	-55	25	100	°C

Sequencing Requirements

There is no requirement to apply power to the ports in a specific order. However, it is recommended to provide a 50Ω termination to each port before applying power. This is a passive diode mixer that requires no DC bias.

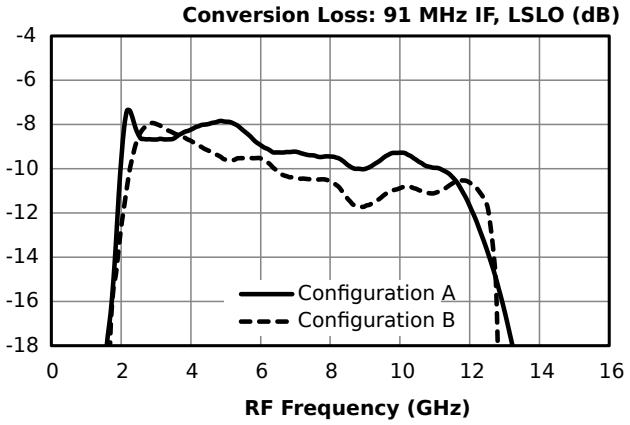
Electrical Specifications

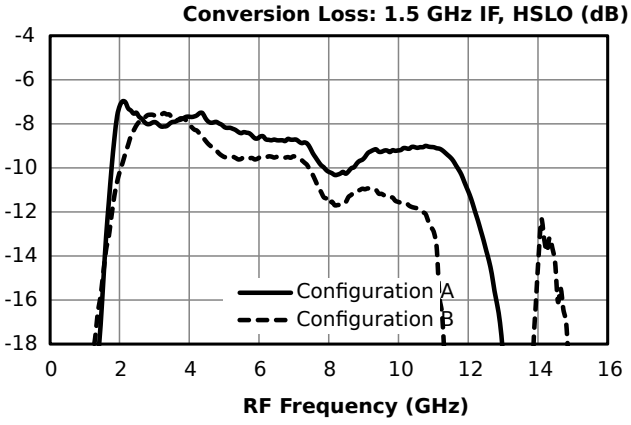
The electrical specifications apply at TA=+25°C in a 50Ω system. Typical data shown is for the connectorized S package mixer used in the forward direction with a +20 dBm sine wave input. Min and Max limits apply only to our connectorized units and are guaranteed at TA=+25°C. All bare die are 100% DC tested and visually inspected.

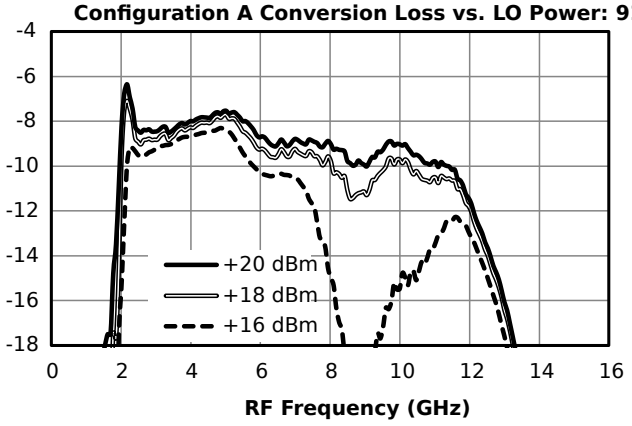
Parameter	Port Configuration	Test Conditions	Min	Typ	Max	Unit
Conversion Loss [1]	A	RF/LO = 2 - 12 GHz I = 0.2 - 3 GHz	-	9	-	dB
Conversion Loss [2]	A	RF/LO = 2 - 12 GHz I = DC - 0.2 GHz	-	9.5	12	dB
Input 1 dB Gain Compression Point (P1dB)	A	-	-	16.5	-	dBm
Input IP3	A	RF/LO = 2 - 12 GHz I = DC - 0.2 GHz	-	28	-	dBm
Conversion Loss [3]	B	RF/LO = 2 - 12 GHz I = 0.2 - 3 GHz	-	12	-	dB
Conversion Loss [4]	B	RF/LO = 2 - 12 GHz I = DC - 0.2 GHz	-	11	13	dB
Input 1 dB Gain Compression Point (P1dB)	B	-	-	18	-	dBm
Input IP3	B	RF/LO = 2 - 12 GHz I = DC - 0.2 GHz	-	29.5	-	dBm
IF Frequency Range	-	-	0	-	3	GHz
Isolation, LO to IF	-	IF/LO = 2 - 12 GHz	-	27	-	dB
Isolation, LO to RF	-	RF/LO = 2 - 12 GHz	-	52	-	dB
Isolation, RF to IF	-	RF/IF = 2 - 12 GHz	-	40	-	dB
LO Frequency Range	-	-	2	-	12	GHz
Noise Figure [5]	-	RF/LO = 2 - 12 GHz I = DC - 0.2 GHz	-	10	-	dBm
RF Frequency Range	-	-	2	-	12	GHz

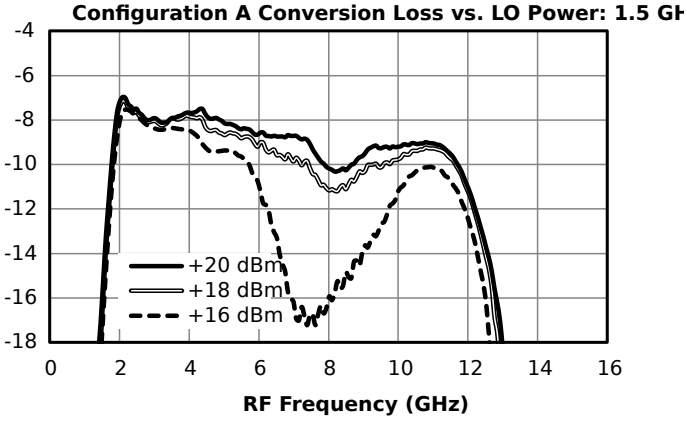
[1][2][3][4] Measured as a down converter to a fixed 91MHz IF.
[5] Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.

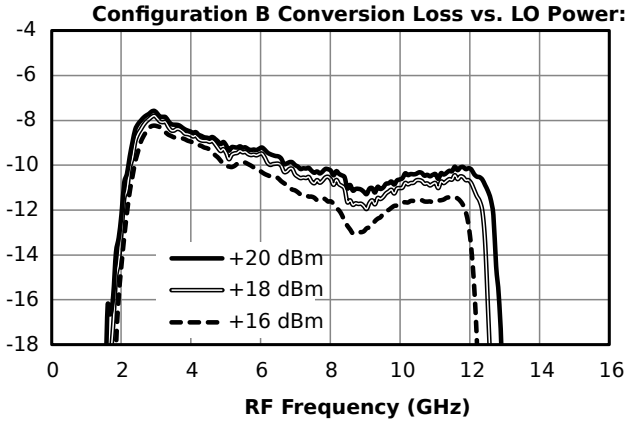
Typical Performance Plots

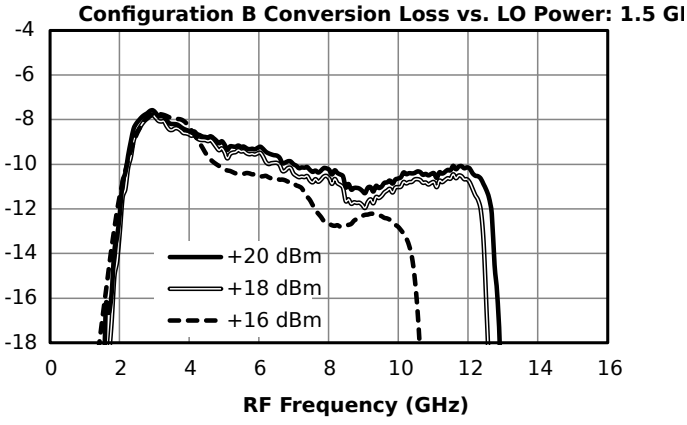


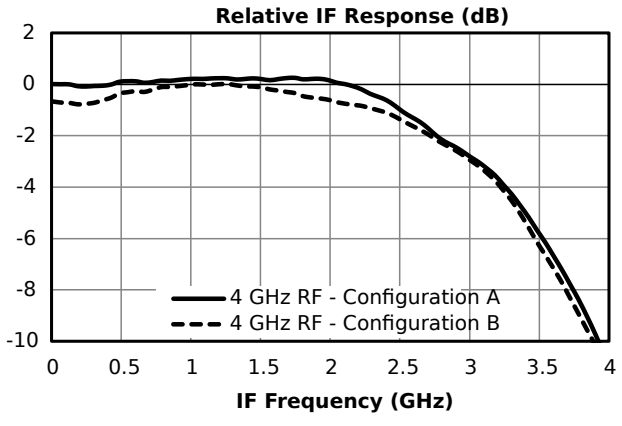


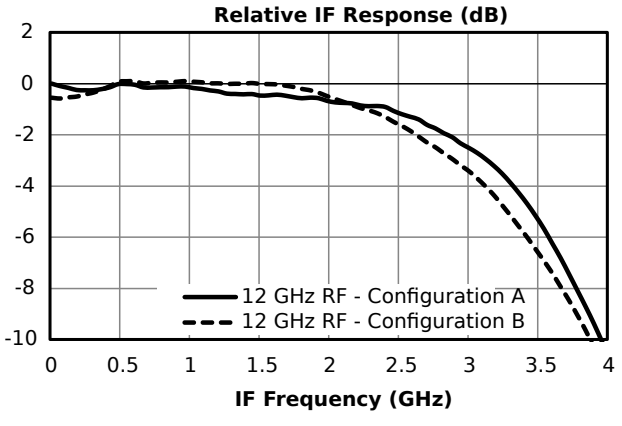


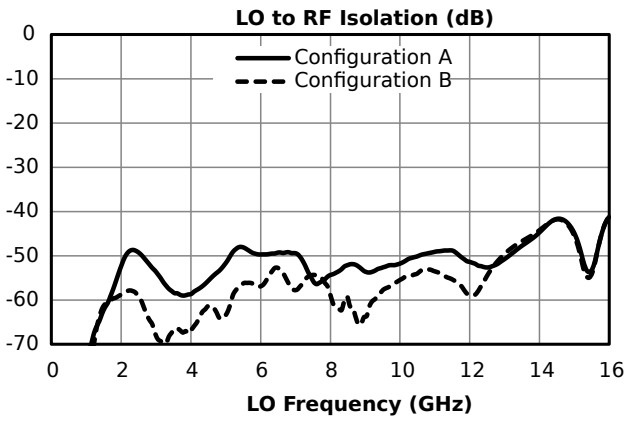


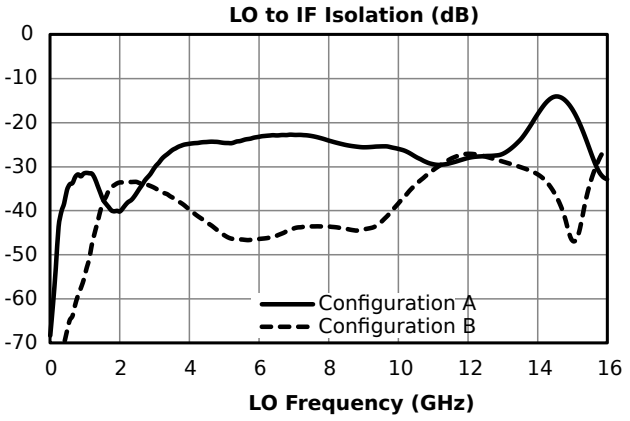


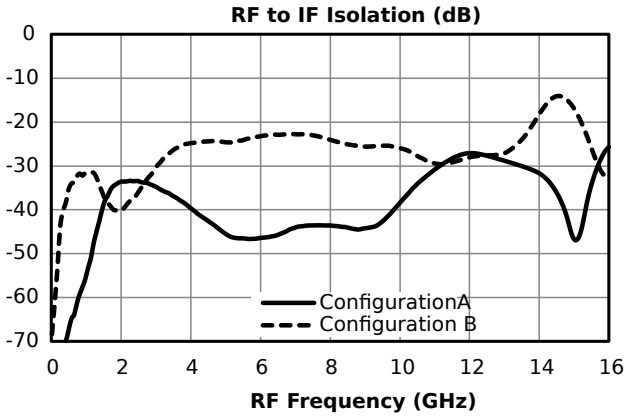


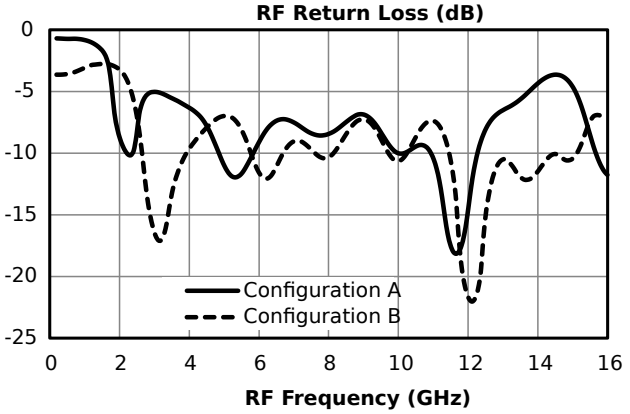


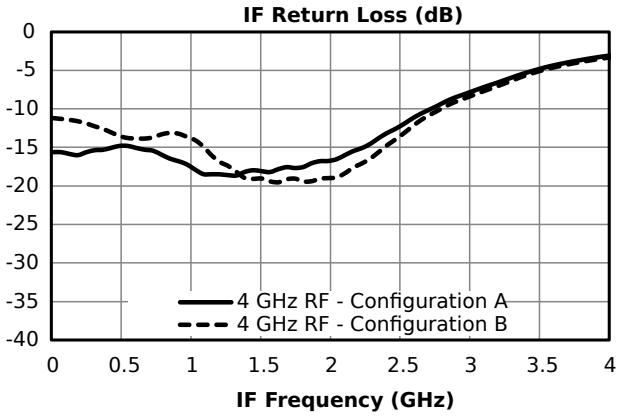


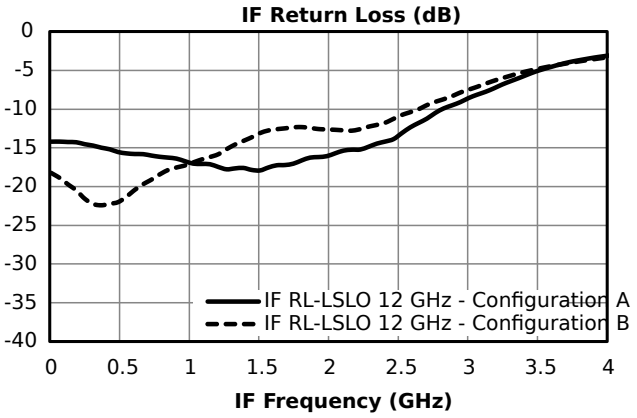


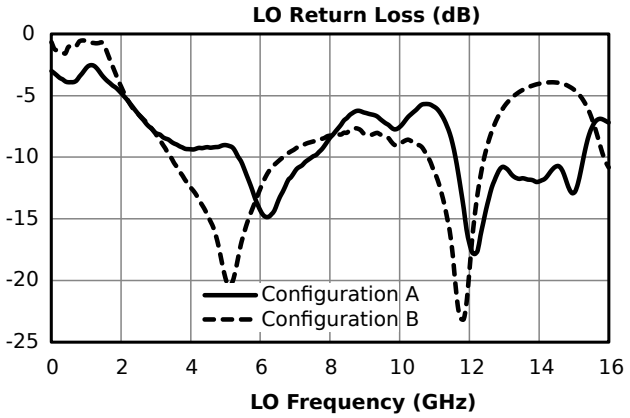




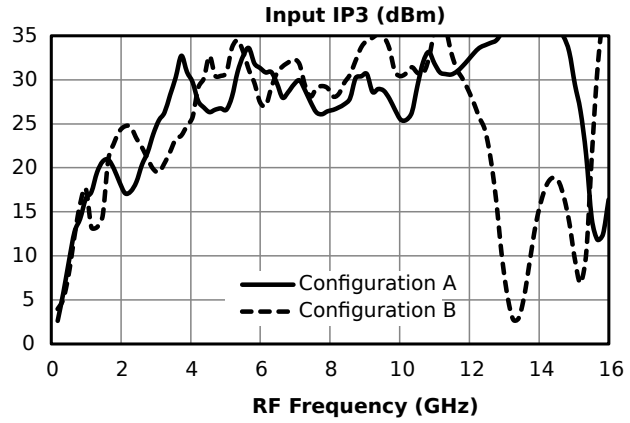


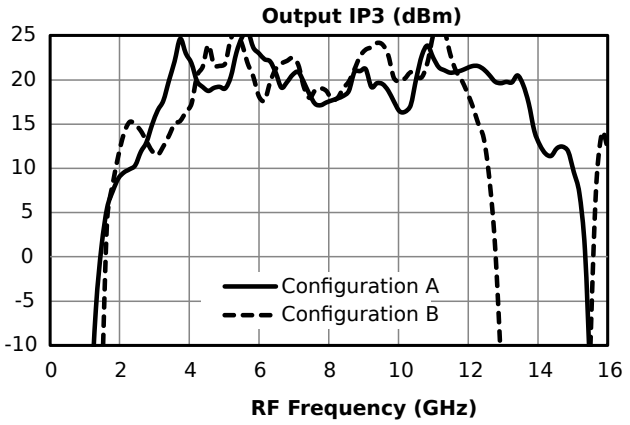


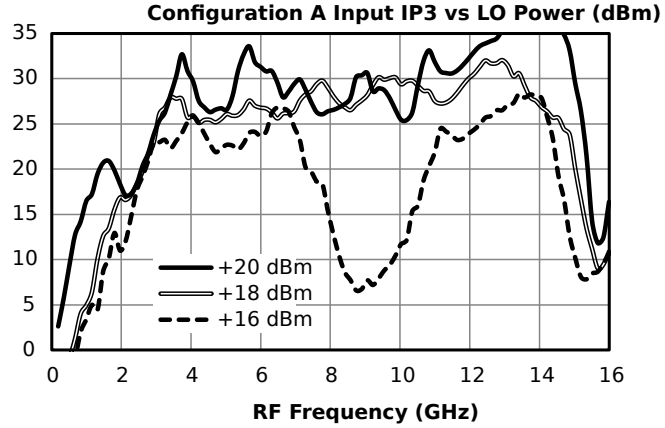


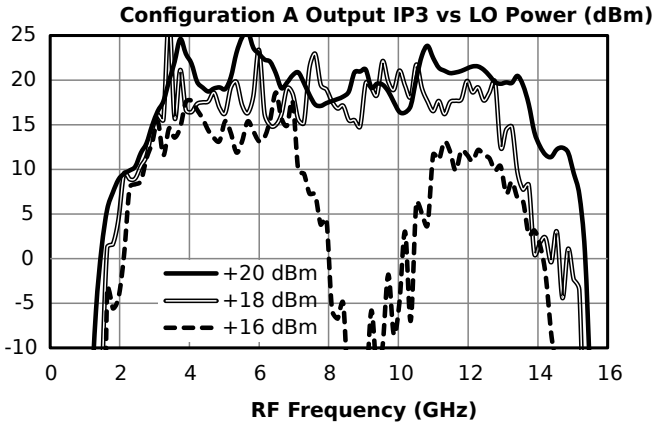


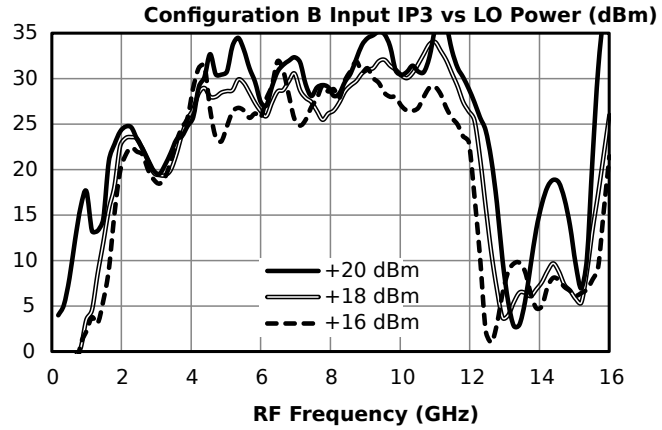
Typical Performance Plots: IP3

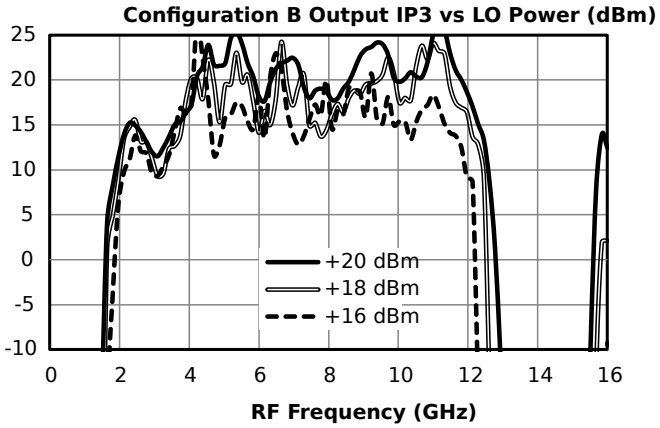




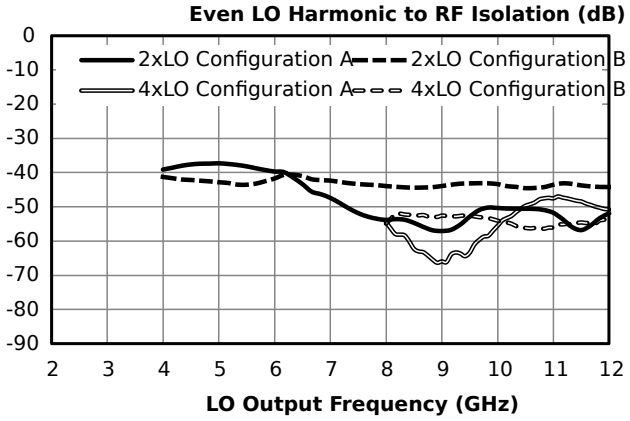


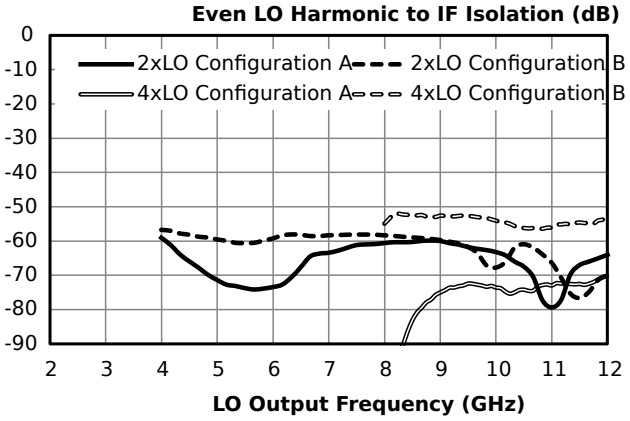


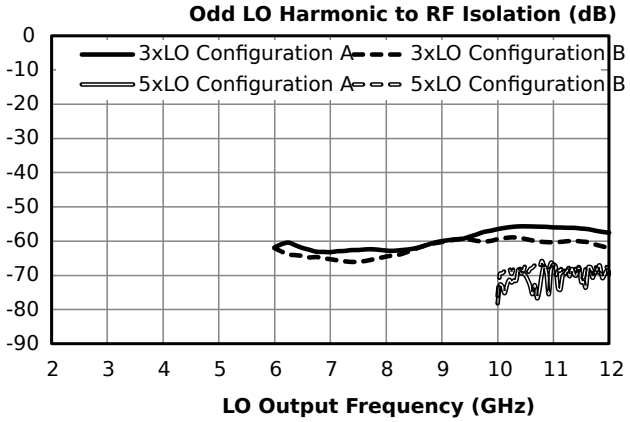


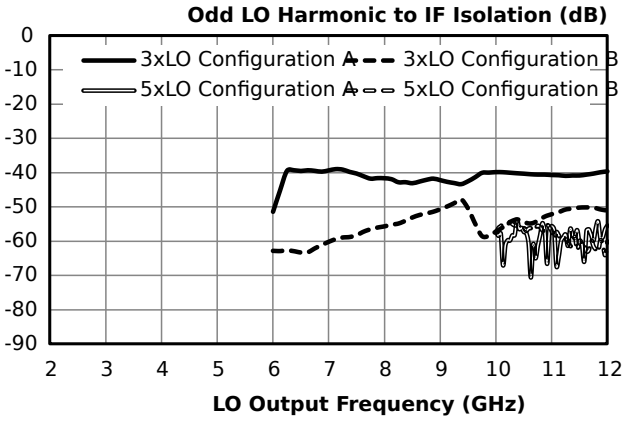


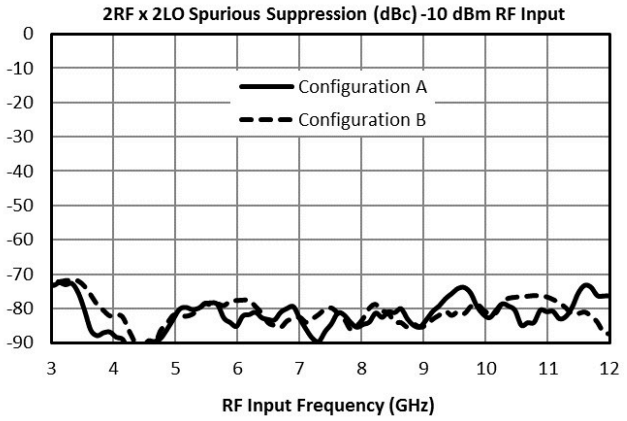
Typical Performance Plots: LO Harmonic Isolation

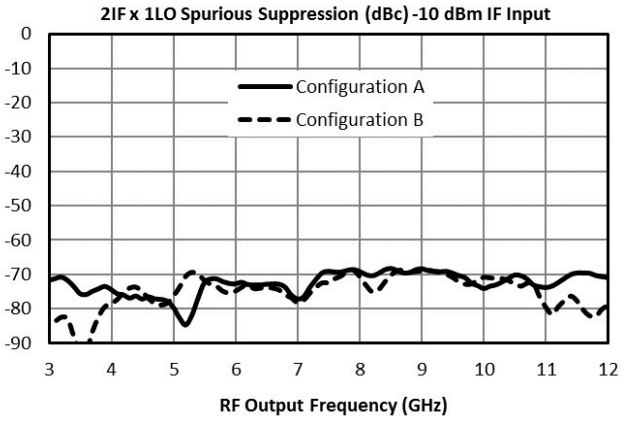












Spur Table

Typical Spurious Performance: Down-Conversion

Typical spurious data is provided by selecting RF and LO frequencies (± m*LO ± n*RF) within the RF/LO bands, to create a spurious output within the IF band. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by (n-1), where "n" is the RF spur order. For example, the 2RF x 2LO spur is 80 dBc for a -10 dBm input, so a -20 dBm RF input creates a spur that is (2-1) x (-10 dB) lower, or 90 dBc. Data is shown for the frequency plan in 3.6 Typical Performance.

Typical Down-conversion spurious suppression (dBc): Config A (B)

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xRF	30 (17)	Reference	33 (33)	11 (13)	44 (41)	23 (23)
2xRF	80 (79)	63 (61)	80 (80)	71 (70)	72 (74)	74 (72)
3xRF	120 (119)	70 (73)	99 (101)	85 (89)	98 (102)	78 (83)
4xRF	148 (145)	112 (125)	130 (129)	127 (128)	132 (136)	127 (127)
5xRF	157 (161)	139 (144)	145 (147)	138 (141)	148 (150)	138 (145)

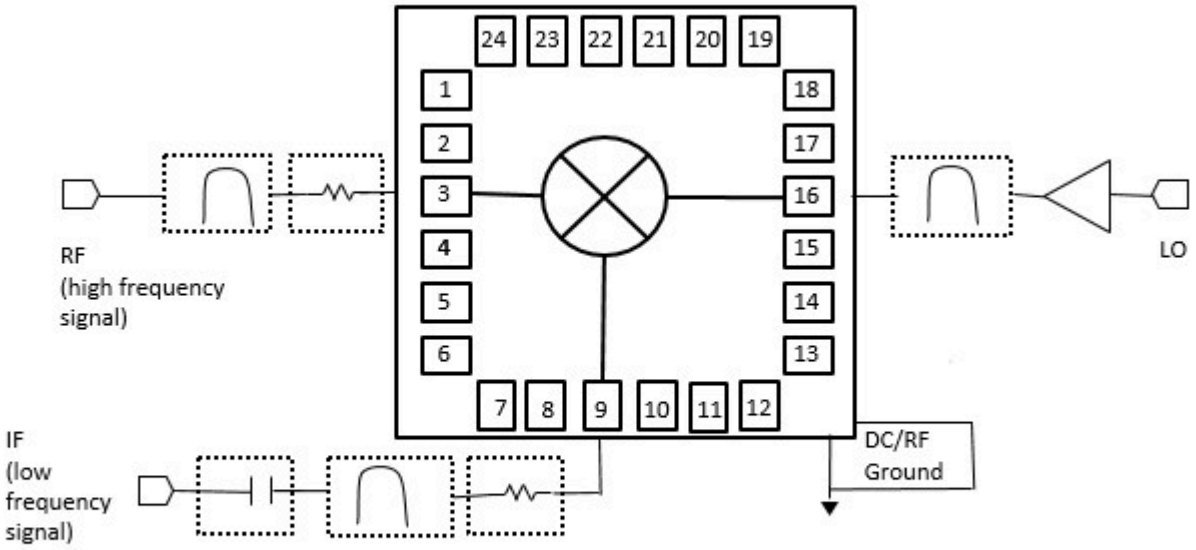
Typical Spurious Performance: Up-Conversion

Typical spurious data is taken by mixing an input within the IF band, with LO frequencies (± m*LO ± n*IF), to create a spurious output within the RF output band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by (n-1), where "n" is the IF spur order. For example, the 2IFx1LO spur is typically 73 dBc for a -10 dBm input with a sine-wave LO, so a -20 dBm IF input creates a spur that is (2-1) x (-10 dB) lower, or 83 dBc. Data is shown for the frequency plan in 3.6 Typical Performance.

Typical Up-conversion spurious suppression (dBc): Config A (B)

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xIF	29 (18)	Reference	33 (33)	11 (12)	44 (38)	29 (28)
2xIF	61 (68)	73 (75)	60 (59)	75 (81)	66 (61)	71 (74)
3xIF	94 (93)	78 (79)	93 (96)	73 (77)	95 (89)	78 (74)
4xIF	120 (128)	132 (134)	120 (118)	127 (134)	112 (114)	131 (119)
5xIF	136 (147)	131 (133)	147 (147)	121 (128)	148 (140)	124 (115)

Application Circuit



Configuration B

Application Circuit Description

IF Port – Used as input on an upconversion, output on downconversion, or LO port in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads. Blocking capacitor is recommended if DC voltage is present on the line.

RF Port – Used as input on a downconversion, output on upconversion, or output in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads.

Filtering and Matching- Filtering is generally desired for spurious and image removal on the output port of the mixer. Reflective filters can cause out of band signals to reflect back into the mixer and cause conversion loss ripple, erroneous spurs, and other undesired behaviors. To eliminate these problems it is recommend that the filters be placed as close to the output port as possible. If undesired behavior is still observed, a diplexer with one port terminated or a 1-3 dB attenuator may reduce this problem.

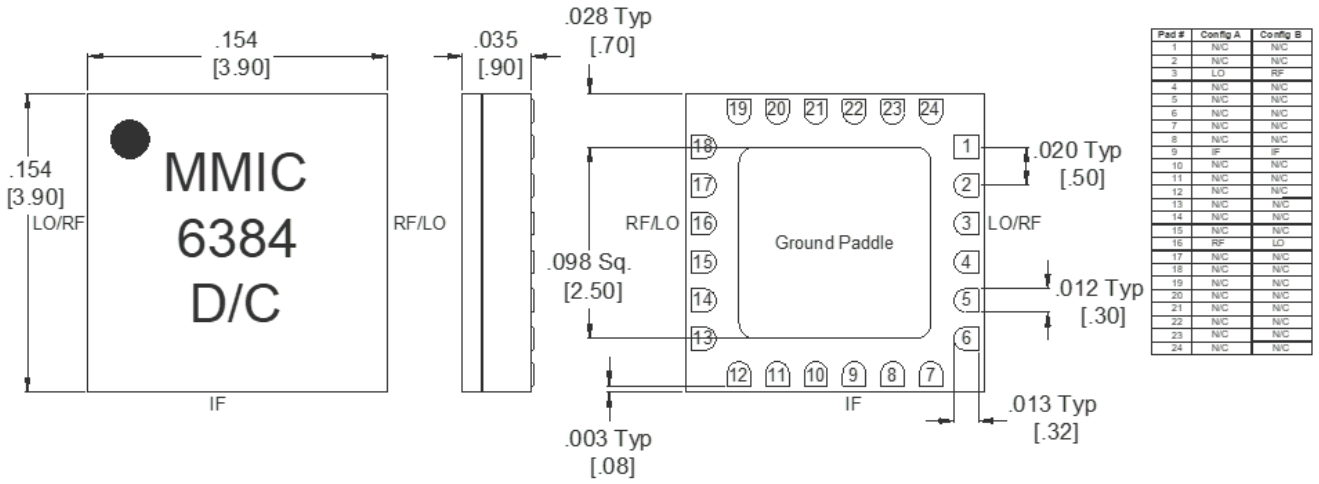
RF Ground – The ground paddle of the QFN should be connected to a low noise RF ground with very low electrical resistance for high frequency operation.

LO Port – The noise floor of the LO input signal should be less than the value of the noise floor plus isolation of the mixer, or a filter is recommended to prevent reduction in dynamic range. An LO amplifier is required if the LO power is below the recommended drive level. It is important to use an amplifier with a broadband 50 ohm match such that it does not reflect spurious signals back into the mixer or other system circuitry.

Mechanical Data

Outline Drawing

Download : Outline 2D Drawing | Outline 3D Drawing | Outline 3D STP



Pad #	Config A	Config B
1	N/C	N/C
2	N/C	N/C
3	LO	RF
4	N/C	N/C
5	N/C	N/C
6	N/C	N/C
7	N/C	N/C
8	N/C	N/C
9	IF	IF
10	N/C	N/C
11	N/C	N/C
12	N/C	N/C
13	N/C	N/C
14	N/C	N/C
15	N/C	N/C
16	RF	LO
17	N/C	N/C
18	N/C	N/C
19	N/C	N/C
20	N/C	N/C
21	N/C	N/C
22	N/C	N/C
23	N/C	N/C
24	N/C	N/C

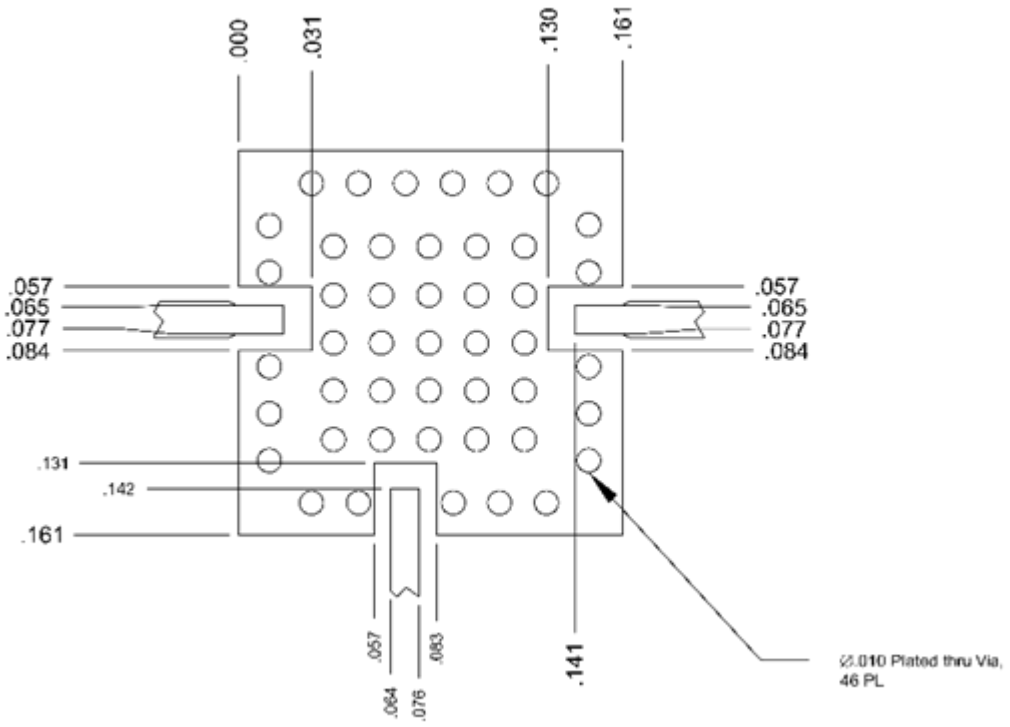
1. Substrate material is ceramic.
2. I/O Leads and Ground Paddle plating is (from base to finish):

Ni:	8.89um MAX	1.27um MIN
Pd:	0.17um MAX	0.07um MIN
Au	0.254um MAX	0.03um MIN

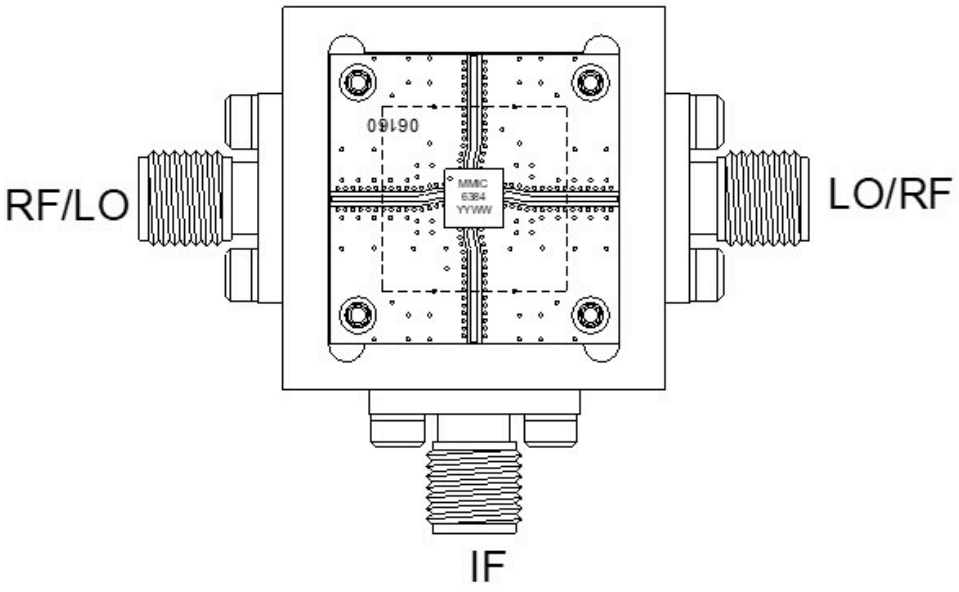
3. All unconnected pads should be connected to PCB RF ground.

Footprint Image

Download : Footprint Drawing



Evaluation Board - Outline Drawing



Configuration A	Configuration B	Connector Type
RF	LO	SMA Female
LO	RF	SMA Female
IF	IF	SMA Female
Note: Eval connectors are not removeable		

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