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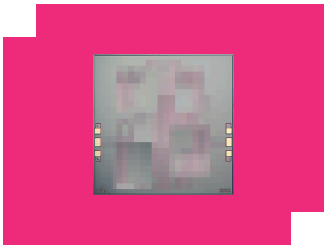
MFBP-00026CH

Passive GaAs MMIC 10.00 - 14.75 GHz Bandpass Filter

DEVICE OVERVIEW

General Description

The MFBP-00026CH family of passive MMIC bandpass filter die are an ideal solution for small form factor, high rejection filtering. Passive GaAs MMIC technology allows production of smaller filter constructions that replace larger form factor circuit board constructions. Tight fabrication tolerances allow for less unit to unit variation than traditional filter technologies. The MFBP-00026CH is available as a wire bondable chip. Low unit to unit variation allows for accurate simulations using the provided S2P file taken from measured production units.



Features

- Excellent Return Loss
- High Stop Band Suppression
- Wide Stop Band with Fast Roll-Off

Applications

- X Band Applications
- Ku Band Applications

Functional Block Diagram

Part Ordering Options

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
MFBP-00026CH	Passive GaAs MMIC 10.00 - 14.75 GHz Bandpass Filter	CH	RoHS REACH	Released	EAR99

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Table Of Contents

Revision History

Revision Code	Revision Date	Comment
-	2023-08-30	Datasheet Initial Release
A	2024-01-05	Updated Production Test Criteria

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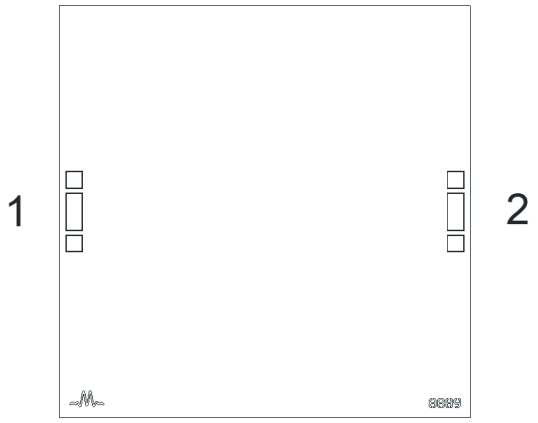
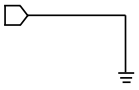
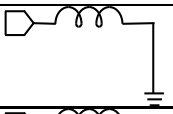
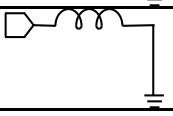
Port Configuration and Functions

Port Diagram

A top-down view of the MFBP-00026CH package outline drawing is shown below. The MMIC bandpass filters are symmetrical allowing Port 1 or Port 2 to be used as the input.



Port Functions

Port	Function	Description	DC Equivalent Circuit
Pad	Ground	CH package ground path is provided through the substrate and ground bond pads.	
Port 1	Input/Output	Port 1 is DC short to ground for the CH package.	
Port 2	Input/Output	Port 2 is DC short to ground for the CH package.	

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Specifications

Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded, the device may be inoperable or have a reduced lifetime.

Parameter	Maximum Rating	Unit
DC Current	200	mA
Maximum Operating Temperature	100	°C
Maximum Storage Temperature	125	°C
Minimum Operating Temperature	-55	°C
Minimum Storage Temperature	-65	°C

Package Information

Parameter	Details	Rating
Dimensions	-	2.40 x 2.40 mm

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Electrical Specifications

The electrical specifications apply at TA=+25°C in a 50Ω system. Min and Max limits are guaranteed at TA=+25°C. All bare die are 100% visually inspected and RF performance is guaranteed by sample testing.

Parameter	Test Conditions	Minimum Frequency (GHz)	Maximum Frequency (GHz)	Min	Typ	Max	Unit
1 dBc Passband	Configuration A, 25°C	10.07	14.7	-	-	-	GHz
30 dBc Rejection Point	Configuration A, 25°C	9.2	16.1	-	-	-	GHz
3 dBc Passband	Configuration A, 25°C	9.8	15.1	-	-	-	GHz
Center Freq	Configuration A, 25°C	-	-	-	12.17	-	GHz
Group Delay	Configuration A, 25°C	-	-	-	276	-	ps
Impedance	Configuration A, 25°C	-	-	-	50	-	Ω
Insertion Loss @ fc	Configuration A, 25°C	-	-	-	1.2	-	dB
Passband Return Loss	Configuration A, 25°C	-	-	-	25	-	dB
Center Frequency, f□	-	-	-	-	12.5	-	GHz
Group Delay	-	9.75	15.1	-	376	-	ps
Impedance	-	-	-	-	50	-	Ω
Insertion Loss @ F□	-	12.5	12.5	-	1.2	-	dB
Passband Return Loss	-	10	14.75	-	19	-	dB
Stopband Suppression	-	0	8.5	30	42	-	dB
Stopband Suppression	-	16.6	29	30	35	-	dB

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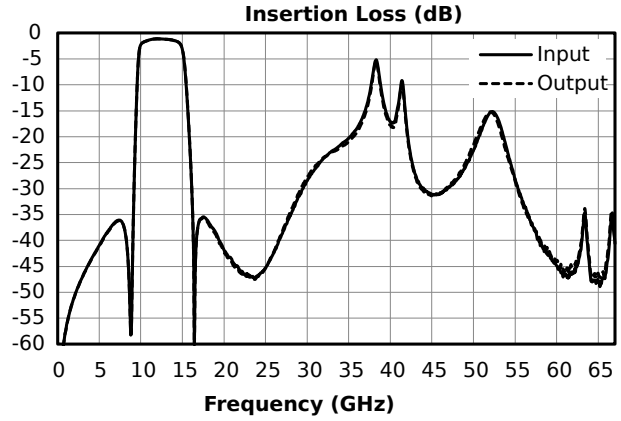
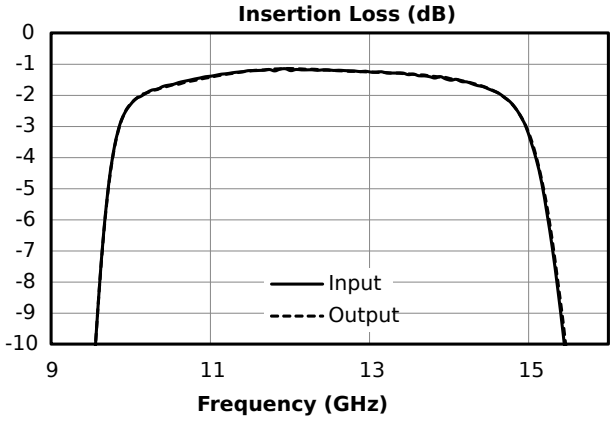
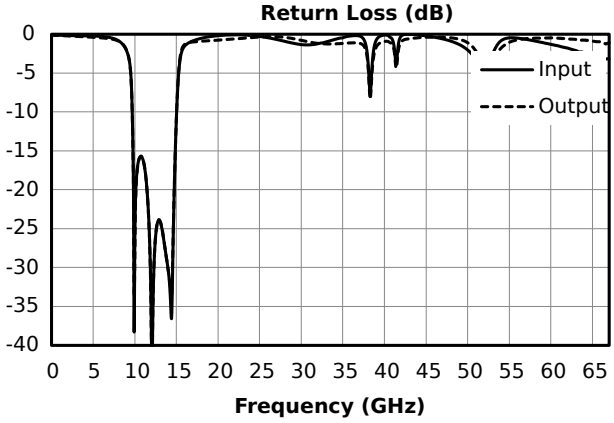
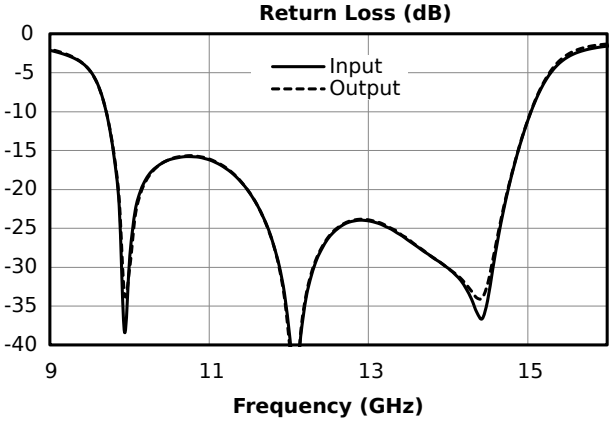
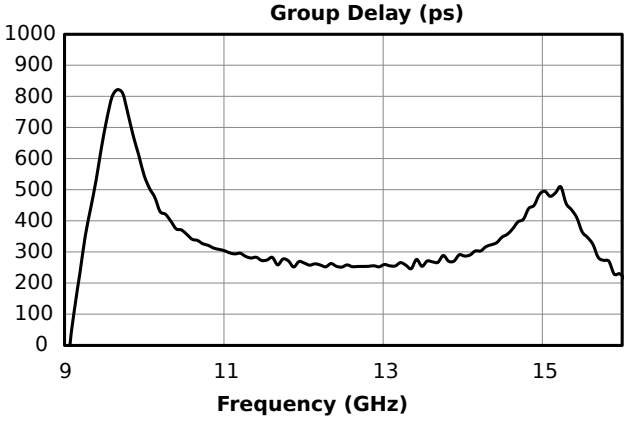
Typical Performance Plots











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Die Mounting Recommendations

Mounting and Bonding Recommendations

Marki MMICs should be attached directly to a ground plane with conductive epoxy. The ground plane electrical impedance should be as low as practically possible. This will prevent resonances and permit the best possible electrical performance. Datasheet performance is only guaranteed in an environment with a low electrical impedance ground.

Mounting - To epoxy the chip, apply a minimum amount of conductive epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip. Cure epoxy according to manufacturer instructions.

Wire Bonding - Ball or wedge bond with 0.025 mm (1 mil) diameter pure gold wire. Thermosonic wirebonding with a nominal stage temperature of 150 °C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Use the minimum level of ultrasonic energy to achieve reliable wirebonds. Wirebonds should be started on the chip and terminated on the package or substrate. Bond wire inductance will improve return loss. Bondwire inductance in the range of 30pH to 200pH will improve performance.

Circuit Considerations– 50 Ω transmission lines should be used for all high frequency connections in and out of the chip. Wirebonds should be kept as short as possible, with multiple wirebonds recommended for higher frequency connections to reduce parasitic inductance. In circumstances where the chip more than .001” thinner than the substrate, a heat spreading spacer tab is optional to further reduce bondwire length and parasitic inductance.

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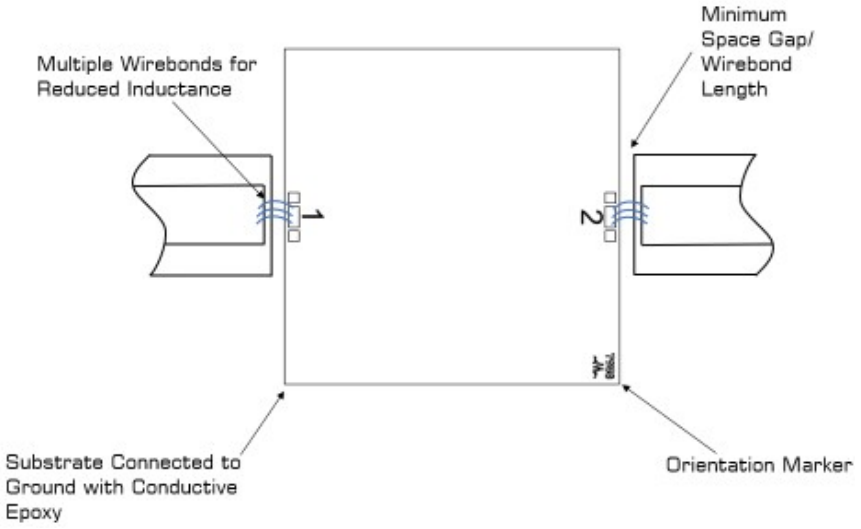
Bonding Diagram



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Handling Precautions

General Handling

Chips should be handled with care using tweezers or a vacuum collet. Users should take precautions to protect chips from direct human contact that can deposit contaminants, like perspiration and skin oils on any of the chip's surfaces.

Static Sensitivity

GaAs MMIC devices are sensitive to ESD and should be handled, assembled, tested, and transported only in static protected environments.

Cleaning and Storage: Do not attempt to clean the chip with a liquid cleaning system or expose the bare chips to liquid. Once the ESD sensitive bags the chips are stored in are opened, chips should be stored in a dry nitrogen atmosphere.

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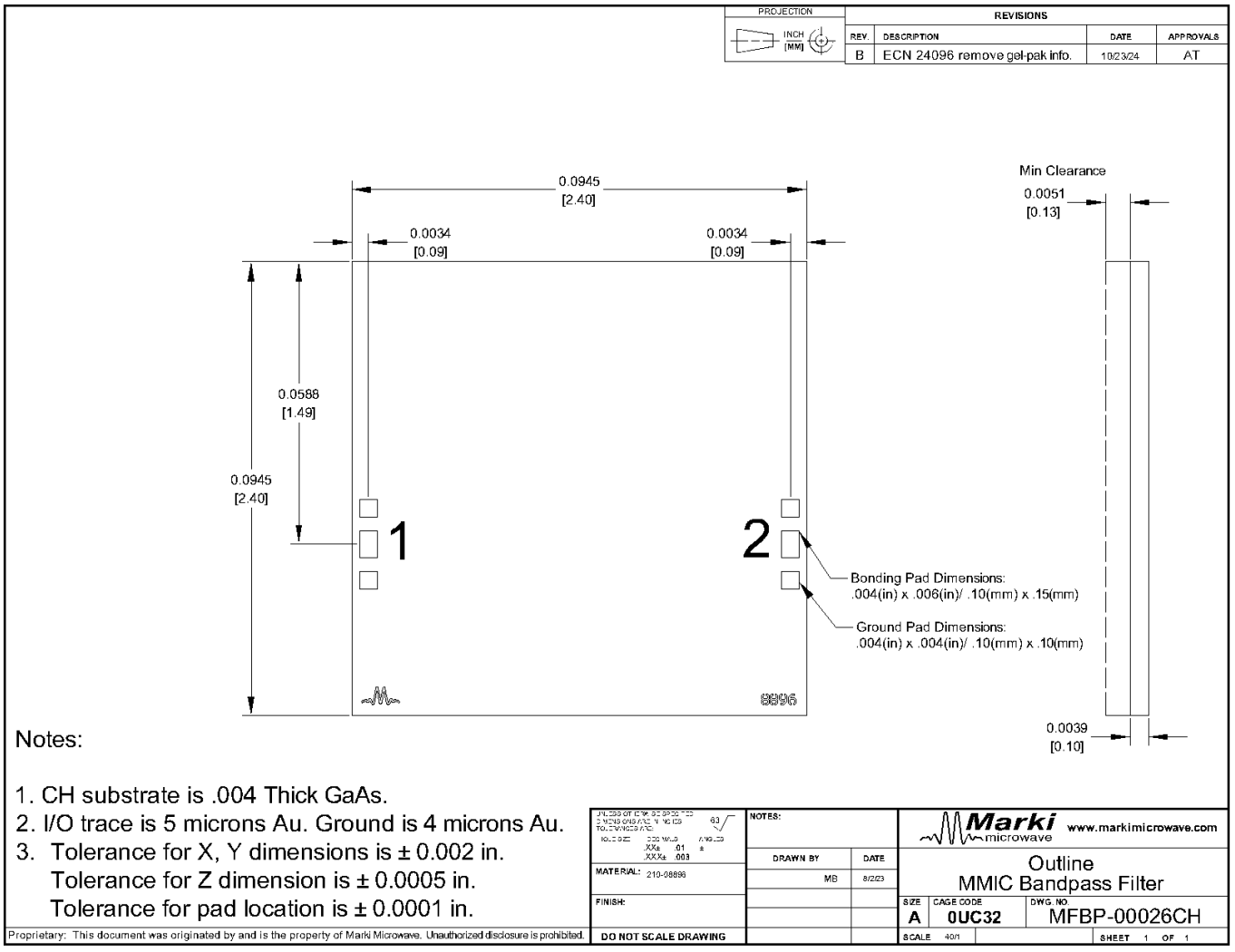
Mechanical Data

Outline Drawing

Download : Outline 2D Drawing



1. CH substrate is .004 in Thick GaAs.
2. Traces are 5 microns Au.
3. Tolerance for X, Y dimensions is ± 0.002 in.

Tolerance for Z dimension is ± 0.0005 in.

Tolerance for pad location is ± 0.0001 in.

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