

MM1-0726HSM-2

GaAs DOUBLE-BALANCED MIXER

DEVICE OVERVIEW

General Description

The MM1-0726HSM is a passive GaAs double balanced MMIC mixer suitable for both up and down-conversion applications. As with all Marki Microwave mixers, it features excellent conversion loss, isolation and spurious performance across a broad bandwidth and in a small form factor. The MM1-0726HSM is available in a lead-free, RoHS compliant QFN surface mount package and is compatible with standard leaded and lead-free PCB reflow soldering processes. Owing to its passive balun circuitry, the mixer can be used in two different configurations: Configuration A for highest efficiency and Configuration B for highest linearity. Refer to page 2 for more information on the operating configurations.



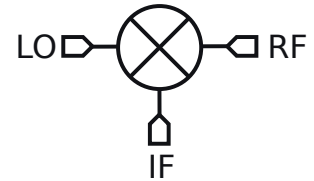
Features

- Compact 3mm QFN SMT Style Package
- Broadband Performance
- Excellent Unit-to-Unit Repeatability
- RoHS Compliant

Applications

N/A

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
MM1-0726HSM-2	GaAs DOUBLE-BALANCED MIXER	QFN	REACH RoHS	Released	EAR99
EVAL-MM1-0726H	Evaluation Board, GaAs 7 - 26.5 GHz Double-balanced Mixer	EVAL	REACH RoHS	Released	EAR99

Table Of Contents

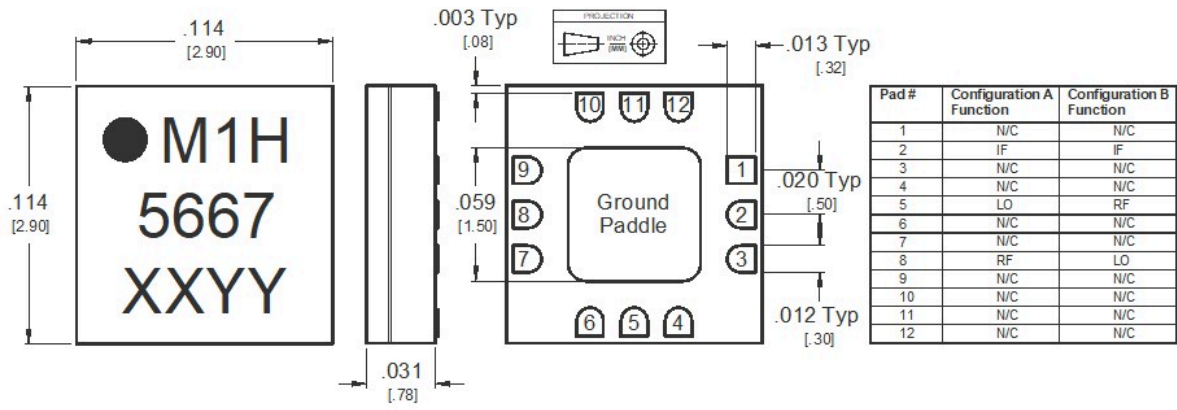
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Revision History

Revision Code	Revision Date	Comment
-	2015-01-01	Initial Release
A	2019-04-01	Updated plating to ENEPIG

Port Configuration and Functions

Port Diagram



Outline Drawing – QFN package

Substrate material is Ceramic.


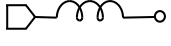

I/O Leads and Ground Paddle plating is (from base to finish):

Ni:	8.89um MAX	1.27um MIN
Pd:	0.17um MAX	0.07um MIN
Au:	0.254um MAX	0.03um MIN

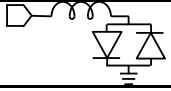
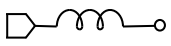
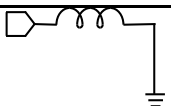
All unconnected pads should be connected to PCB RF ground.

Port Functions

Configuration A

Port	Function	Description	Equivalent Circuit for Package
Port 2	IF	Port 2 is DC coupled to the diodes. Blocking capacitor is optional.	
Port 5	LO	Port 5 is DC open to ground and AC matched to 50 Ohms from 7 GHz to 26.5 GHz. Blocking capacitor is optional.	
Port 8	RF	Port 8 is DC short to ground and AC matched to 50 Ohms from 7 GHz to 26.5 GHz. Blocking capacitor is optional.	

Configuration B

Port	Function	Description	Equivalent Circuit for Package
Port 2	IF	Port 2 is DC coupled to the diodes. Blocking capacitor is optional.	
Port 5	RF	Port 5 is DC open to ground and AC matched to 50 Ohms from 7 GHz to 26.5 GHz. Blocking capacitor is optional.	
Port 8	LO	Port 8 is DC short to ground and AC matched to 50 Ohms from 7 GHz to 26.5 GHz. Blocking capacitor is optional.	

Specifications

Absolute Maximum Ratings

Parameter	Maximum Rating	Unit
Maximum Operating Temperature	100	°C
Maximum Storage Temperature	125	°C
Minimum Operating Temperature	-55	°C
Minimum Storage Temperature	-65	°C
Port 2 DC Current	30	mA
Port 5 DC Current	30	mA
Port 8 DC Current	30	mA
RF Power Handling (RF+LO), 100°C	20	dBm
RF Power Handling (RF+LO), 25°C	25	dBm

Package Information

Parameter	Details	Rating
Dimensions	-	3 x 3 mm
Moisture Sensitivity Level	-	MSL 1

Recommended Operating Conditions

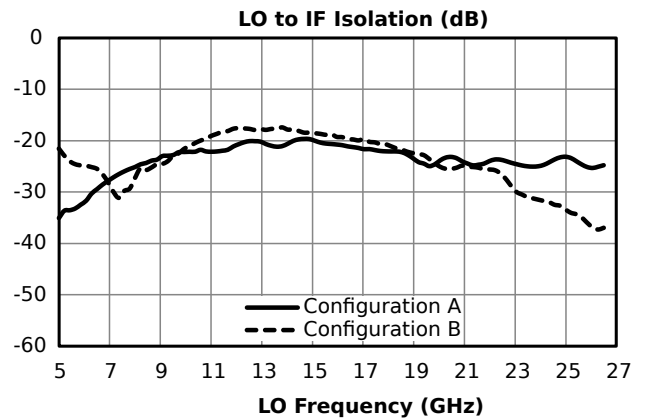
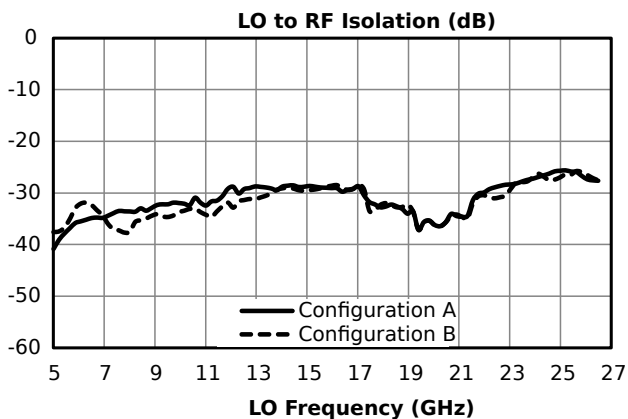
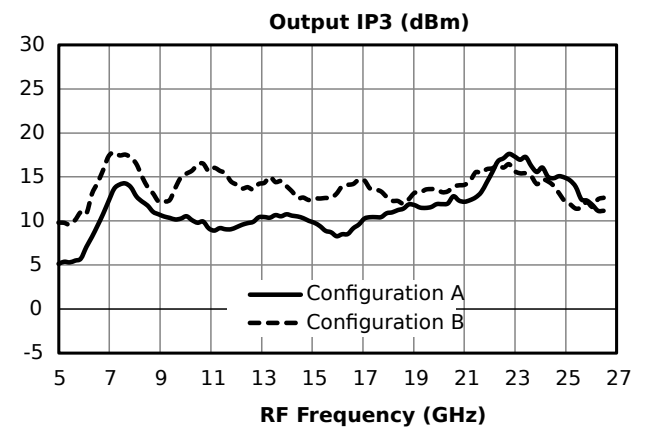
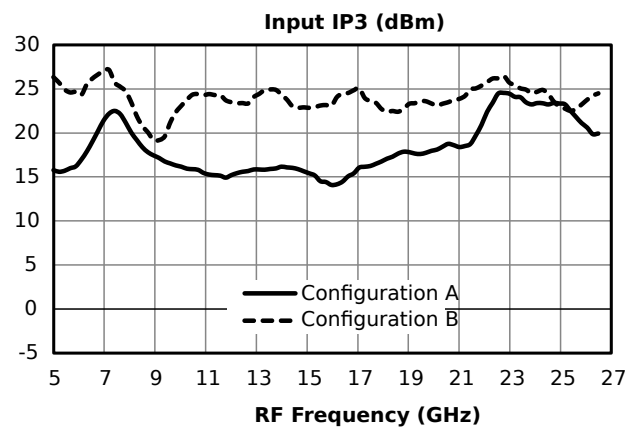
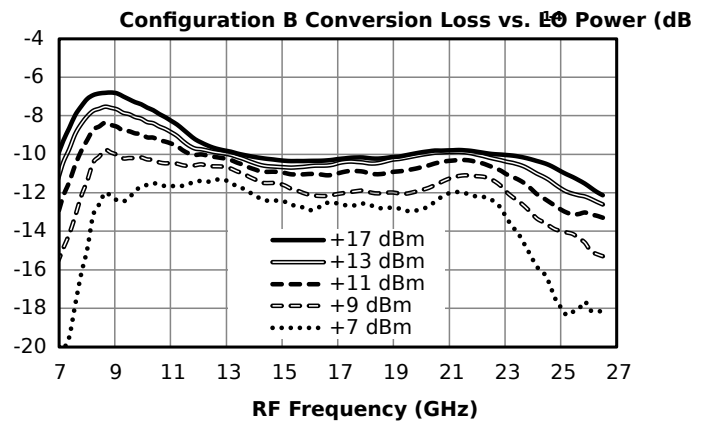
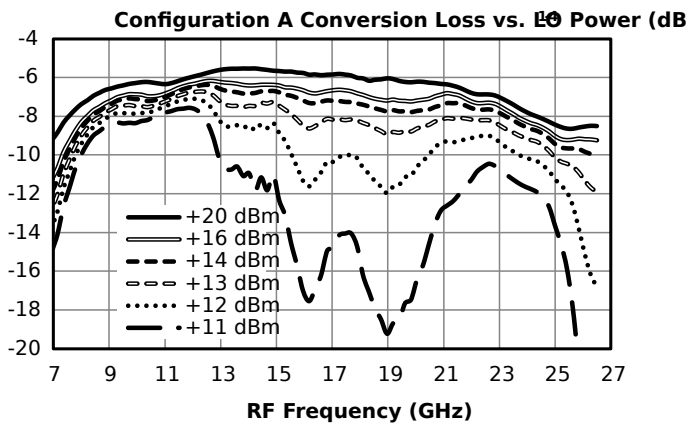
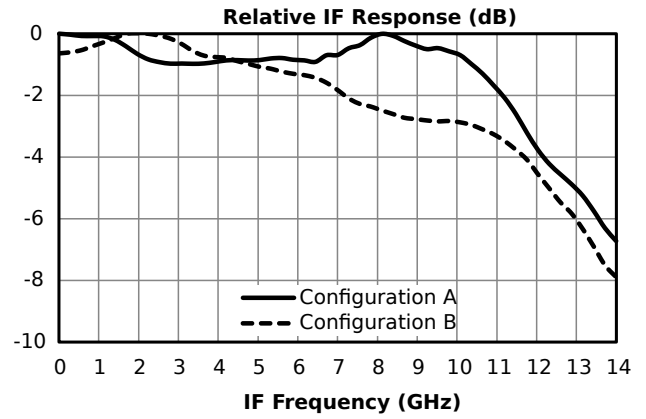
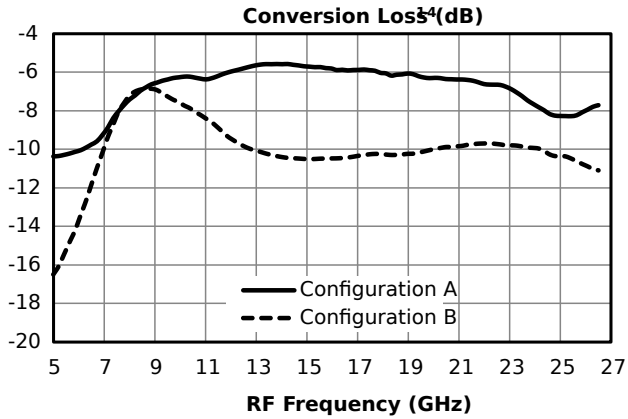
Parameter	Min	Nominal	Max	Unit
LO Input Power	15	-	23	-

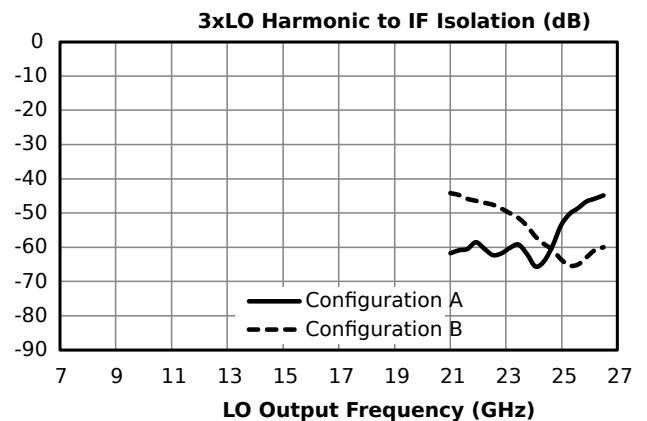
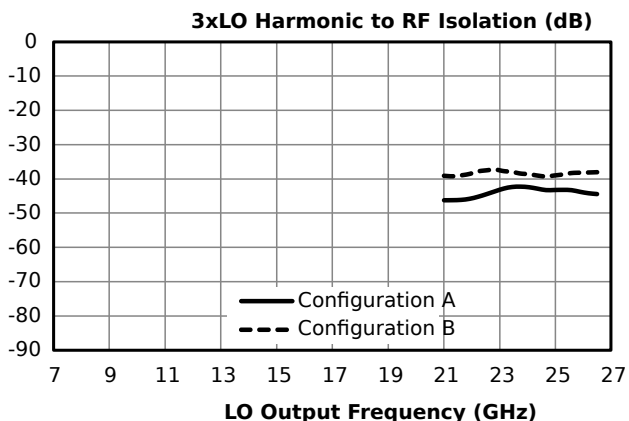
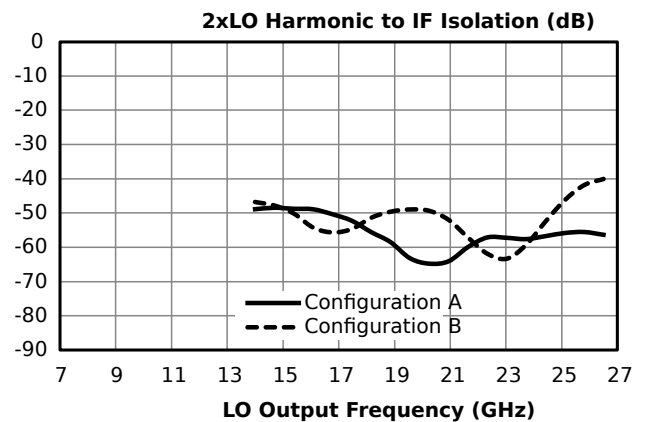
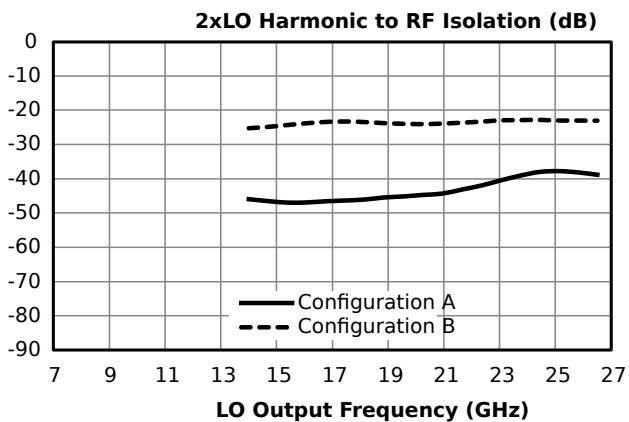
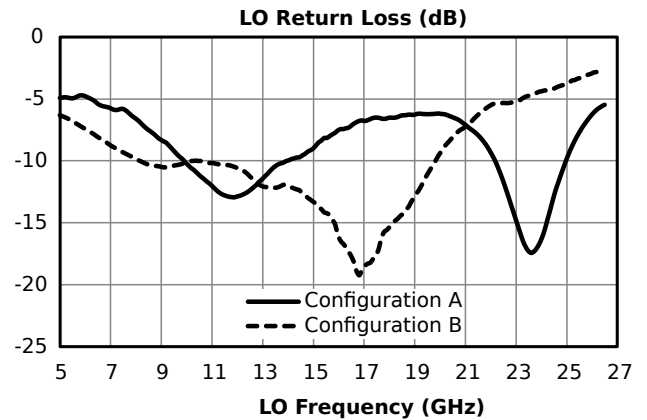
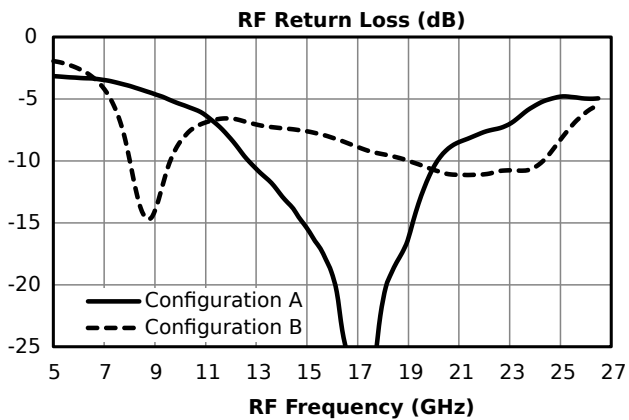
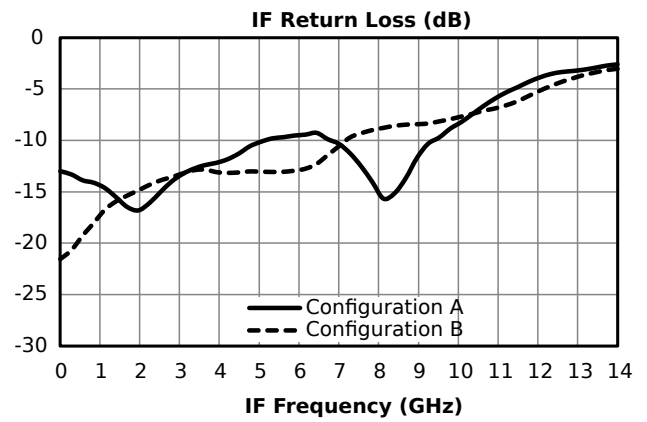
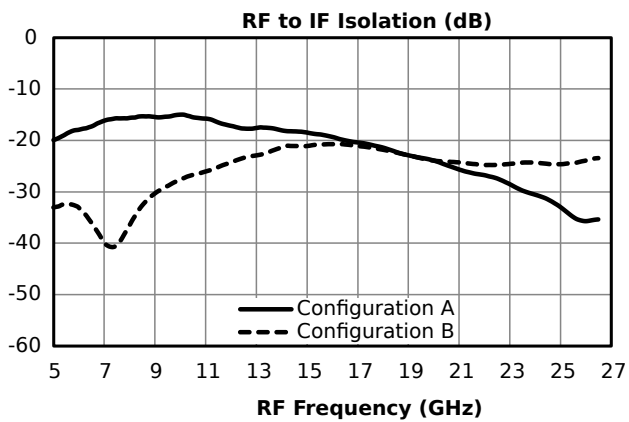
Electrical Specifications

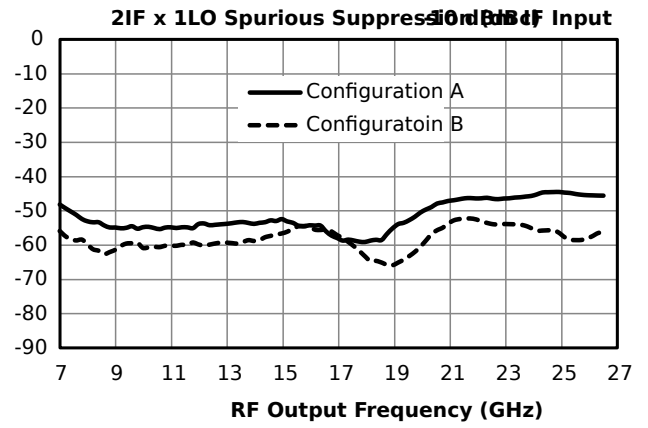
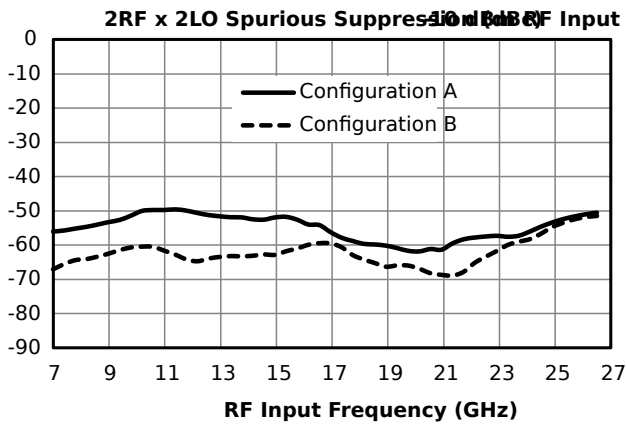
Specifications guaranteed from -55 to +100°C, measured in a 50Ω system.

Parameter	Port Configuration	Test Conditions	Min	Typ	Max	Unit
Conversion Loss	A	LO/RF=7-26.5GHz IF=DC-9GHz	-	6.5	12	dB
IF Frequency Range	A	-	0	-	9	GHz
Input 1 dB Compression	A	LO/RF=7-26.5GHz IF=DC-9GHz LO drive level=17-23dBm	-	9	-	dBm
Input IP3	A	LO/RF=7-26.5GHz IF=DC-9GHz LO drive level=17-23dBm	-	17	-	dBm
LO Frequency Range	A	-	7	-	26.5	GHz
RF Frequency Range	A	-	7	-	26.5	GHz
Conversion Loss	B	LO/RF=7-26.5GHz IF=DC-9GHz	-	9.5	14.5	dB
Input 1 dB Compression	B	LO/RF=7-26.5GHz IF=DC-9GHz LO drive level=14-20dBm	-	10	-	dBm
Input IP3	B	LO/RF=7-26.5GHz IF=DC-9GHz LO drive level=14-20dBm	-	23	-	dBm
Isolation, LO to RF	-	-	-	27	-	dB

Typical Performance Plots







Spur Table

Downconversion Spurious Suppression

Spurious data is taken by selecting RF and LO frequencies (+mLO+nRF) within the RF/LO bands, to create a spurious output within the IF output band. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by (n-1), where “n” is the RF spur order. For example, the 2RFx2LO spur is 55 dBc for the A configuration for a -10 dBm input, so a -20 dBm RF input creates a spur that is (2-1) x (-10 dB) dB lower, or 65 dBc.

Typical Downconversion Spurious Suppression (dBc): H Diode, A Configuration (B Configuration) ⁴

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xRF	16 (14)	Reference	17 (35)	10 (13)	18 (28)	24 (18)
2xRF	61 (68)	52 (48)	55 (62)	52 (50)	56 (67)	52 (48)
3xRF	85 (92)	63 (69)	68 (84)	71 (81)	70 (87)	69 (81)
4xRF	110 (110)	84 (102)	93 (104)	98 (106)	98 (111)	98 (107)
5xRF	124 (121)	109 (125)	105 (123)	112 (124)	113 (129)	117 (128)

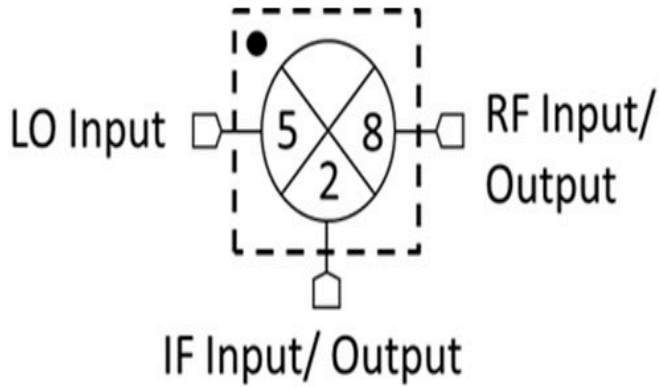
Upconversion Spurious Suppression

Spurious data is taken by mixing an input within the IF band, with LO frequencies (+mLO+nIF), to create a spurious output within the RF output band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by (n-1), where “n” is the IF spur order. For example, the 2IFx1LO spur is typically 52 dBc for the A configuration for a -10 dBm input, so a -20 dBm IF input creates a spur that is (2-1) x (-10 dB) dB lower, or 62 dBc.

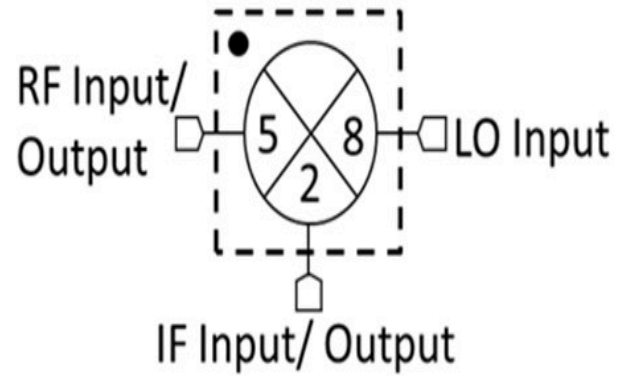
Typical Upconversion Spurious Suppression (dBc): H Diode, A Configuration (B Configuration)⁴

-10 dBm IF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xIF	20 (27)	Reference	12 (32)	8 (9)	18 (32)	24 (16)
2xIF	56 (50)	52 (58)	50 (48)	48 (43)	53 (38)	55 (47)
3xIF	75 (81)	68 (74)	70 (79)	69 (68)	71 (69)	68 (62)
4xIF	108 (102)	96 (105)	89 (92)	98 (99)	95 (85)	90 (89)
5xIF	128 (124)	110 (122)	107 (117)	120 (121)	116 (110)	105 (99)

Application Information



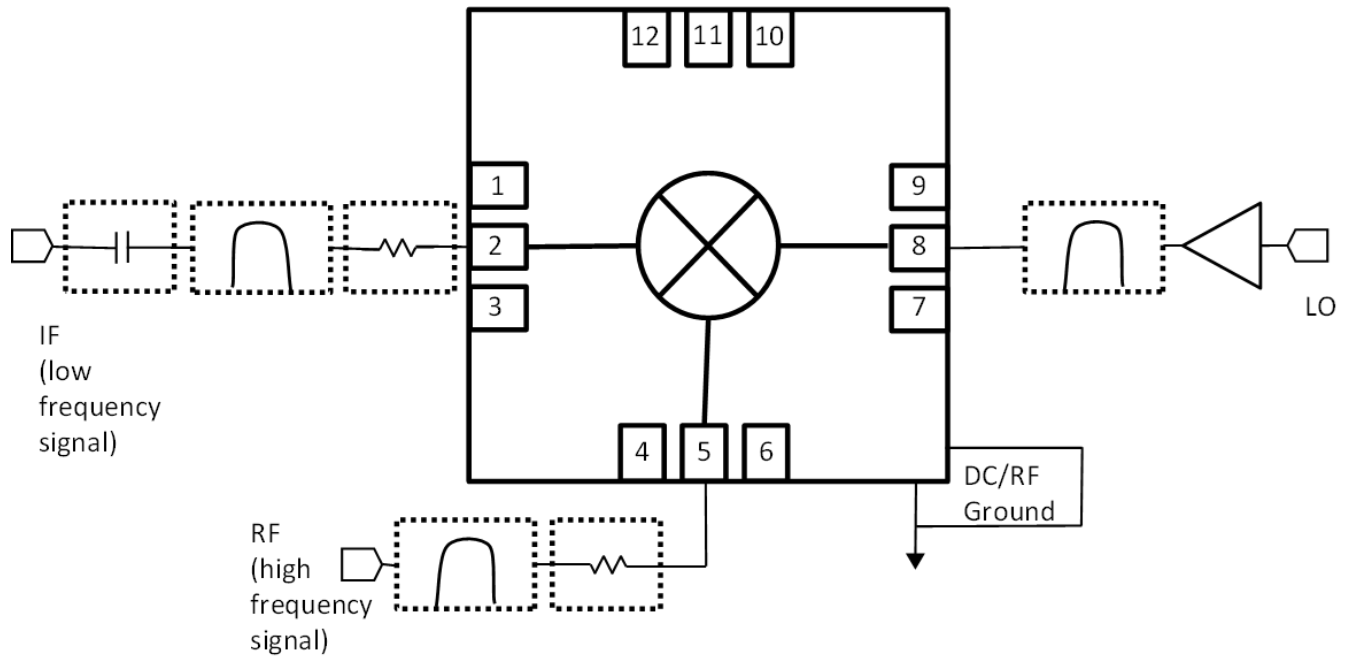
Configuration A



Configuration B

Configuration A/B refer to the same part number (MM1-0320H) used in one of two different ways for optimal spurious performance. For the lowest conversion loss, use the mixer in Configuration A (pin 5 as the LO input, pin 8 as the RF input or output). If you need to use a lower LO drive, use the mixer in Configuration B (pin 5 as the RF input or output, pin 8 as the LO input). For optimal spurious suppression, experimentation or simulation is required to choose between Configuration A and B.

Application Circuit



Configuration B

Application Circuit Description

IF Port – Used as input on an upconversion, output on downconversion, or LO port in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads. Blocking capacitor is recommended if DC voltage is present on the line.

RF Port – Used as input on a downconversion, input on upconversion, or output in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads.

Filtering and Matching- Filtering is generally desired for spurious and image removal on the output port of the mixer. Reflective filters can cause out of band signals to reflect back into the mixer and cause conversion loss ripple, erroneous spurs, and other undesired behaviors. To eliminate these problems it is recommend that the filters be placed as close to the output port as possible. If undesired behavior is still observed, a diplexer with one port terminated or a 1-3 dB attenuator may reduce this problem.

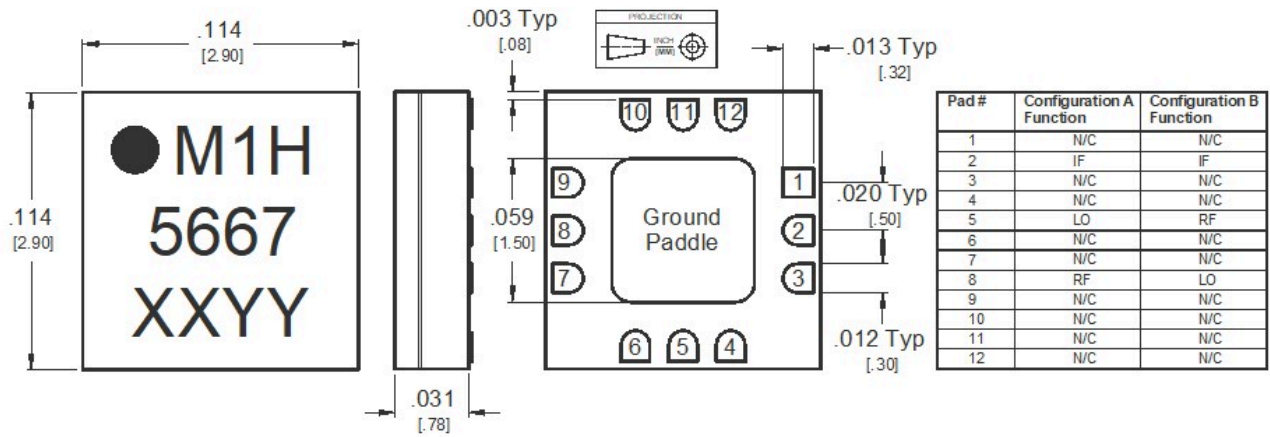
RF Ground – The ground paddle of the QFN should be connected to a low noise RF ground with very low electrical resistance for high frequency operation.

LO Port – The noise floor of the LO input signal should be less than the value of the noise floor plus isolation of the mixer, or a filter is recommended to prevent reduction in dynamic range. An LO amplifier is required if the LO power is below the recommended drive level.

Mechanical Data

Outline Drawing

Download : [Outline 2D Drawing](#) | [Outline 3D Drawing](#) | [Outline 3D STP](#)



Outline Drawing – QFN package

Substrate material is Ceramic.

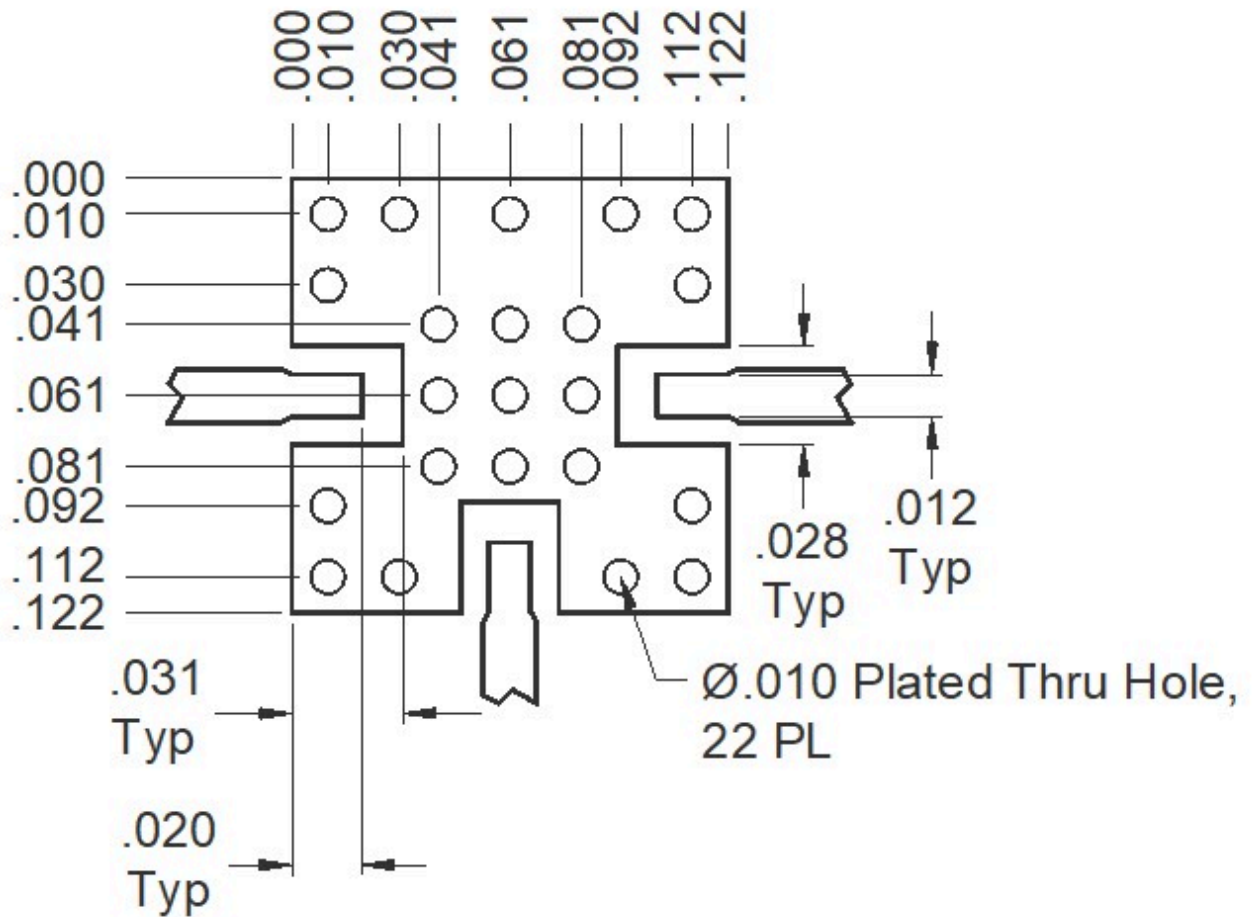
I/O Leads and Ground Paddle plating is (from base to finish):

- Ni: 8.89um MAX 1.27um MIN
- Pd: 0.17um MAX 0.07um MIN
- Au: 0.254um MAX 0.03um MIN

All unconnected pads should be connected to PCB RF ground.

Footprint Image

Download: [Footprint Drawing](#)



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