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Tech Support: support@markimicrowave.com

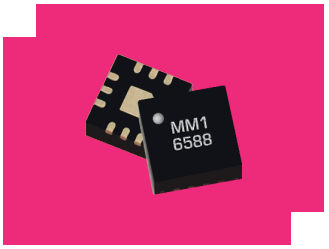
MM1-0832HPSM-2

GaAs MMIC Double Balanced Mixer

DEVICE OVERVIEW

General Description

The MM1-0832HPSM is a GaAs MMIC double balanced mixer suitable for both up and down-conversion applications. As with all Marki Microwave mixers, it features excellent conversion loss, isolation and spurious performance across a broad bandwidth and in a small form factor. The MM1-0832HPSM is available in a lead-free, RoHS compliant 3x3 mm QFN package and is compatible with standard leaded and lead-free PCB reflow soldering processes. Evaluation boards are also available.



Features

RF/LO response: 8 GHz – 32 GHz

IF response: DC – 12 GHz

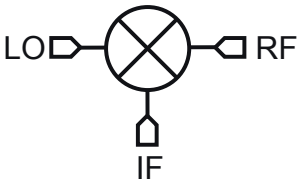
Conversion Loss: 8 dB

LO to RF Isolation : 50 dB

Applications

- Test and Measurement Equipment
- SATCOM
- 5G
- Electronic Warfare

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
MM1-0832HPSM-2	GaAs MMIC Double Balanced Mixer	QFN	REACH RoHS	Released	EAR99
EVAL-MM1-0832HP	Evaluation Board, GaAs MMIC 8 - 32 GHz Double Balanced Mixer	EVAL	REACH RoHS	Released	EAR99

Table Of Contents

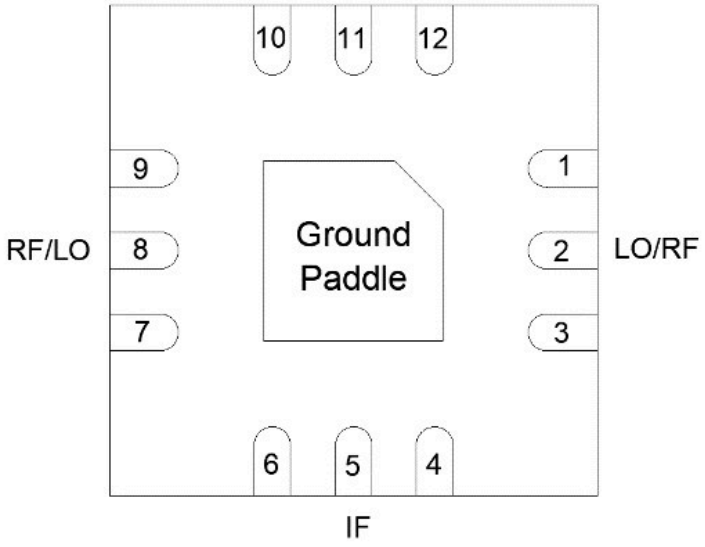
Revision History

Revision Code	Revision Date	Comment
-	2021-09-01	Datasheet Initial Release

Port Configuration and Functions

Port Diagram

A bottom-up view of the MM1-0832HPSM's PSM package outline drawing is shown below. The MM1-0832HPSM has the input and output ports given in Port Functions. The MM1-0832HPSM can be used in either an up or down conversion application. For configuration A, input the LO into pin 2, use pin 8 for the RF, and pin 5 for the IF. For configuration B, input the LO into pin 8, use pin 2 for the RF, and pin 5 for the IF.

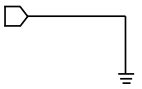
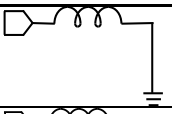
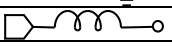


Port Functions

Configuration A

Port	Function	Description	Equivalent Circuit for Package
GND	Ground	PSM package ground path is provided through the ground paddle.	
Pin 2	LO	Pin 2 is DC short and AC matched to 50 Ohms from 8 to 32 GHz.	
Pin 5	IF	Pin 5 is DC coupled to the diodes. Blocking capacitor is optional.	
Pin 8	RF	Pin 8 is DC open and AC matched to 50 Ohms from 8 to 32 GHz.	

Configuration B

Port	Function	Description	Equivalent Circuit for Package
GND	Ground	PSM package ground path is provided through the ground paddle.	
Pin 2	RF	Pin 2 is DC short and AC matched to 50 Ohms from 8 to 32 GHz.	
Pin 5	IF	Pin 5 is DC coupled to the diodes. Blocking capacitor is optional.	
Pin 8	LO	Pin 8 is DC open and AC matched to 50 Ohms from 8 to 32 GHz.	

Specifications

Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded, the device may be inoperable or have a reduced lifetime.

Parameter	Maximum Rating	Unit
Maximum Operating Temperature	100	°C
Maximum Storage Temperature	125	°C
Minimum Operating Temperature	-55	°C
Minimum Storage Temperature	-65	°C
Pin 2 DC Current	30	mA
Pin 5 DC Current	15	mA
Power Handling, at any Port	28	dBm

Package Information

Parameter	Details	Rating
ESD	250 to < 500 Volts	HBM Class 1A
Dimensions	-	3 x 3 mm
Moisture Sensitivity Level	-	MSL 1

Recommended Operating Conditions

The Recommended Operating Conditions indicate the limits, inside which the device should be operated, to guarantee the performance given in Electrical Specifications Operating outside these limits may not necessarily cause damage to the device, but the performance may degrade outside the limits of the electrical specifications. For limits, above which damage may occur, see Absolute Maximum Ratings.

Parameter	Min	Nominal	Max	Unit
LO Input Power	12	18	23	dBm
Ambient Temperature	-55	25	100	°C

Sequencing Requirements

There is no requirement to apply power to the ports in a specific order. However, it is recommended to provide a 50Ω termination to each port before applying power. This is a passive diode mixer that requires no DC bias.

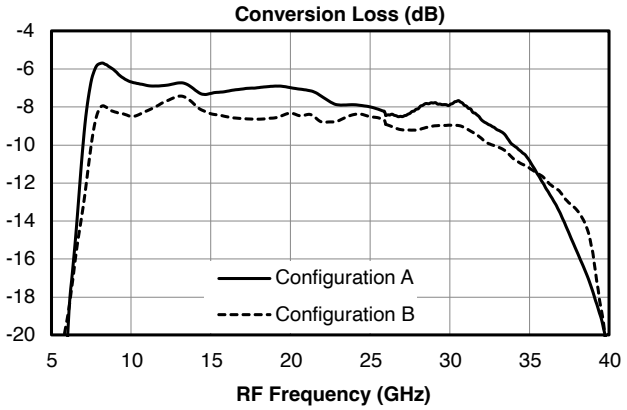
Electrical Specifications

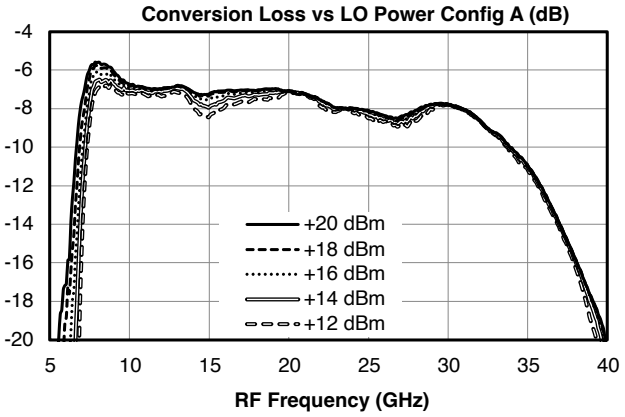
The electrical specifications apply at TA=+25°C in a 50Ω system. Typical data shown is for a down conversion application with a +18 dBm sine wave LO input.

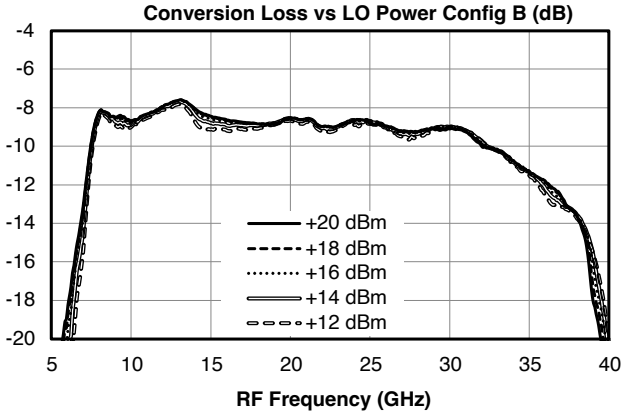
Parameter	Port Configuration	Test Conditions	Min	Typ	Max	Unit
Conversion Loss [1]	A	RF/LO = 8 - 32 GHz I = 0.2 - 12 GHz	-	8.5	-	dB
Conversion Loss [2]	A	RF/LO = 8 - 32 GHz I = DC - 0.2 GHz	-	7.5	10	dB
Input 1 dB Gain Compression Point	A	-	-	10	-	dBm
Input IP3	A	RF/LO = 8 - 32 GHz I = DC - 0.2 GHz	-	23	-	dBm
Isolation, LO to IF	A	IF/LO = 8 - 32 GHz	-	33	-	dB
Isolation, LO to RF	A	RF/LO = 8 - 32 GHz	-	50	-	dB
Isolation, RF to IF	A	RF/IF = 8 - 32 GHz	-	37	-	dB
Noise Figure [3]	A	RF/LO = 8 - 32 GHz I = DC - 0.2 GHz	-	7.5	-	dB
Conversion Loss [4]	B	RF/LO = 8 - 32 GHz I = 0.2 - 12 GHz	-	9.5	-	dB
Conversion Loss [5]	B	RF/LO = 8 - 32 GHz I = DC - 0.2 GHz	-	8.5	11	dB
Input 1 dB Gain Compression Point	B	-	-	13	-	dBm
Input IP3	B	RF/LO = 8 - 32 GHz I = DC - 0.2 GHz	-	23	-	dBm
Isolation, LO to IF	B	IF/LO = 8 - 32 GHz	-	36	-	dB
Isolation, LO to RF	B	RF/LO = 8 - 32 GHz	-	50	-	dB
Isolation, RF to IF	B	RF/IF = 8 - 32 GHz	-	33	-	dB
Noise Figure [6]	B	RF/LO = 8 - 32 GHz I = DC - 0.2 GHz	-	8.5	-	dB
IF Frequency Range	-	-	0	-	12	GHz
LO Frequency Range	-	-	8	-	32	GHz
RF Frequency Range	-	-	8	-	32	GHz

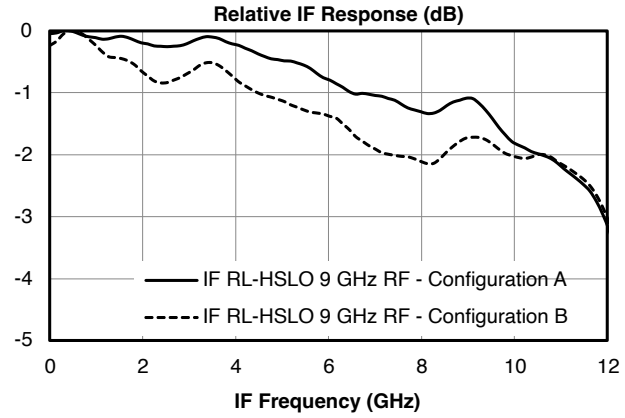
[1][2][4][5] Measured as a down converter to a fixed 91MHz IF.
[3][6] Mixer Noise Figure typically measures within 0.5 dB of conversion loss for IF frequencies greater than 5 MHz.

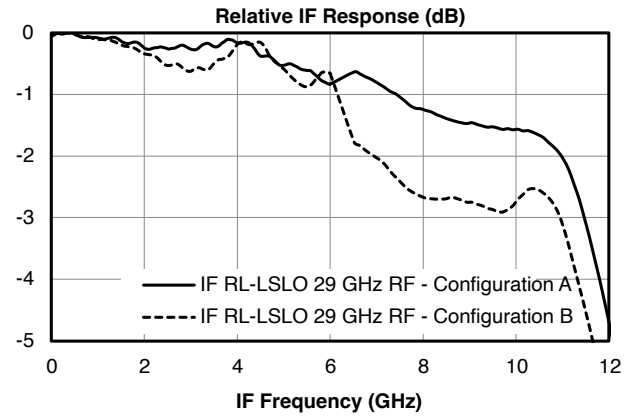
Typical Performance Plots

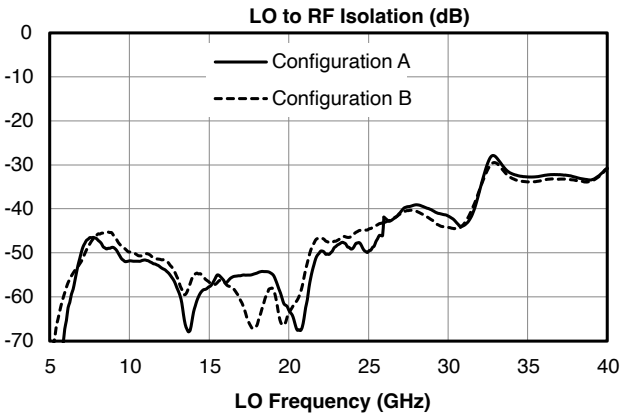


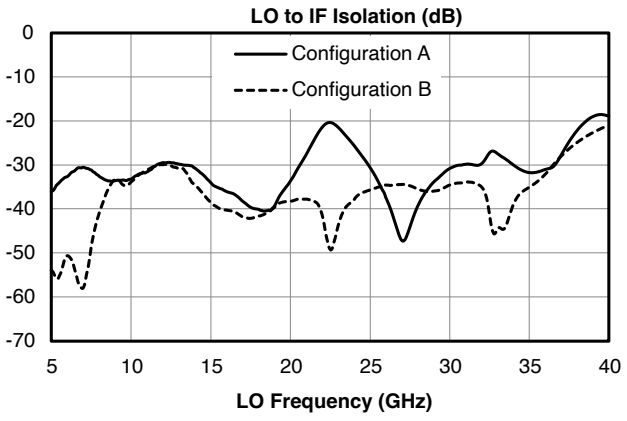


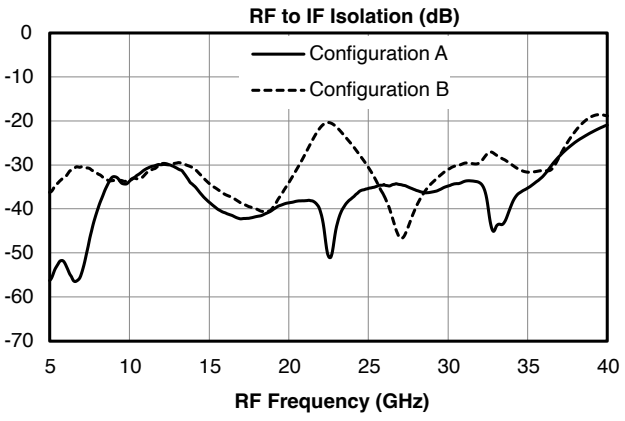


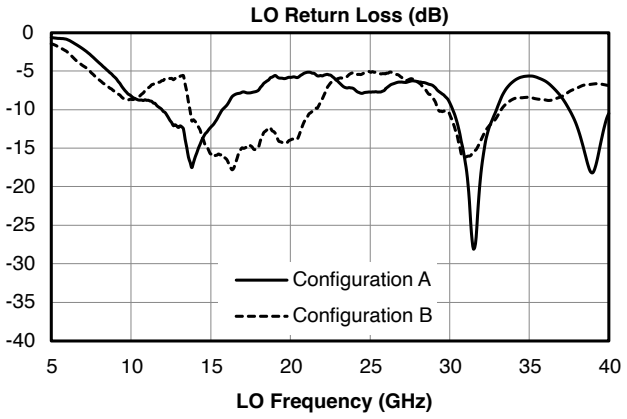


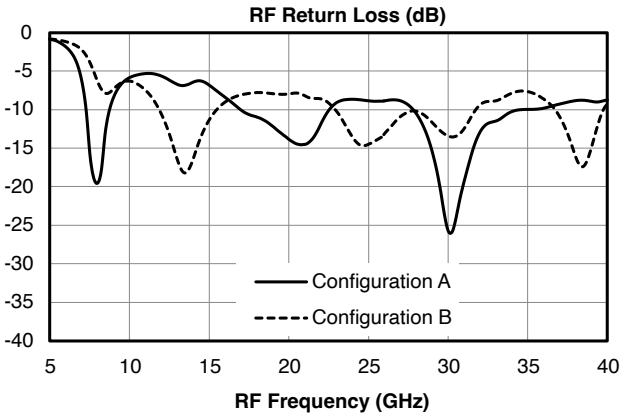


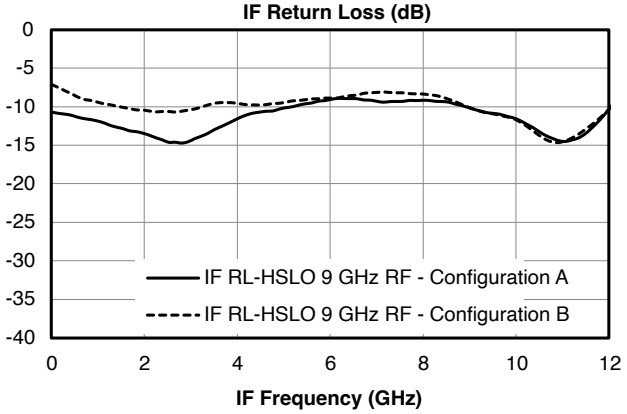


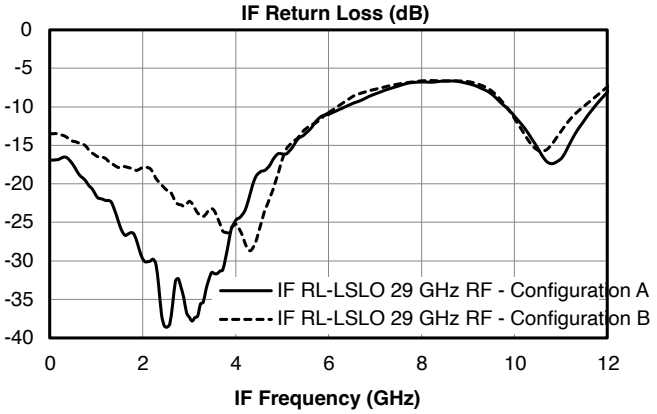




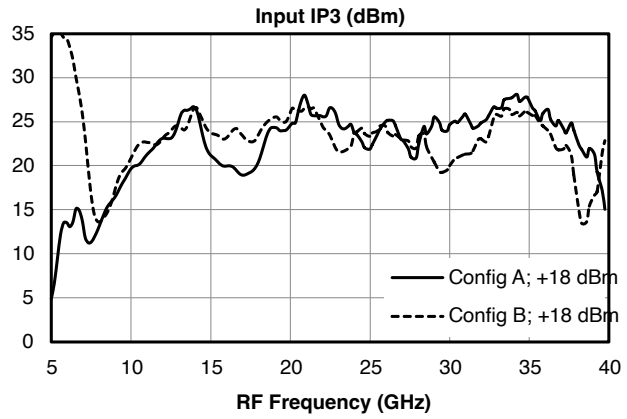


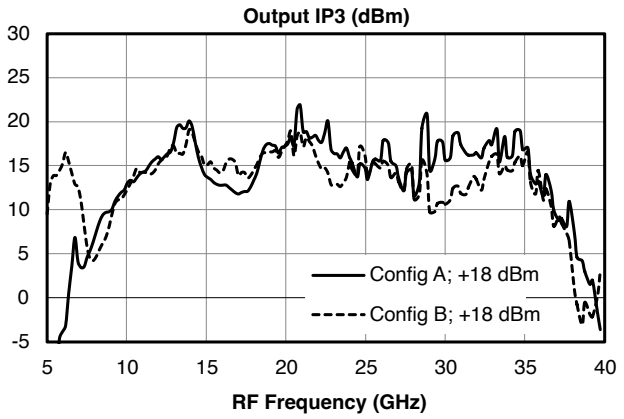


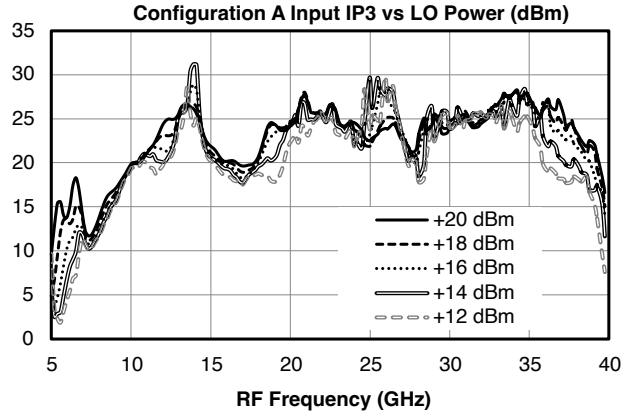


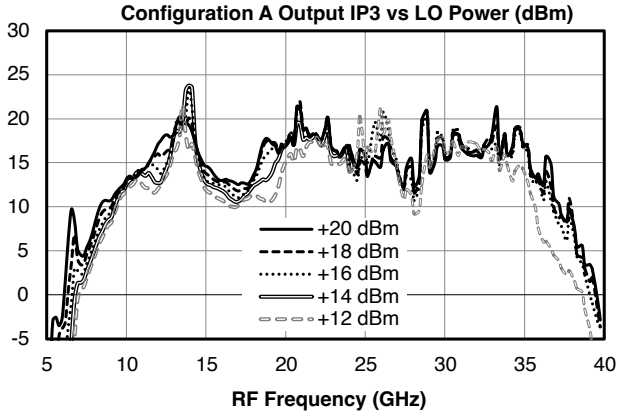


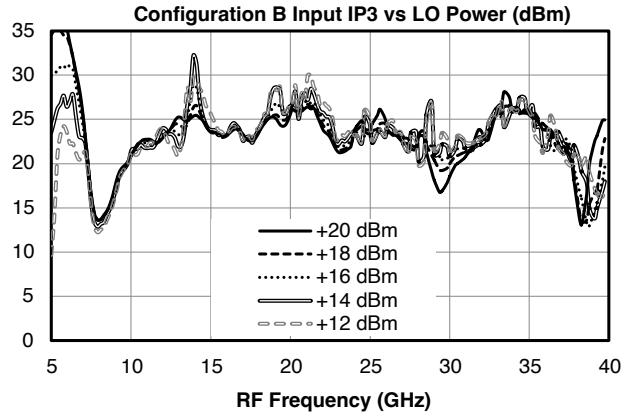
Typical Performance Plots: IP3

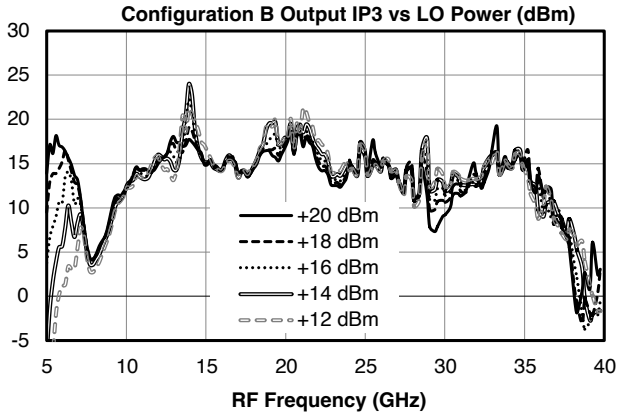




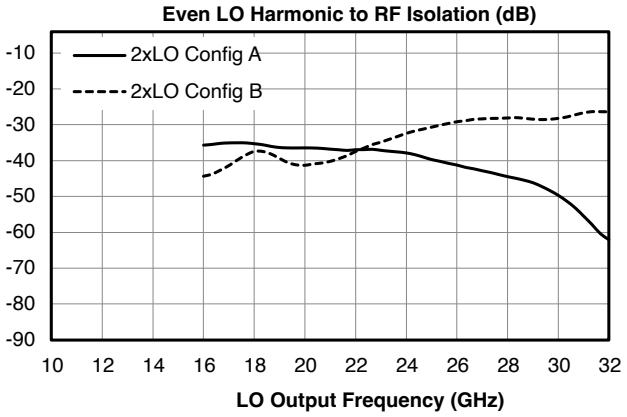


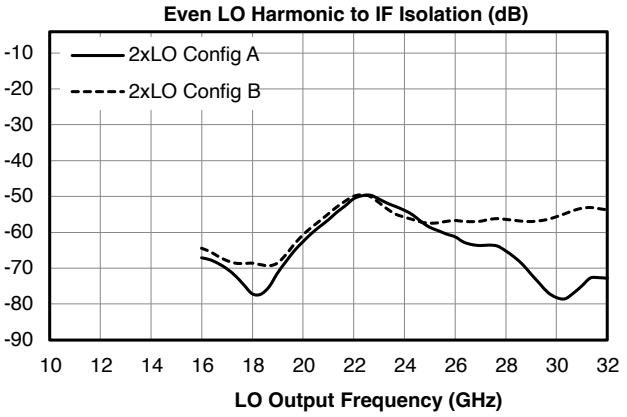


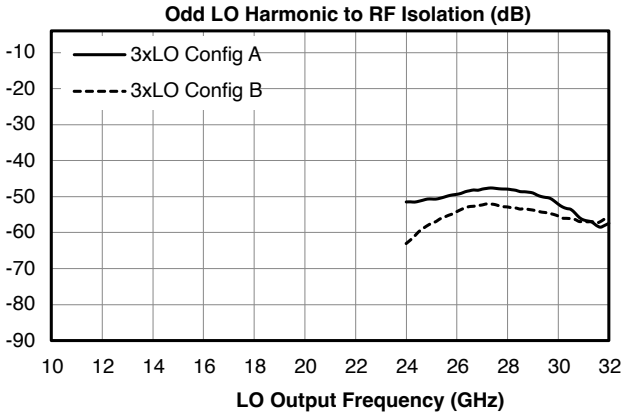


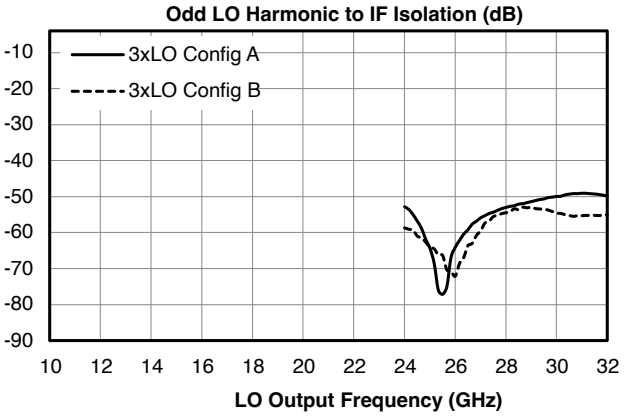


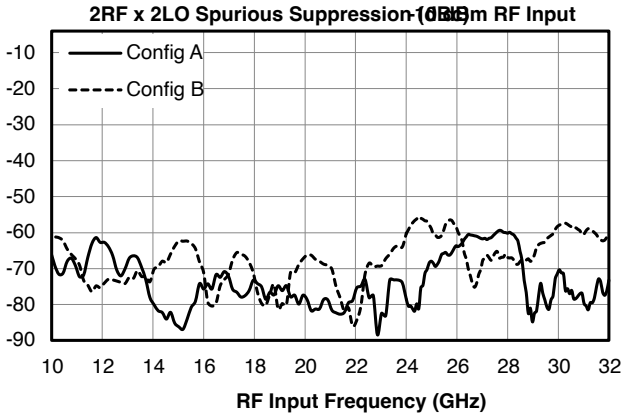
Typical Performance Plots: LO Harmonic Isolation

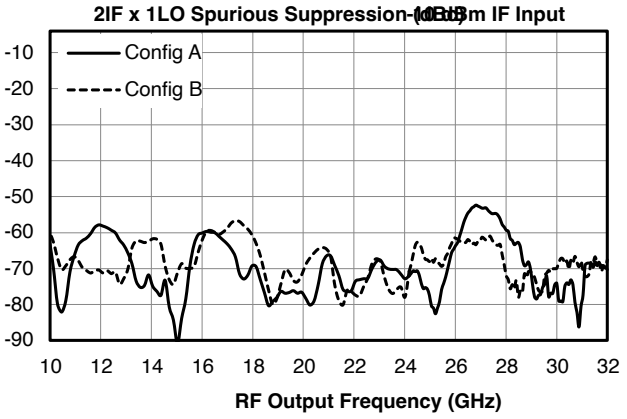












Spur Table

Typical Spurious Performance: Down-Conversion

Typical spurious data is provided by selecting RF and LO frequencies (± m*LO ± n*RF) within the RF/LO bands, to create a spurious output within the IF band. The mixer is swept across the full spurious band and the mean is calculated. The numbers shown in the table below are for a -10 dBm RF input. Spurious suppression is scaled for different RF power levels by (n-1), where "n" is the RF spur order. For example, the 2RF x 2LO spur is71 dBc for a -10 dBm input, so a -20 dBm RF input creates a spur that is (2-1) x (-10 dB) lower, or 81 dBc. Data is shown for the frequency plan inTypical Performance. 0LOx1RF plot is identical to the plot of LO-RF isolation.

Typical Down-conversion spurious suppression (dBc): Config A (B)

-10 dBm RF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xRF	29 (24)	Reference	28 (35)	12 (12)	N/A	N/A
2xRF	87 (85)	64 (60)	71 (67)	67 (56)	67 (68)	63 (58)
3xRF	102 (104)	64 (66)	84 (94)	76 (76)	80 (91)	69 (76)
4xRF	N/A	N/A	117 (116)	116 (109)	113 (113)	116 (109)
5xRF	N/A	N/A	114 (131)	131 (135)	134 (144)	130 (129)

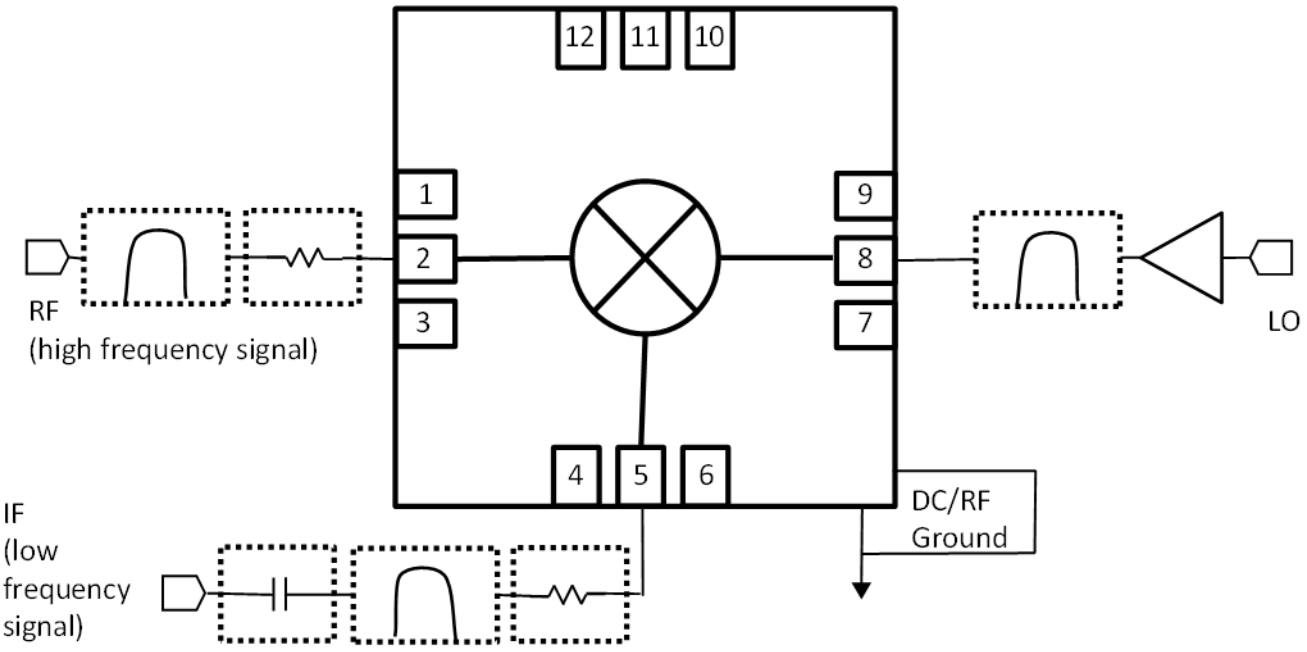
Typical Spurious Performance: Up-Conversion

Typical spurious data is taken by mixing an input within the IF band, with LO frequencies(± m*LO ± n*IF), to create a spurious output within the RF output band. The mixer is swept across the full spurious output band and the mean is calculated. The numbers shown in the table below are for a -10 dBm IF input. Spurious suppression is scaled for different IF input power levels by (n-1), where "n" is the IF spur order. For example, the 2IFx1LO spur is typically69 dBc for a -10 dBm input with a sine-wave LO, so a -20 dBm IF input creates a spur that is (2-1) x (-10 dB) lower, or 79 dBc. Data is shown for the frequency plan inTypical Performance.

Typical Up-conversion spurious suppression (dBc): Config A (B)

-10 dBm IF Input	0xLO	1xLO	2xLO	3xLO	4xLO	5xLO
1xIF	33 (32)	Reference	31 (33)	11 (11)	27 (29)	25 (23)
2xIF	65 (54)	69 (68)	62 (52)	65 (63)	60 (56)	62 (63)
3xIF	83 (90)	73 (76)	77 (84)	75 (69)	77 (79)	61 (58)
4xIF	113 (102)	109 (102)	105 (98)	105 (105)	98 (101)	104 (103)
5xIF	130 (137)	127 (125)	123 (125)	113 (100)	123 (124)	110 (109)

Application Circuit



Configuration B

Application Circuit Description

Ports Operation

IF Port – Used as input on an upconversion, output on downconversion, or LO port in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads. Blocking capacitor is recommended if DC voltage is present on the line.

RF Port – Used as input on a downconversion, output on upconversion, or output in a band shifting application. Signals should be connected by 50 ohm microstrip or coplanar traces to well matched broadband 50 ohm sources and loads.

Filtering and Matching- Filtering is generally desired for spurious and image removal on the output port of the mixer. Reflective filters can cause out of band signals to reflect back into the mixer and cause conversion loss ripple, erroneous spurs, and other undesired behaviors. To eliminate these problems it is recommend that the filters be placed as close to the output port as possible. If undesired behavior is still observed, a diplexer with one port terminated or a 1-3 dB attenuator may reduce this problem.

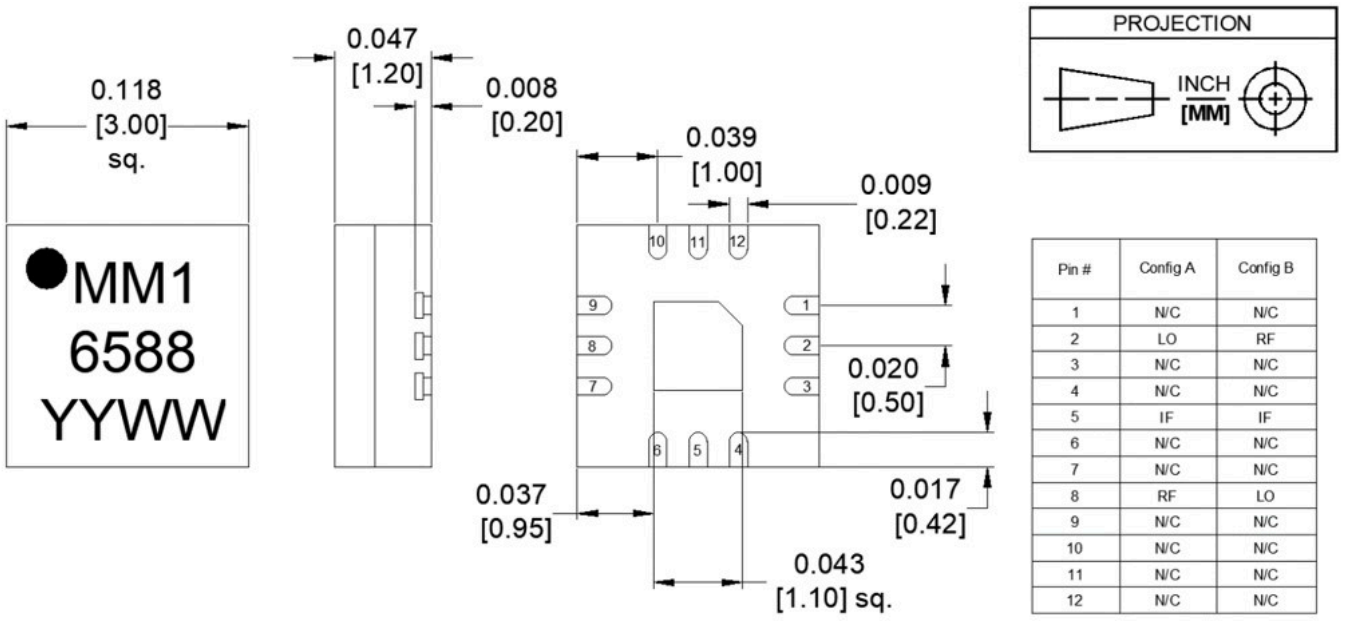
RF Ground – The ground paddle of the QFN should be connected to a low noise RF ground with very low electrical resistance for high frequency operation.

LO Port – The noise floor of the LO input signal should be less than the value of the noise floor plus isolation of the mixer, or a filter is recommended to prevent reduction in dynamic range. An LO amplifier is required if the LO power is below the recommended drive level. It is important to use an amplifier with a broadband 50 ohm match such that it does not reflect spurious signals back into the mixer or other system circuitry.

Mechanical Data

Outline Drawing

Download : Outline 3D Drawing | Outline 3D STP



Pin #	Config A	Config B
1	N/C	N/C
2	LO	RF
3	N/C	N/C
4	N/C	N/C
5	IF	IF
6	N/C	N/C
7	N/C	N/C
8	RF	LO
9	N/C	N/C
10	N/C	N/C
11	N/C	N/C
12	N/C	N/C

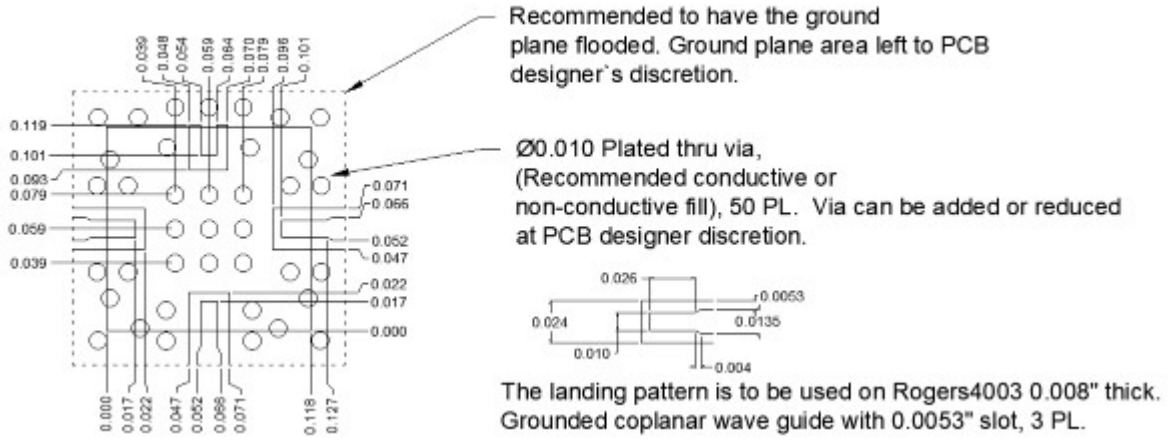
1. Substrate material is LCP.
2. I/O Leads and Ground Paddle plating is (from base to finish):

Ni:	0.5 um MIN
Pd:	0.02 um MIN
Au	0.05 um MAX

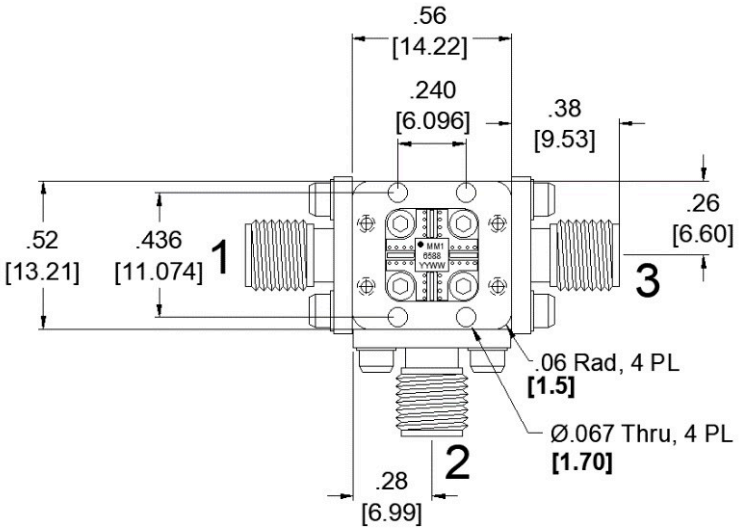
3. All unconnected pins should be connected to PCB RF ground.

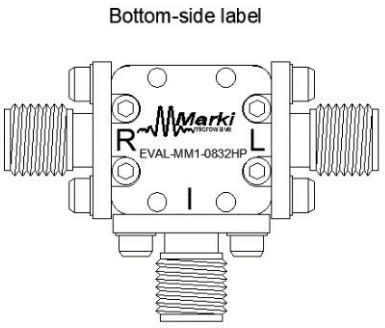
Footprint Image

Download : Footprint Drawing

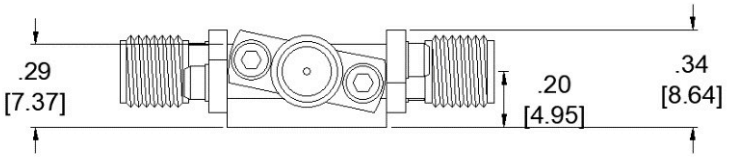


Evaluation Board - Outline Drawing





Function	Configuration A Port Number	Configuration B Port Number	Connector Type
LO	1	3	2.92 mm Female
IF	2	2	SMA Female
RF	3	1	2.92 mm Female
Note: Eval Connectors are not removeable			



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