

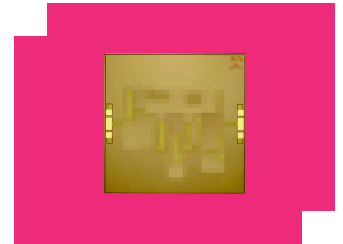
MFBC-00009CH

Passive GaAs MMIC 46.50-63.50 GHz Bandpass Filter

DEVICE OVERVIEW

General Description

The MFBC-00009CH family of passive MMIC bandpass filters are an ideal solution for small form factor, mmWave, high rejection filtering. Passive GaAs MMIC technology allows production of smaller filter constructions that replace larger form factor circuit board constructions. Tight fabrication tolerances allow for less unit-to-unit variation than traditional filter technologies. Low unit-to-unit variation allows for accurate simulations using the provided S2P file taken from measured production units. The MFBC-00009CH is available as a wire bondable die.



[Download s-parameters here](#)

Features

- Excellent Return Loss
- High Stop Band Suppression
- Wide Stop Band with Fast Roll-Off

Applications

N/A

Functional Block Diagram



Part Ordering Options

Part Number	Description	Package	Green Status	Product Lifecycle	Export Classification
MFBC-00009CH	Passive GaAs MMIC 46.50-63.50 GHz Bandpass Filter	CH	RoHS REACH	Released	EAR99

MFBC-00009CH

Passive GaAs MMIC 46.50-63.50 GHz Bandpass Filter

Table Of Contents

- **Device Overview**
 - General Description
 - Features
 - Applications
 - Functional Block Diagram
- **Port Configuration and Functions**
 - Port Diagram
 - Port Functions
- **Revision History**
- **Specifications**
 - Absolute Maximum Ratings
 - Package Information
 - Electrical Specifications
 - Typical Performance Plots
- **Die Mounting Recommendations**
 - Mounting and Bounding Recommendations
- **Operation**
 - Application Information
 - Application Circuit
- **Mechanical Data**
 - Outline Drawing

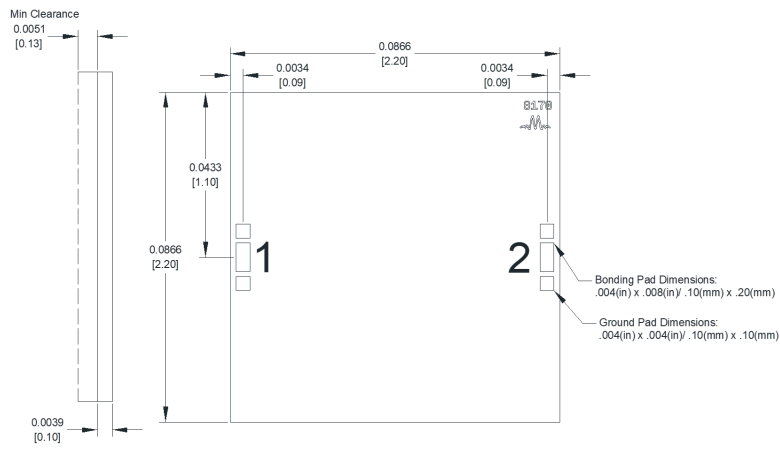
Revision History

Revision Code	Revision Date	Comment
-	2023-03-28	Datasheet Initial Release
A	2024-01-05	Updated Production Test Criteria
B	2025-11-06	MSL information added

Port Configuration and Functions

Port Diagram

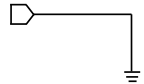
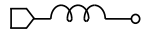
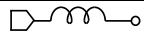
The MMIC bandpass filters are symmetrical allowing Port 1 or Port 2 to be used as the input.



Notes:

1. CH substrate is .004 Thick GaAs.
2. I/O trace is 5 microns Au. Ground is 4 microns Au.
3. Tolerance for X, Y dimensions is ± 0.002 in.
Tolerance for Z dimension is ± 0.0005 in.
Tolerance for pad location is ± 0.0001 in.

Port Functions

Port	Function	Description	DC Equivalent Circuit
Pad	Ground	CH package ground path is provided through the substrate and ground bond pads.	
Port 1	Input/Output	Port 1 is DC open to ground for the CH package.	
Port 2	Input/Output	Port 2 is DC open to ground for the CH package.	

MFBC-00009CH

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Specifications

Absolute Maximum Ratings

The Absolute Maximum Ratings indicate limits beyond which damage may occur to the device. If these limits are exceeded or met simultaneously, the device may be inoperable or have a reduced lifetime.

Parameter	Maximum Rating	Unit
Maximum Operating Temperature	100	°C
Maximum Storage Temperature	125	°C
Minimum Operating Temperature	-55	°C
Minimum Storage Temperature	-65	°C

Package Information

Parameter	Details	Rating
Dimensions	-	2.20 x 2.20 mm
Moisture Sensitivity Level	-	MSL 1

Electrical Specifications

The electrical specifications apply at TA=+25°C in a 50Ω system. Min and Max limits are guaranteed at TA=+25°C. All bare die are 100% visually inspected and RF performance is guaranteed by sample testing.

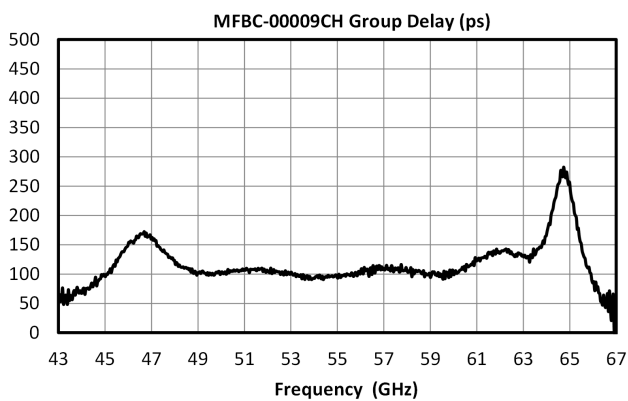
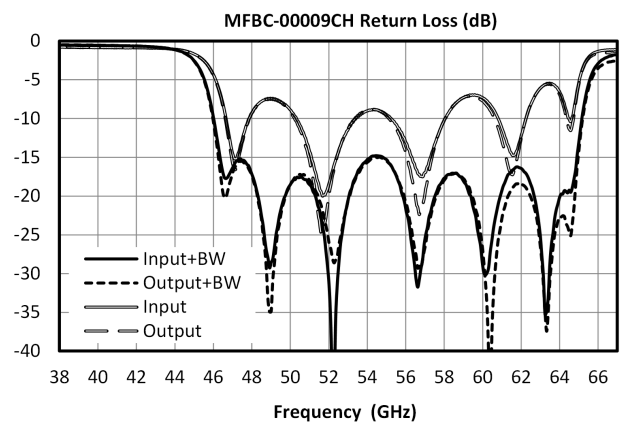
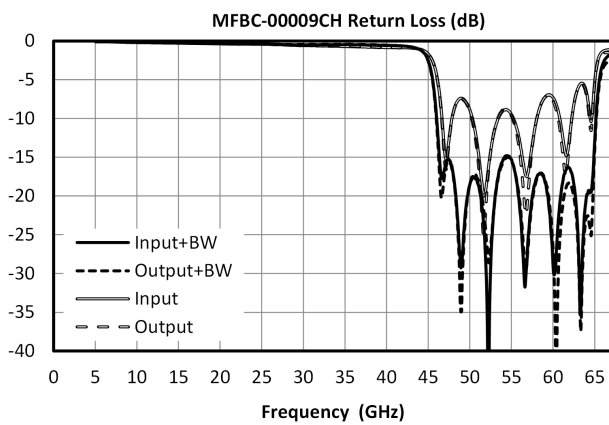
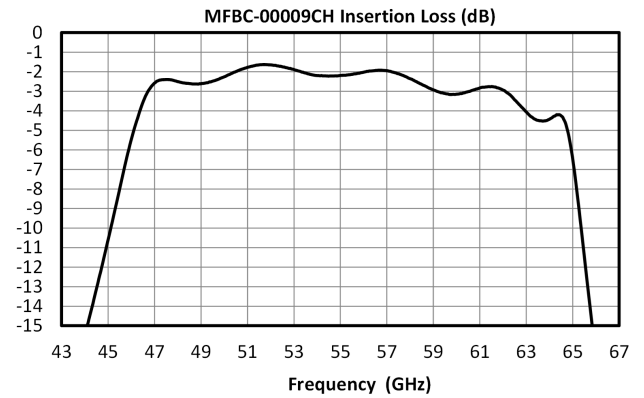
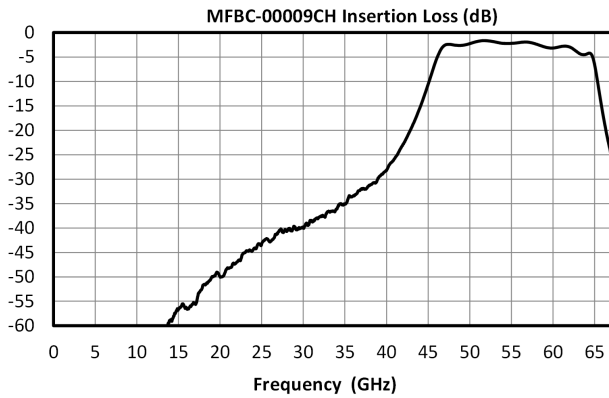
Parameter	Test Conditions	Minimum Frequency (GHz)	Maximum Frequency (GHz)	Min	Typ	Max	Unit
1 dBc Passband ¹	Configuration A, 25°C	46.9	62.44	-	-	-	GHz
30 dBc Rejection Point ²	Configuration A, 25°C	40.4	-	-	-	-	GHz
3 dBc Passband ³	Configuration A, 25°C	46.1	64.72	-	-	-	GHz
Center Freq ⁴	Configuration A, 25°C	-	-	-	54.12	-	GHz
Group Delay ⁵	Configuration A, 25°C	-	-	-	238	-	ps
Impedance ⁶	Configuration A, 25°C	-	-	-	50	-	Ω
Insertion Loss @ f _c ⁷	Configuration A, 25°C	-	-	-	2.3	-	dB
Passband Return Loss ⁸	Configuration A, 25°C	-	-	-	20	-	dB
Stopband Suppression	-	0	29.5	40	67	-	dB

[1][2][3][4][5][6][7][8] Includes Bondwires

MFBC-00009CH

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Typical Performance Plots



MFBC-00009CH

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Application Information

To show performance improvement with bond wires (BW), inductance was added to these measurements in simulation per table below.

MFBC-00009CH

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Application Circuit

Filter Name	Number of Bondwires	Bondwire Length (um)	Bondwire Diameter (mil)	Bondwire Inductance (pH)
MFBC-00008CH	2	400	1	128
MFBC-00009CH	2	400	1	132
MFBC-00017CH	2	275	1	74
MFBC-00018CH	3	275	1	30

Die Mounting Recommendations

Mounting and Bonding Recommendations

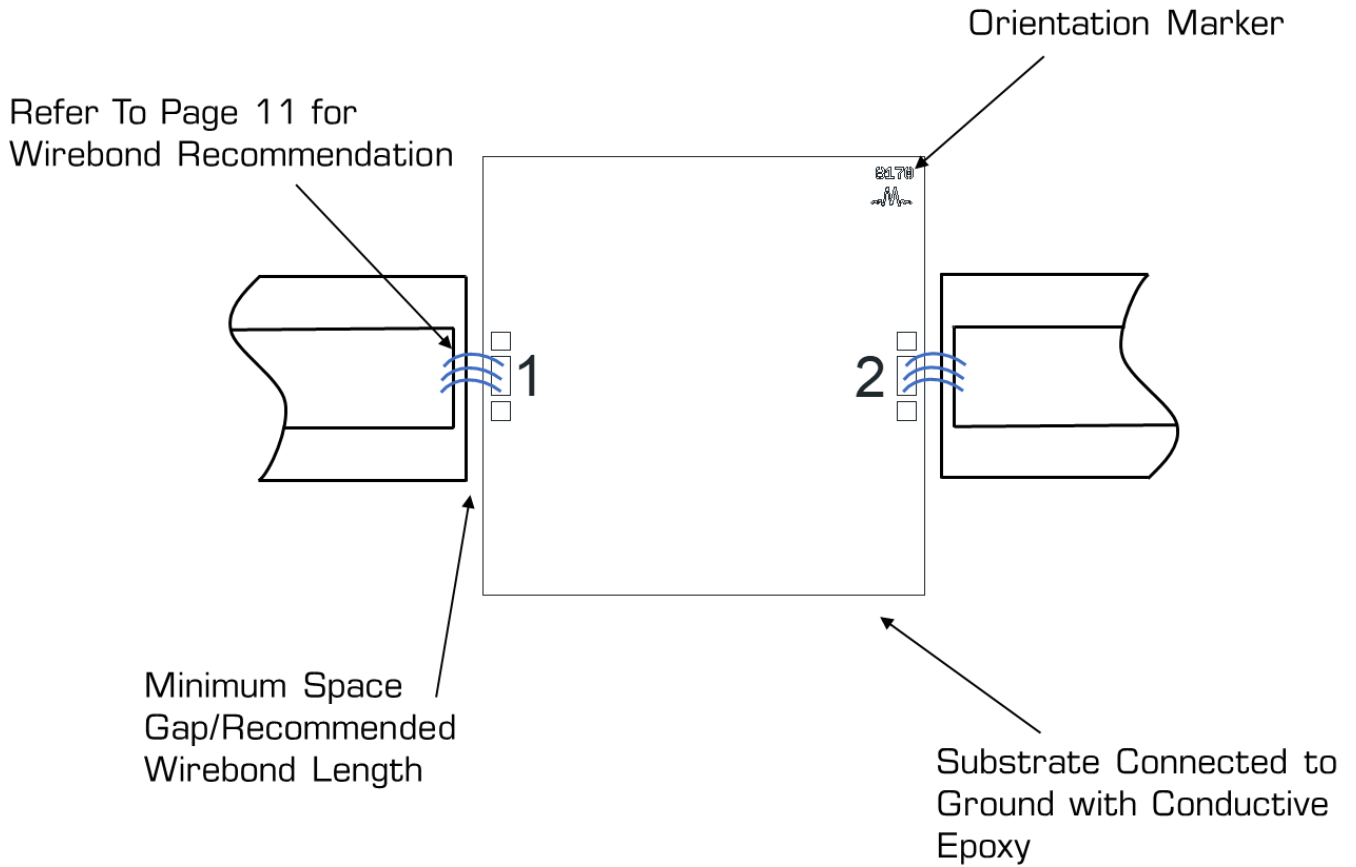
Marki MMICs should be attached directly to a ground plane with conductive epoxy. The ground plane electrical impedance should be as low as practically possible. This will prevent resonances and permit the best possible electrical performance. Datasheet performance is only guaranteed in an environment with a low electrical impedance ground.

Mounting- To epoxy the chip, apply a minimum amount of conductive epoxy to the mounting surface so that a thin epoxy fillet is observed around the perimeter of the chip. Cure epoxy according to manufacturer instructions.

Wire Bonding - Ball or wedge bond with 0.025 mm (1 mil) diameter pure gold wire. Thermosonic wirebonding with a nominal stage temperature of 150 °C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Use the minimum level of ultrasonic energy to achieve reliable wirebonds. Wirebonds should be started on the chip and terminated on the package or substrate. Bond wire inductance will improve return loss. Bondwire inductance in the range of 30pH to 200pH will improve performance.

Circuit Considerations – 50 Ω transmission lines should be used for all high frequency connections in and out of the chip. Wirebonds should be kept as short as possible, with multiple wirebonds recommended for higher frequency connections to reduce parasitic inductance. Refer to table on page 11 for wirebond recommendation. In circumstances where the chip is more than .001” thinner than the substrate, a heat spreading spacer tab is optional to further reduce bondwire length and parasitic inductance.

Bonding Diagram



Handling Precautions

General Handling

Chips should be handled with care using tweezers or a vacuum collet. Users should take precautions to protect chips from direct human contact that can deposit contaminants, like perspiration and skin oils on any of the chip's surfaces.

Static Sensitivity

GaAs MMIC devices are sensitive to ESD and should be handled, assembled, tested, and transported only in static protected environments.

Cleaning and Storage: Do not attempt to clean the chip with a liquid cleaning system or expose the bare chips to liquid. Once the ESD sensitive bags the chips are stored in are opened, chips should be stored in a dry nitrogen atmosphere.

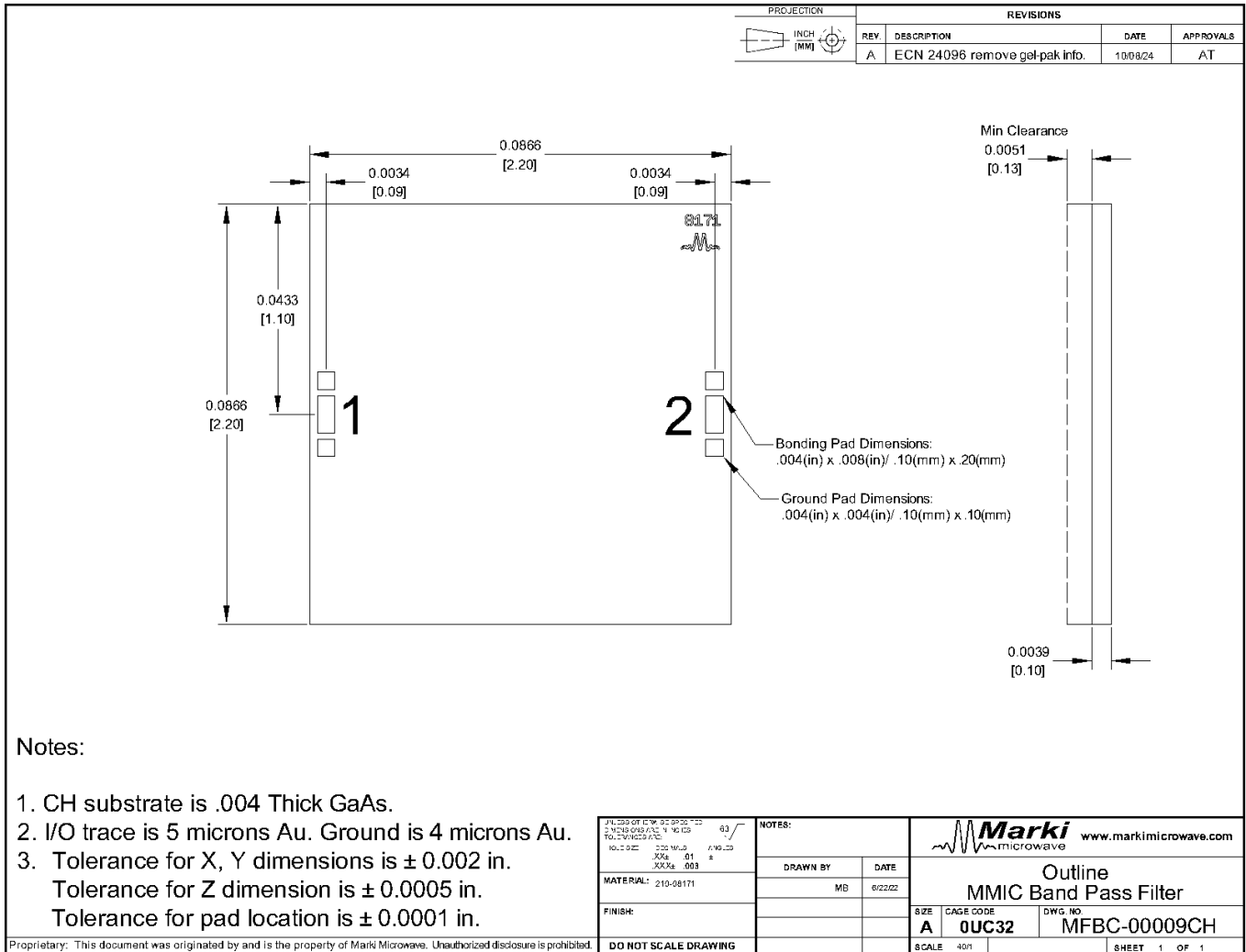
MFBC-00009CH

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Mechanical Data

Outline Drawing

Download : [Outline 2D Drawing](#)



MFBC-00009CH

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